

# GSMDC2116M

## 20V N+P Dual Channel MOSFETs

### Product Description

These N+P dual Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

These devices are well suited for high efficiency fast switching applications.

### Features

- N-Channel  
20V, 5A,  $R_{DS(ON)}=40m\Omega@V_{GS}=4.5V$
- P-Channel  
-20V, -4.7A,  $R_{DS(ON)}=100m\Omega@V_{GS}=-4.5V$
- Fast switching
- Suit for -1.8V/1.8V Gate Drive Applications
- Green Device Available
- DFN2X3-8L package design

### Applications

- Notebook
- Load Switch
- Networking
- Hand-Held Instruments

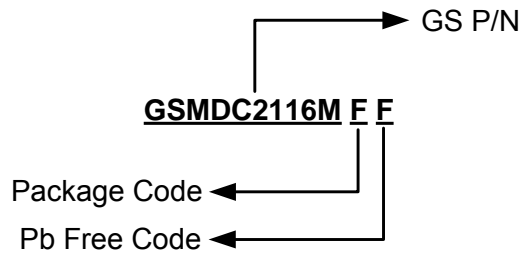
### Packages & Pin Assignments

**GSMDC2116MFF (DFN2X3-8L)**

Bottom Views

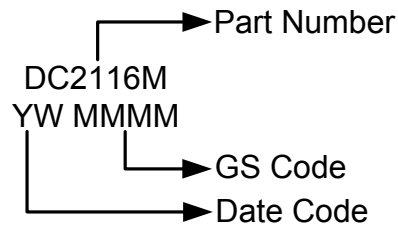
Pin	Description
1	Source 1
2	Gate 1
3	Source 2
4	Gate 2
5	Drain 2
6	Drain 2
7	Drain 1
8	Drain 1

## Ordering Information



Part Number	Package	Quantity
GSMDC2116MFF	DFN2X3-8L	3000pcs

## Marking Information



## Absolute Maximum Ratings

T<sub>c</sub>=25°C Unless otherwise noted

Symbol	Parameter	Typical		Unit	
		N-Channel	P-Channel		
V <sub>DS</sub>	Drain-Source Voltage	20	-20	V	
V <sub>GS</sub>	Gate –Source Voltage	±10	±10	V	
I <sub>D</sub>	Continuous Drain Current (T <sub>J</sub> =150°C)	T <sub>c</sub> =25°C	5	-4.7	A
		T <sub>c</sub> =100°C	4.1	-3.9	
I <sub>DM</sub>	Pulsed Drain Current	15.2	-10	A	
P <sub>D</sub>	Power Dissipation	T <sub>c</sub> =25°C	1.56	W	
		Derate above 25°C	0.0125	W/°C	
T <sub>J</sub>	Operating Junction Temperature Range	-55 to +150		°C	
T <sub>STG</sub>	Storage Temperature Range	-55 to +150		°C	
R <sub>θJA</sub>	Thermal Resistance-Junction to Ambient	80		°C/W	

## Electrical Characteristics (N-Channel)

$T_J=25^{\circ}\text{C}$  Unless otherwise noted

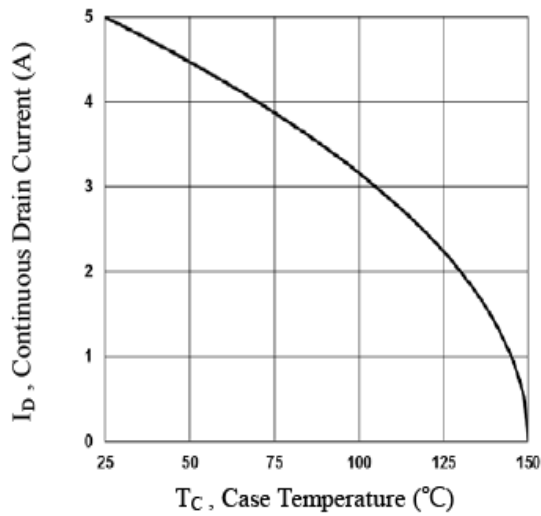
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static</b>						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	20			V
$\Delta BV_{DSS}/\Delta T_J$	$BV_{DSS}$ Temperature Coefficient	Reference to $25^{\circ}\text{C}$ , $I_D=1\text{mA}$		0.02		$\text{V}/^{\circ}\text{C}$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	0.3	0.6	1.0	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient			-2		$\text{mV}/^{\circ}\text{C}$
$I_{GSS}$	Gate Leakage Current	$V_{DS}=0V, V_{GS}=\pm 10V$			$\pm 100$	nA
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=20V, V_{GS}=0V$			1	$\mu\text{A}$
		$V_{DS}=16V, V_{GS}=0V$ , $T_J=125^{\circ}\text{C}$			10	
$I_S$	Continuous Source Current	$V_G=V_D=0V$ , Force Current			5	A
$I_{SM}$	Pulsed Source Current				10	
$R_{DS(on)}$	Drain-Source On-Resistance	$V_{GS}=4.5V, I_D=3A$		30	40	$\text{m}\Omega$
		$V_{GS}=2.5V, I_D=2A$		42	55	
		$V_{GS}=1.8V, I_D=1.5A$		55	70	
$g_{FS}$	Forward Transconductance	$V_{DS}=10V, I_D=2A$		4.4		S
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0V, I_S=1A$			1	V
<b>Dynamic</b>						
$Q_g$	Total Gate Charge	$V_{DS}=10V, V_{GS}=4.5V$ , $I_D=3A$		5.8	10	nC
$Q_{gs}$	Gate-Source Charge			0.6	1.5	
$Q_{gd}$	Gate-Drain Charge			1.5	3	
$C_{iss}$	Input Capacitance	$V_{DS}=15V, V_{GS}=0V$ , $f=1\text{MHz}$		315	600	$\text{pF}$
$C_{oss}$	Output Capacitance			50	80	
$C_{rss}$	Reverse Transfer Capacitance			40	60	
$t_{d(on)}$	Turn-On Time	$V_{DD}=10V, I_D=1A$ , $V_{GS}=4.5V, R_G=25\Omega$		2.9	6	ns
$t_r$				8.4	16	
$t_{d(off)}$	Turn-Off Time			19.2	38	
$t_f$				5.6	12	

## Electrical Characteristics (P-Channel)

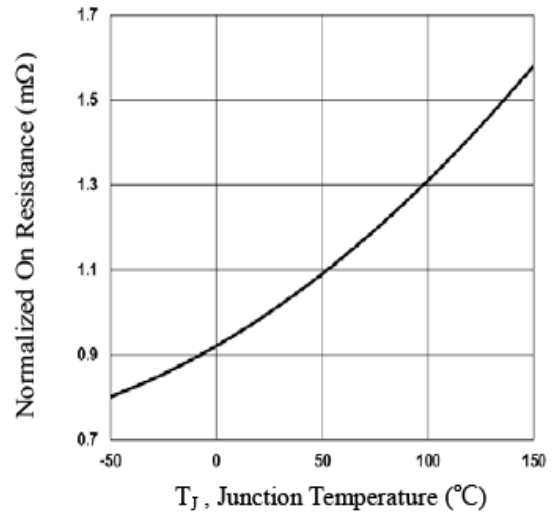
$T_J=25^{\circ}\text{C}$  Unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static</b>						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-20			V
$\Delta BV_{DSS}/\Delta T_J$	$BV_{DSS}$ Temperature Coefficient	Reference to $25^{\circ}\text{C}$ , $I_D=-1\text{mA}$		-0.01		$\text{V}/^{\circ}\text{C}$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	-0.3	-0.6	-1.0	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient			3		$\text{mV}/^{\circ}\text{C}$
$I_{GSS}$	Gate Leakage Current	$V_{DS}=0V, V_{GS}=\pm 10V$			$\pm 100$	nA
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=-20V, V_{GS}=0V$			-1	$\mu A$
		$V_{DS}=-16V, V_{GS}=0V$ , $T_J=125^{\circ}\text{C}$			-10	
$I_S$	Continuous Source Current	$V_G=V_D=0V$ , Force Current			-4.7	A
$I_{SM}$	Pulsed Source Current				-9.4	
$R_{DS(on)}$	Drain-Source On-Resistance	$V_{GS}=-4.5V, I_D=-3A$		82	100	m $\Omega$
		$V_{GS}=-2.5V, I_D=-2A$		125	140	
		$V_{GS}=-1.8V, I_D=-1A$		197	230	
$g_{FS}$	Forward Transconductance	$V_{DS}=-10V, I_D=-1A$		2.2		S
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0V, I_S=-1A$			-1	V
<b>Dynamic</b>						
$Q_g$	Total Gate Charge	$V_{DS}=-10V, V_{GS}=-4.5V$ , $I_D=-2A$		4.8	10	nC
$Q_{gs}$	Gate-Source Charge			0.5	1	
$Q_{gd}$	Gate-Drain Charge			1.9	4	
$C_{iss}$	Input Capacitance	$V_{DS}=-15V, V_{GS}=0V$ , $f=1\text{MHz}$		350	510	pF
$C_{oss}$	Output Capacitance			65	95	
$C_{rss}$	Reverse Transfer Capacitance			50	75	
$t_{d(on)}$	Turn-On Time	$V_{DD}=-10V, I_D=-1A$ , $V_{GS}=-4.5V, R_G=25\Omega$		3.5	7	ns
$t_r$				12.6	24	
$t_{d(off)}$	Turn-Off Time			32.6	62	
$t_f$				8.4	16	

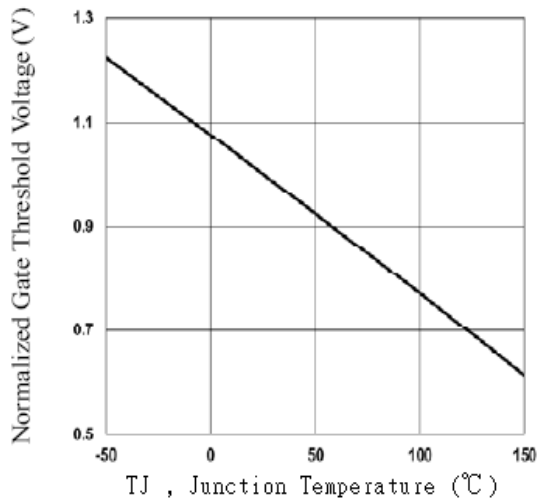
## Typical Performance Characteristics (N-Channel)



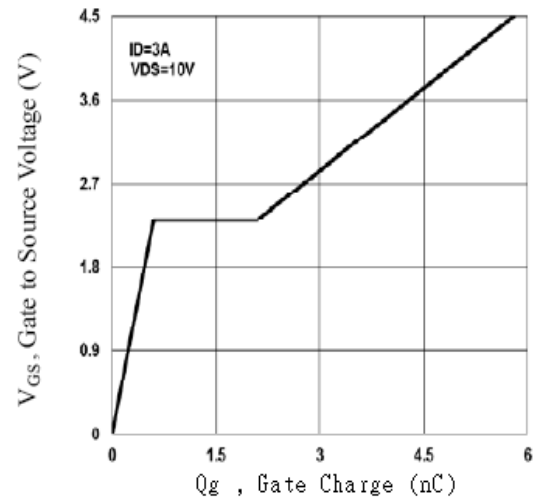
**Fig.1 Continuous Drain Current vs.  $T_c$**



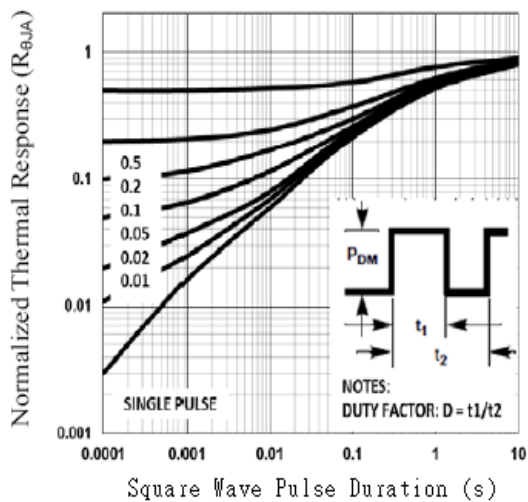
**Fig.2 Normalized RDSON vs.  $T_j$**



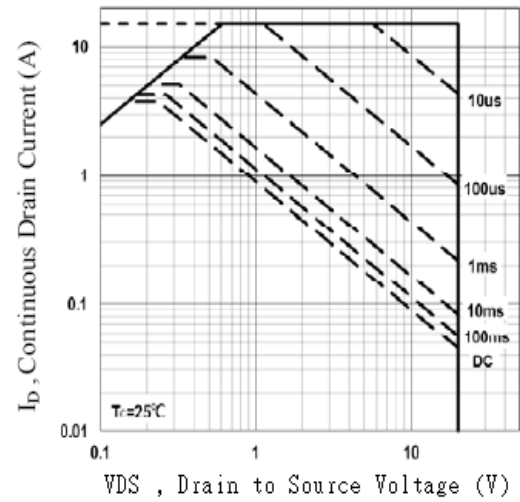
**Fig.3 Normalized  $V_{th}$  vs.  $T_j$**



**Fig.4 Gate Charge Waveform**



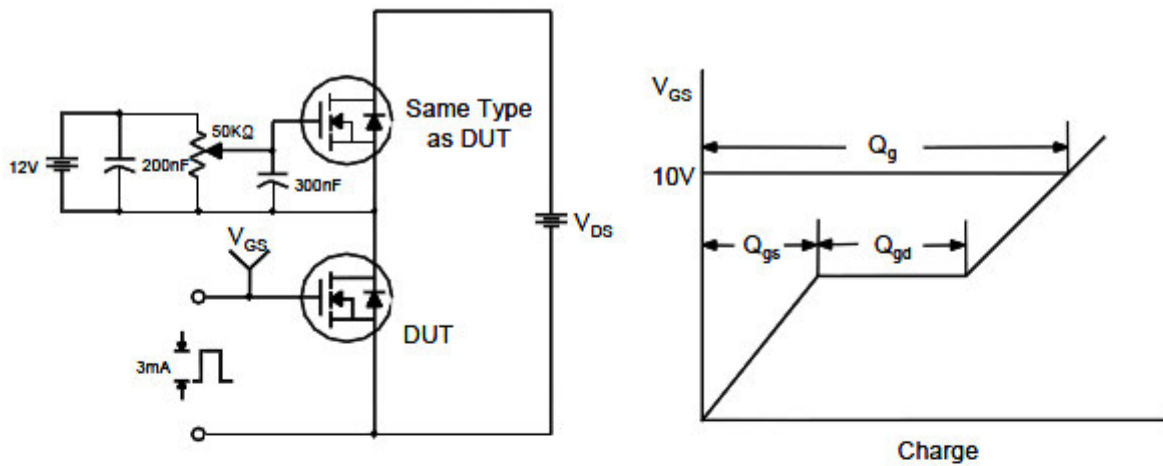
**Fig.5 Normalized Transient Impedance**



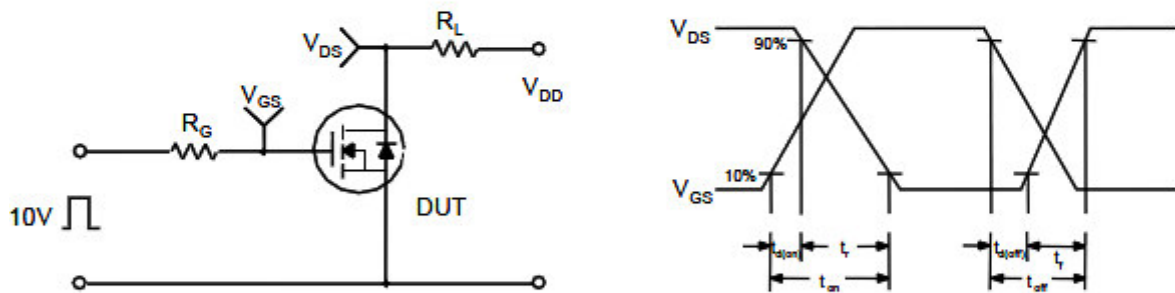
**Fig.6 Maximum Safe Operation Area**

## Typical Performance Characteristics (N-Channel)

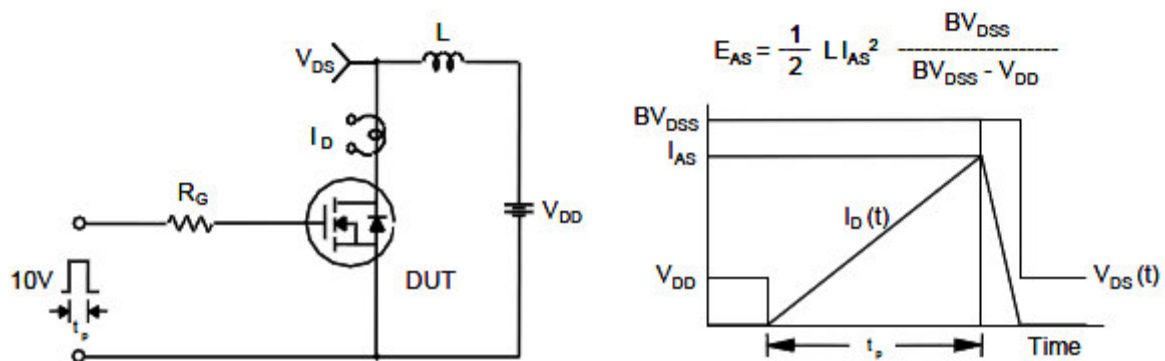
Gate Charge Test Circuit & Waveform



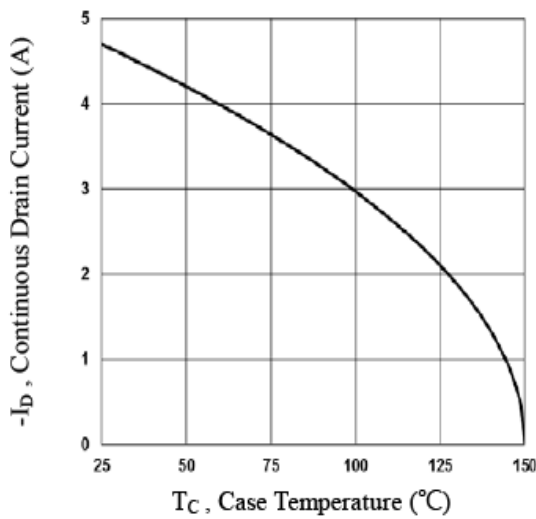
Resistive Switching Test Circuit & Waveforms



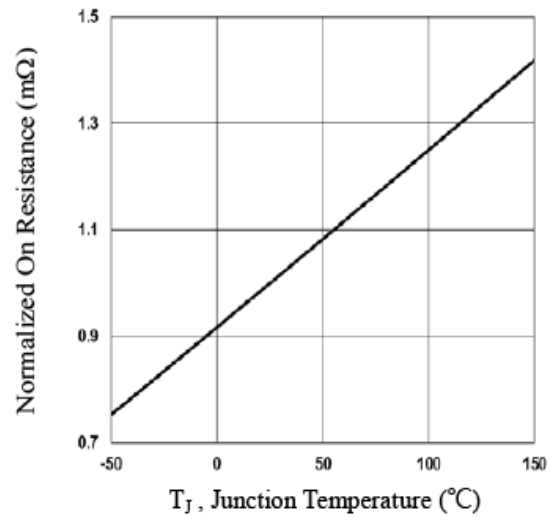
Unclamped Inductive Switching Test Circuit & Waveforms



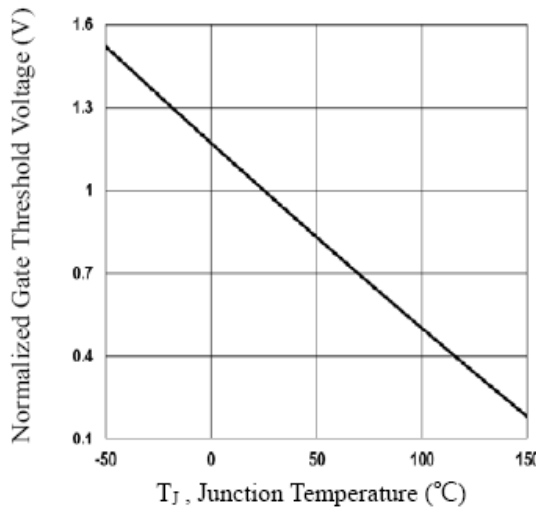
## Typical Performance Characteristics (P-Channel)



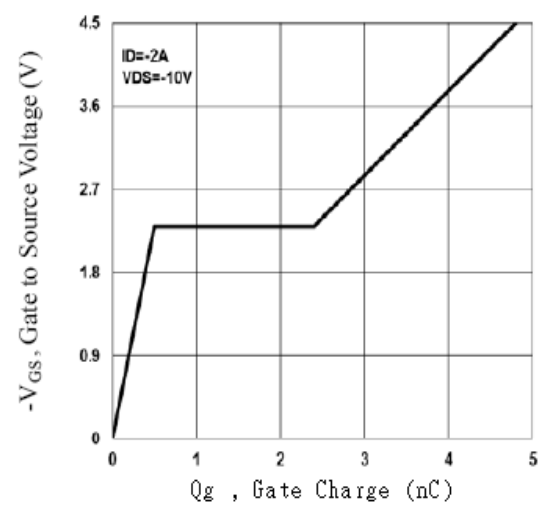
**Fig.1 Continuous Drain Current vs.  $T_c$**



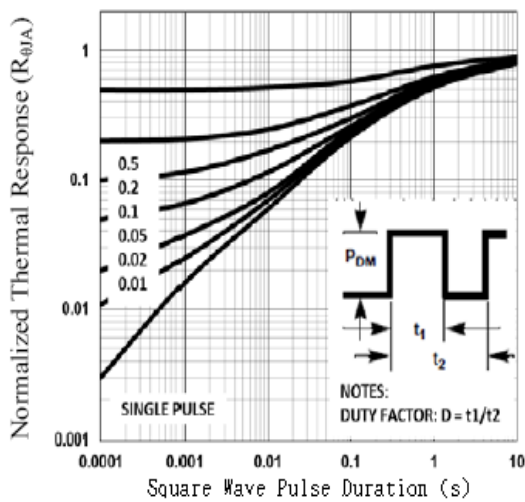
**Fig.2 Normalized RDSON vs.  $T_j$**



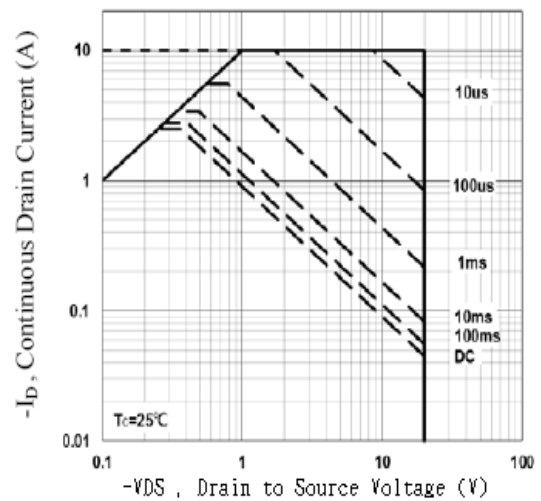
**Fig.9 Normalized  $V_{th}$  vs.  $T_j$**



**Fig.10 Gate Charge Waveform**



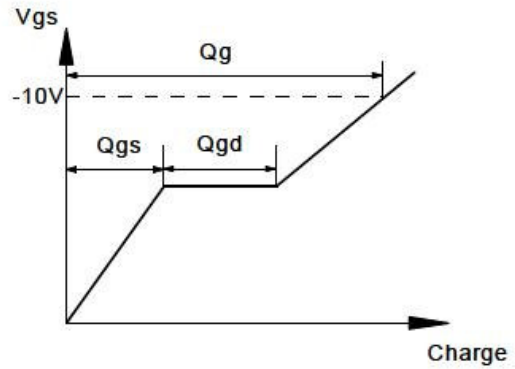
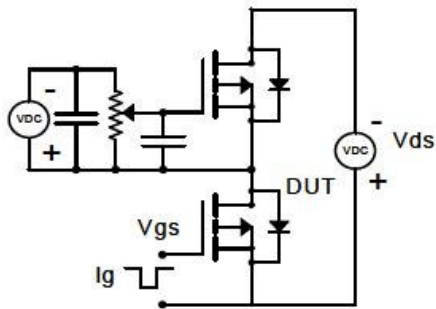
**Fig.11 Normalized Transient Impedance**



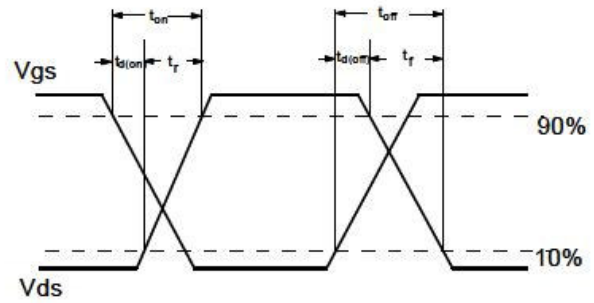
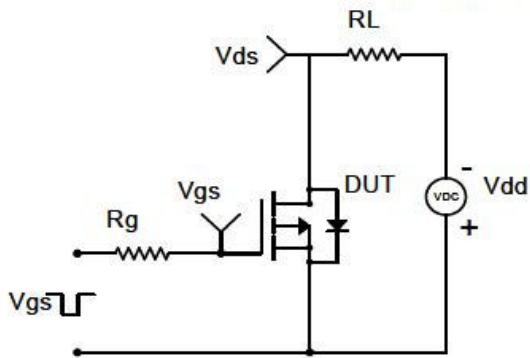
**Fig.12 Maximum Safe Operation Area**

## Typical Performance Characteristics (P-Channel)

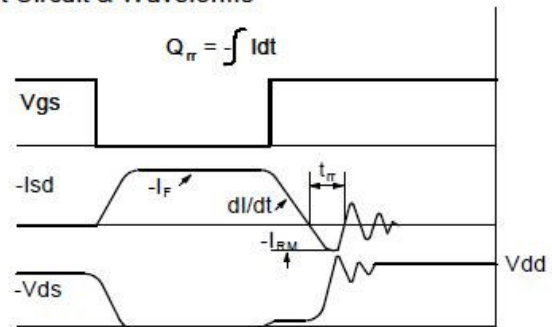
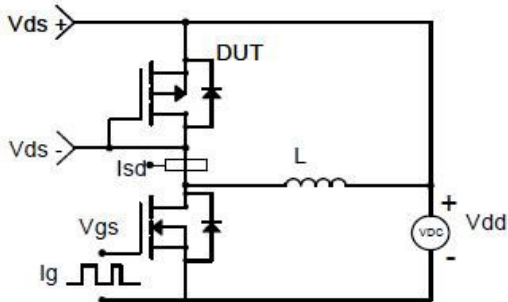
### Gate Charge Test Circuit & Waveform



### Resistive Switching Test Circuit & Waveforms



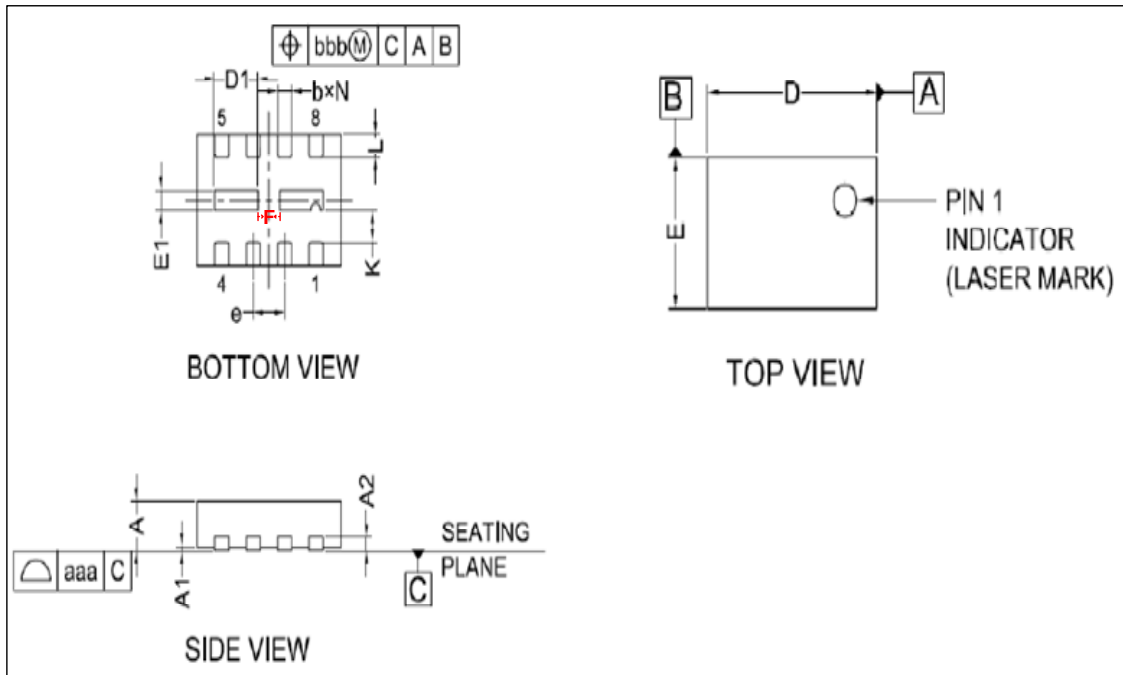
### Diode Recovery Test Circuit & Waveforms





## Package Dimension

### DFN2X3-8L







#### Dimensions




Symbol	Millimeters			Inches		
	Min	Typ	Max	Min	Typ	Max
A	0.70	0.75	0.80	0.027	0.029	0.032
A1	0.00	0.02	0.05	0.000	0.001	0.002
A2	0.203			0.008		
b	0.25	0.30	0.35	0.009	0.012	0.014
D	2.95	3.00	3.05	0.116	0.118	0.120
D1	0.80	0.90	1.00	0.031	0.035	0.039
E	1.95	2.00	2.05	0.076	0.078	0.081
E1	0.20	0.30	0.40	0.007	0.012	0.016
F	0.25	0.45	0.65	0.009	0.017	0.026
e	0.65 (BSC)			0.026 (BSC)		
L	0.30	0.35	0.40	0.012	0.014	0.016
K	0.20 (MIN)			0.007 (MIN)		
N	8			8		
aaa	0.08			0.003		
bbb	0.10			0.004		



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