

# GSMDC3812V

## 30V Dual N-Channel MOSFETs

### Product Description

These Dual N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

These devices are well suited for high efficiency fast switching applications.


### Features

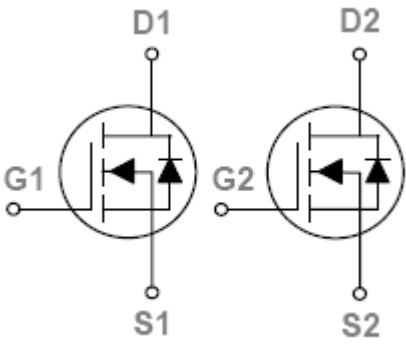
- 30V, 20A,  $R_{DS(ON)}=20m\Omega@V_{GS}=10V$
- Improved dv/dt capability
- Fast switching
- 100% EAS guaranteed
- Green Device Available
- DFN3X3-8L package design

### Applications

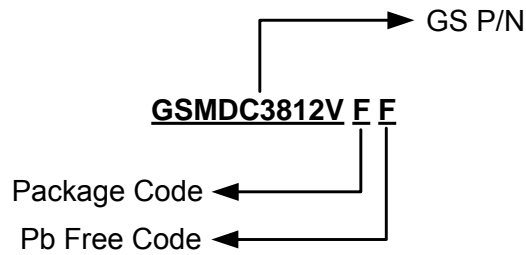
- MB / VGA / Vcore
- POL Applications
- SMPS 2nd SR
- Li-Battery Protection

### Packages & Pin Assignments

GSMDC3812VFF (DFN3X3-8L)	
 <p>Top View</p>	
Pin	Description
1	Source 1
2	Gate 1
3	Source 2
4	Gate 2
5	Drain 2
6	Drain 2
7	Drain 1
8	Drain 1

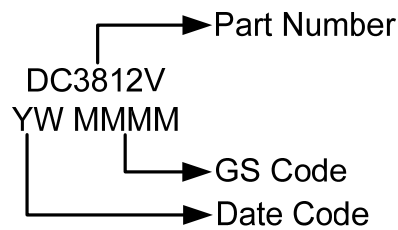


## Ordering Information



Part Number	Package	Quantity Reel
GSMDC3812VFF	DFN3X3-8L	3000 PCS

## Marking Information



## Absolute Maximum Ratings

$T_C=25^{\circ}\text{C}$  Unless otherwise noted

Symbol	Parameter	Typical	Unit
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current	$T_C=25^{\circ}\text{C}$	20
		$T_C=100^{\circ}\text{C}$	13
$I_{DM}$	Pulsed Drain Current (Note 1)	80	A
EAS	Single Pulse Avalanche Energy (Note 2)	14	mJ
IAS	Single Pulse Avalanche Current (Note 2)	17	A
$P_D$	Power Dissipation ( $T_C=25^{\circ}\text{C}$ )	20	W
	Power Dissipation (Derate above $25^{\circ}\text{C}$ )	0.16	W/ $^{\circ}\text{C}$
$T_J$	Operating Junction Temperature Range	-55 to +150	$^{\circ}\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to +150	$^{\circ}\text{C}$
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	62	$^{\circ}\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance-Junction to Case	6.4	$^{\circ}\text{C}/\text{W}$

Note 1: Repetitive Rating: Pulsed width limited by maximum junction temperature.

Note 2:  $V_{DD}=25\text{V}$ ,  $V_{GS}=10\text{V}$ ,  $L=0.1\text{mH}$ ,  $I_{AS}=17\text{A}$ ,  $R_G=25\Omega$ , Starting  $T_J=25^{\circ}\text{C}$ .

## Electrical Characteristics

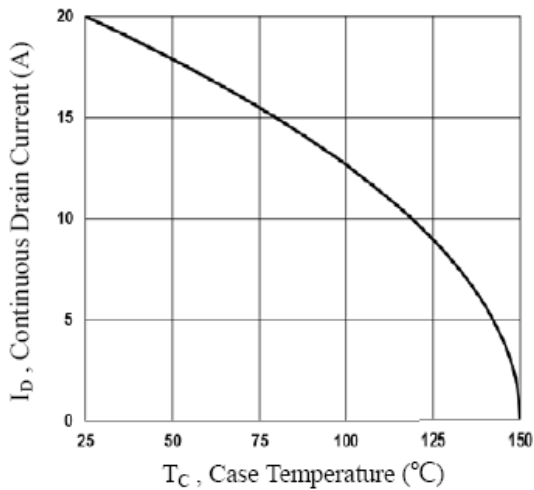
T<sub>J</sub>=25°C Unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	30			V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =1mA		0.04		V/°C
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	1.2	1.5	2.5	V
ΔV <sub>GS(th)</sub>	V <sub>GS(th)</sub> Temperature Coefficient			-4		mV/°C
I <sub>GSS</sub>	Gate Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V			1	uA
		V <sub>DS</sub> =24V, V <sub>GS</sub> =0V, T <sub>J</sub> =125°C			10	
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current			20	A
I <sub>SM</sub>	Pulsed Source Current (Note 3)				80	
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =10A		17	20	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =6A		23	30	
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =6A		13		S
V <sub>SD</sub>	Diode Forward Voltage (Note 3)	V <sub>GS</sub> =0V, I <sub>S</sub> =1A			1	V
<b>Dynamic</b>						
Q <sub>g</sub>	Total Gate Charge (Note 3,4)	V <sub>DS</sub> =15V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =8A		4.1	6	nC
Q <sub>gs</sub>	Gate-Source Charge (Note 3,4)			1	1.4	
Q <sub>gd</sub>	Gate-Drain Charge (Note 3,4)			2.1	4	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHz		345	500	pF
C <sub>oss</sub>	Output Capacitance			55	80	
C <sub>rss</sub>	Reverse Transfer Capacitance			32	55	
t <sub>d(on)</sub>	Turn-On Time (Note 3,4)	V <sub>DD</sub> =15V, I <sub>D</sub> =1A, V <sub>GS</sub> =10V, R <sub>G</sub> =6Ω		2.8	5	ns
t <sub>r</sub>				7.2	14	
t <sub>d(off)</sub>	Turn-Off Time (Note 3,4)			15.8	30	
t <sub>f</sub>				4.6	9	
R <sub>g</sub>	Gate Resistance		V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz		3.2	

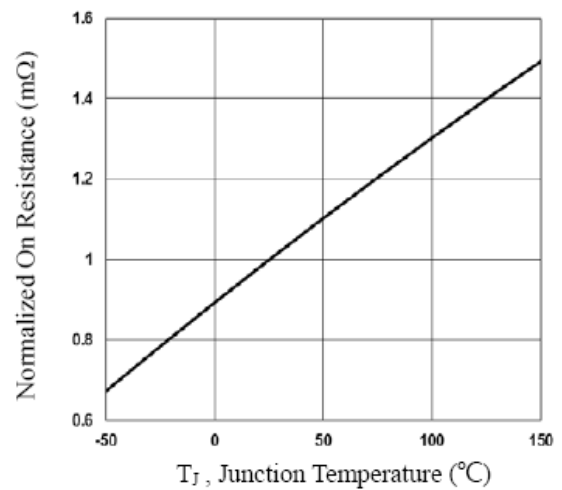
Note 3: The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.

Note 4: Essentially independent of operating temperature.

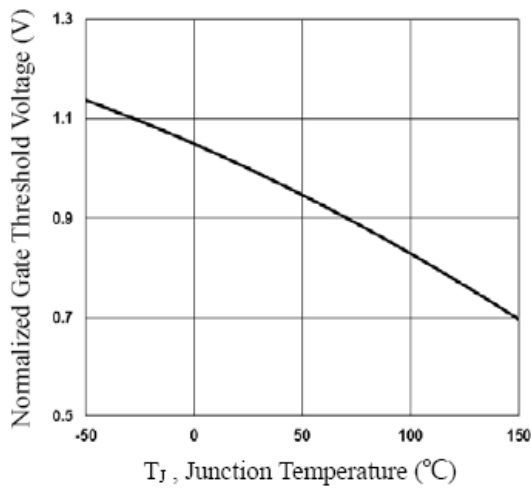
## Typical Performance Characteristics



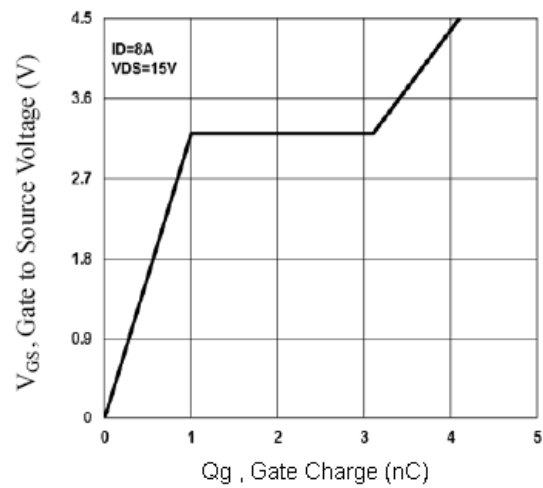
**Fig.1 Continuous Drain Current vs.  $T_C$**



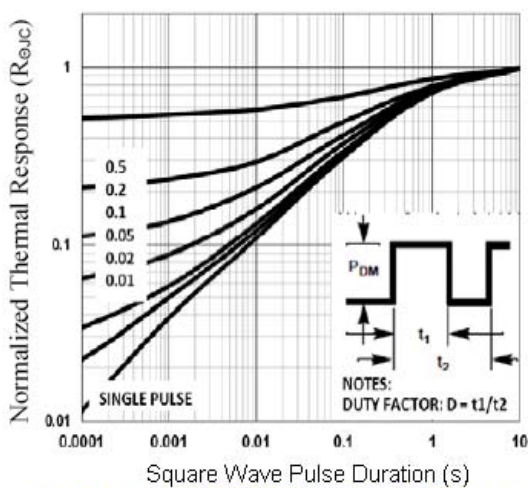
**Fig.2 Normalized  $R_{DS(on)}$  vs.  $T_J$**



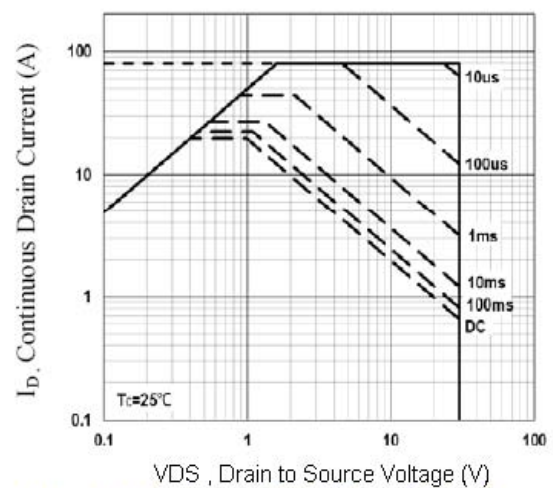
**Fig.3 Normalized  $V_{th}$  vs.  $T_J$**



**Fig.4 Gate Charge Waveform**



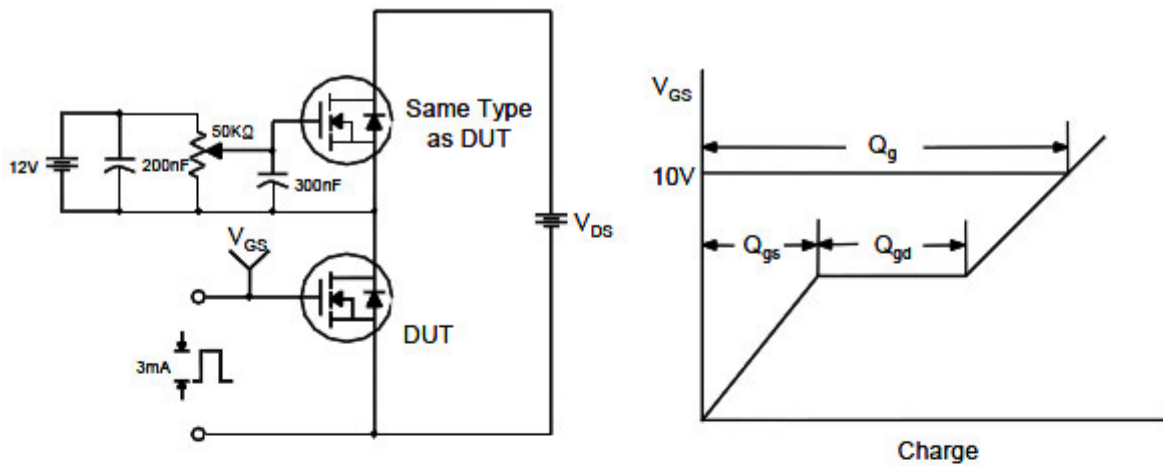
**Fig.5 Normalized Transient Response**



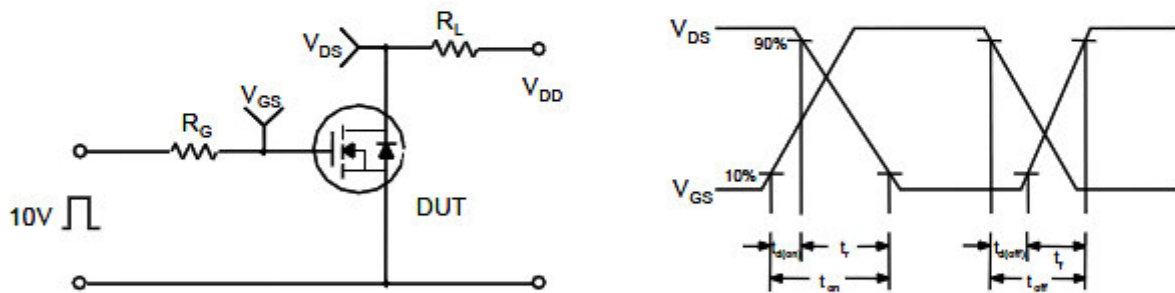
**Fig.6 Maximum Safe Operation Area**

## Typical Performance Characteristics (Continue)

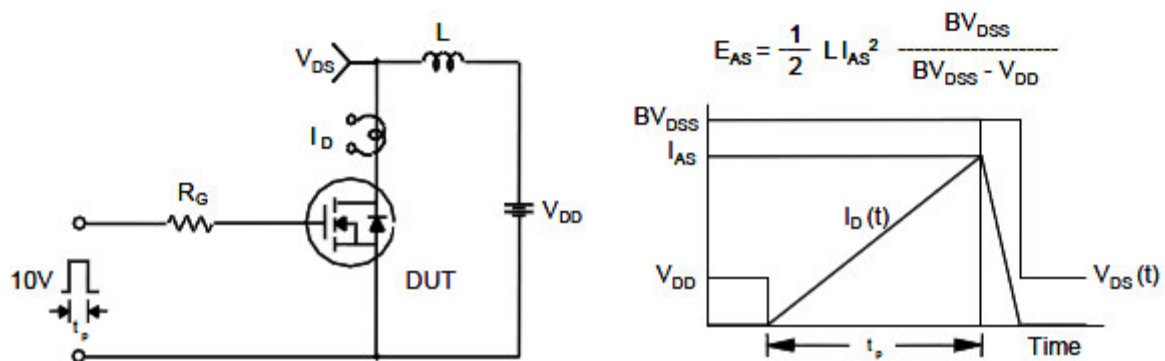
### Gate Charge Test Circuit & Waveform



### Resistive Switching Test Circuit & Waveforms

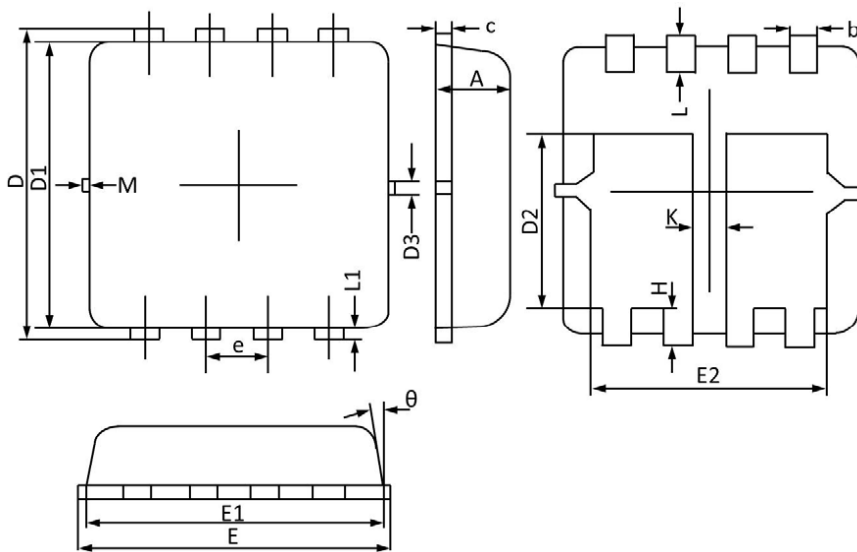


### Unclamped Inductive Switching Test Circuit & Waveforms



## Package Dimension

### DFN3X3-8L










Dimensions				
Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A	0.700	0.800	0.028	0.031
b	0.250	0.350	0.010	0.013
c	0.100	0.250	0.004	0.009
D	3.250	3.450	0.128	0.135
D1	3.000	3.200	0.119	0.125
D2	1.780	1.980	0.070	0.077
D3	0.130 (REF)		0.005 (REF)	
E	3.200	3.400	0.126	0.133
E1	3.000	3.200	0.119	0.125
E2	2.390	2.590	0.094	0.102
e	0.650 (BSC)		0.026 (BSC)	
H	0.300	0.500	0.011	0.019
L	0.300	0.500	0.011	0.019
L1	0.130 (REF)		0.005 (REF)	
K	0.300 (REF)		0.012 (REF)	
$\theta$	0°	12°	0°	12°
M	0.150 (REF)		0.006 (REF)	



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