TOSHIBA Insulated Gate Bipolar Transistor Silicon N Channel IGBT

# GT40Q323

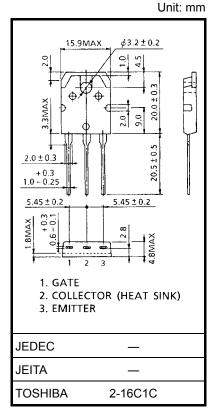
# Voltage Resonance Inverter Switching Application

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- Enhancement-mode
- High speed:  $t_f = 0.14 \,\mu s$  (typ.) (I<sub>C</sub> = 40A)
- FRD included between emitter and collector
- 4th generation
- TO-3P (N) (Toshiba package name)

## **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics		Symbol	Rating	Unit	
Collector-emitter voltage		V <sub>CES</sub>	1200	V	
Gate-emitter voltage		$V_{GES}$	±25	V	
Continuous collector current	@ Tc = 100°C	Ic	20	Α	
	@ Tc = 25°C	iC	39		
Pulsed collector current		I <sub>CP</sub>	80	Α	
Diode forward current	DC	IF	10	А	
	Pulsed	IFP	80		
Collector power dissipation	@ Tc = 100°C	Pc	80	W	
	@ Tc = 25°C	FC	200		
Junction temperature		Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	−55 to 150	°C	



Weight: 4.6 g (typ.)

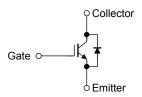
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance (IGBT)	R <sub>th (j-c)</sub>	0.625	°C/W
Thermal resistance (diode)	R <sub>th (j-c)</sub>	1.79	°C/W

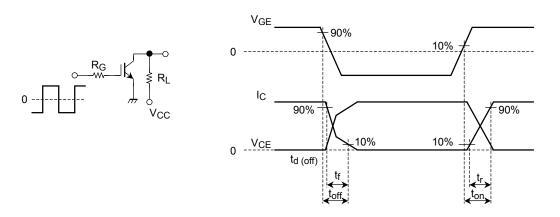
### **Equivalent Circuit**

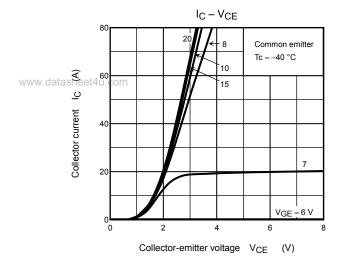


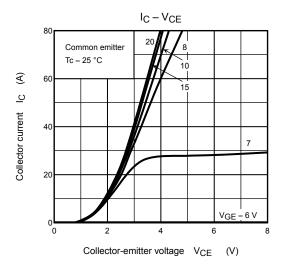
# **Electrical Characteristics (Ta = 25°C)**

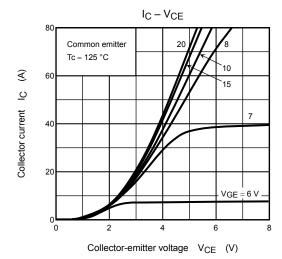
	Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
	Gate leakage current  Collector cut-off current		I <sub>GES</sub>	V <sub>GE</sub> = ±25 V, V <sub>CE</sub> = 0	_	_	±500	nA
			I <sub>CES</sub>	V <sub>CE</sub> = 1200 V, V <sub>GE</sub> = 0	_	_	5.0	mA
www.datas	Gaté-emitter cut-off voltage		V <sub>GE</sub> (OFF)	I <sub>C</sub> = 40 mA, V <sub>CE</sub> = 5 V	4.0	_	7.0	V
	Collector-emitter saturation voltage		V <sub>CE</sub> (sat)	I <sub>C</sub> = 40 A, V <sub>GE</sub> = 15 V	_	3.0	3.7	V
	Input capacitance		C <sub>ies</sub>	V <sub>CE</sub> = 10 V, V <sub>GE</sub> = 0, f = 1 MHz	_	5550	_	pF
	Switching time	Rise time	t <sub>r</sub>	Resistive Load	_	0.18	_	- µs
		Turn-on time	t <sub>on</sub>	V <sub>CC</sub> = 600 V, I <sub>C</sub> = 40 A	_	0.26	_	
		Fall time	t <sub>f</sub>	$V_{GG}$ = ±15 V, $R_G$ = 39 $\Omega$	_	0.14	0.21	
		Turn-off time	t <sub>off</sub>	(Note 1)	_	0.43	_	
	Diode forward voltage  Reverse recovery time		V <sub>F</sub>	I <sub>F</sub> = 10 A, V <sub>GE</sub> = 0	_	_	2.1	V
			t <sub>rr</sub>	I <sub>F</sub> = 10 A, di/dt = -20 A/μs	_	0.4	_	μs

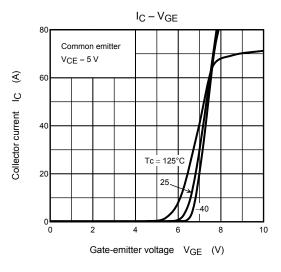
Note 1: Switching time measurement circuit and input/output waveforms

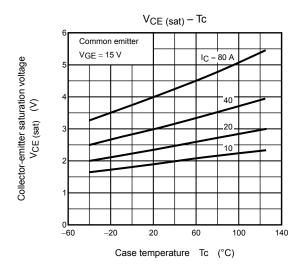


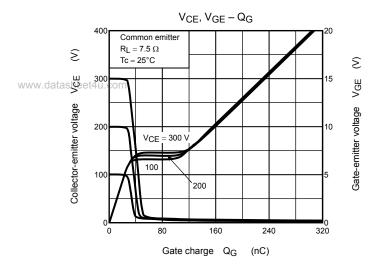


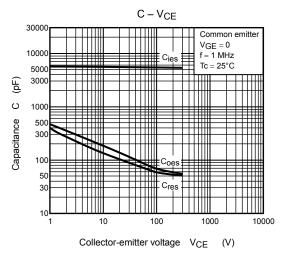


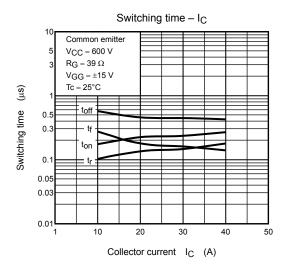


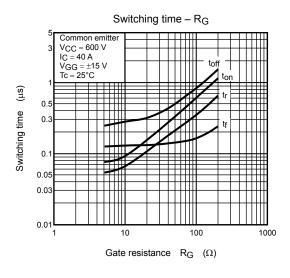


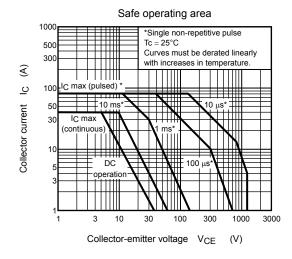


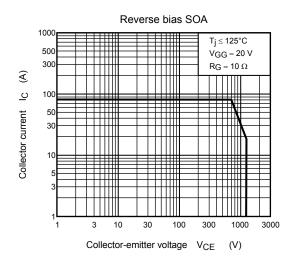




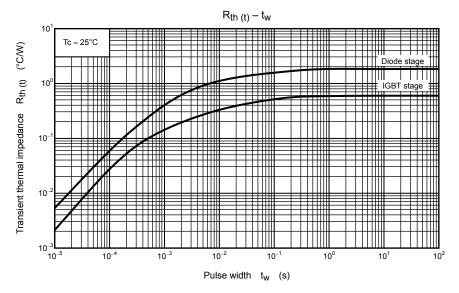


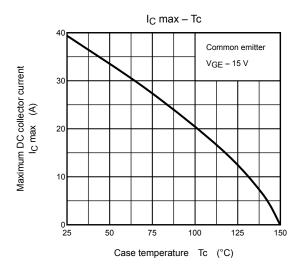


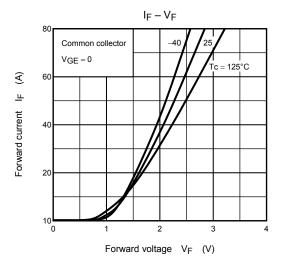


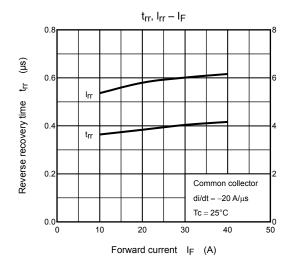


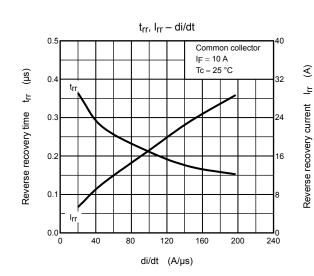
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Reverse recovery current Irr

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