

GU75N07

N-CHANNEL ENHANCEMENT MODE POWER MOSFET

BVDSS	75V
RDS(ON)	11mΩ
ID	80A

Description

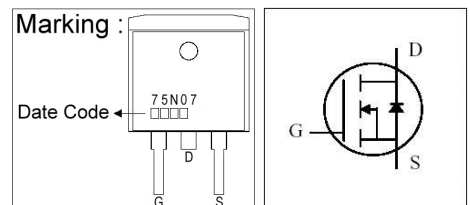
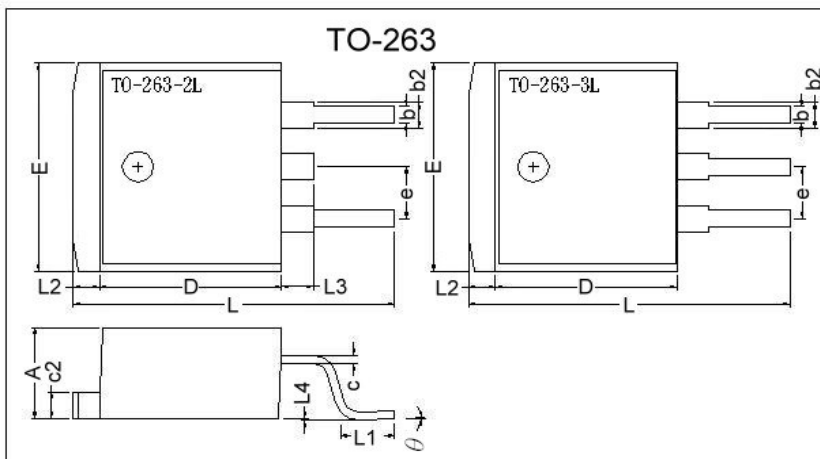
The GU75N07 provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-263 package is universally preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.

Features

- *Low Gate Charge
- *Simple Drive Requirement
- *Fast Switching Characteristics
- *RoHS Compliant

Package Dimensions



REF.	Min.	Max.	REF.	Min.	Max.
A	4.40	4.80	c2	1.25	1.45
b	0.76	1.00	b2	1.17	1.47
L4	0.00	0.30	D	8.6	9.0
c	0.36	0.5	e	2.54	REF.
L3	1.50	REF.	L	14.6	15.8
L1	2.29	2.79	θ	0°	8°
E	9.80	10.4	L2	1.27	REF.

Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	75	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current, $V_{GS}@10V$	$I_D @T_C=25^{\circ}C$	80	A
Continuous Drain Current, $V_{GS}@10V$	$I_D @T_C=100^{\circ}C$	56	A
Pulsed Drain Current ¹	I_{DM}	300	A
Total Power Dissipation	$P_D @T_C=25^{\circ}C$	156	W
Linear Derating Factor		0.125	W/°C
Single Pulse Avalanche Energy ³	E_{AS}	450	mJ
Operating Junction and Storage Temperature Range	T_j, T_{stg}	-55 ~ +150	°C

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance Junction-case Max.	$R_{thj-case}$	0.8	°C/W
Thermal Resistance Junction-ambient Max.	$R_{thj-amb}$	62	°C/W

Electrical Characteristics (T_j = 25°C unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	BV _{DSS}	75	-	-	V	V _{GS} =0, I _D =1mA
Breakdown Voltage Temperature Coefficient	$\Delta BV_{DSS} / \Delta T_j$	-	0.08	-	V/°C	Reference to 25°C, I _D =1mA
Gate Threshold Voltage	V _{GS(th)}	1.0	-	3.0	V	V _{DS} =V _{GS} , I _D =250uA
Forward Transconductance	g _{fs}	-	40	-	S	V _{DS} =10V, I _D =40A
Gate-Source Leakage Current	I _{GSS}	-	-	±100	nA	V _{GS} = ±20V
Drain-Source Leakage Current(T _j =25°C)	I _{DSS}	-	-	10	uA	V _{DS} =60, V _{GS} =0
Drain-Source Leakage Current(T _j =150°C)		-	-	100	uA	V _{DS} =75V, V _{GS} =0
Static Drain-Source On-Resistance	R _{DS(ON)}	-	-	11	mΩ	V _{GS} =10V, I _D =40A
Total Gate Charge ²	Q _g	-	83	130	nC	I _D =40A V _{DS} =60V V _{GS} =4.5V
Gate-Source Charge	Q _{gs}	-	10	-		
Gate-Drain ("Miller") Change	Q _{gd}	-	51	-		
Turn-on Delay Time ²	T _{d(on)}	-	15	-	ns	V _{DS} =40V I _D =30A V _{GS} =10V R _G =10Ω R _D =1.33Ω
Rise Time	T _r	-	73	-		
Turn-off Delay Time	T _{d(off)}	-	340	-		
Fall Time	T _f	-	200	-		
Input Capacitance	C _{iss}	-	4270	6830	pF	V _{GS} =0V V _{DS} =25V f=1.0MHz
Output Capacitance	C _{oss}	-	690	-		
Reverse Transfer Capacitance	C _{rss}	-	320	-		
Gate Resistance	R _g	-	1.8	2.7	Ω	f=1.0MHz

Source-Drain Diode

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Forward On Voltage ²	V _{SD}	-	-	1.5	V	I _S =40A, V _{GS} =0V, T _j =25°C
Reverse Recovery Time	T _{rr}	-	90	-	ns	I _S =40A, V _{GS} =0V di/dt=100A/μs
Reverse Recovery Charge	Q _{rr}	-	235	-	nC	

Notes: 1. Pulse width limited by safe operating area.

2. Pulse width ≤ 300us, duty cycle ≤ 2%.

3. Starting T_j=25°C, V_{DD}=50V, L=1mH, R_G=25Ω, I_{AS}=30A.

Characteristics Curve

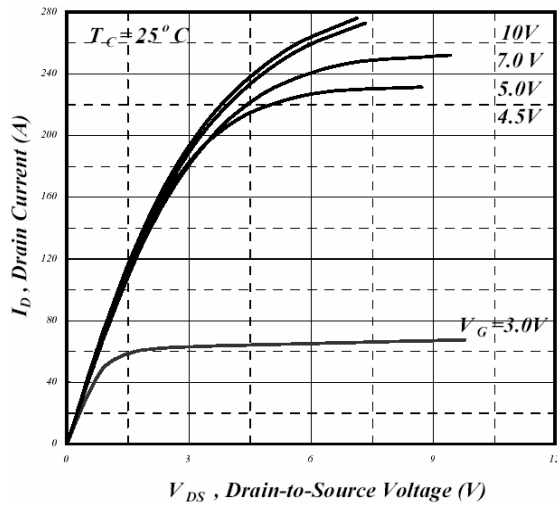


Fig 1. Typical Output Characteristics

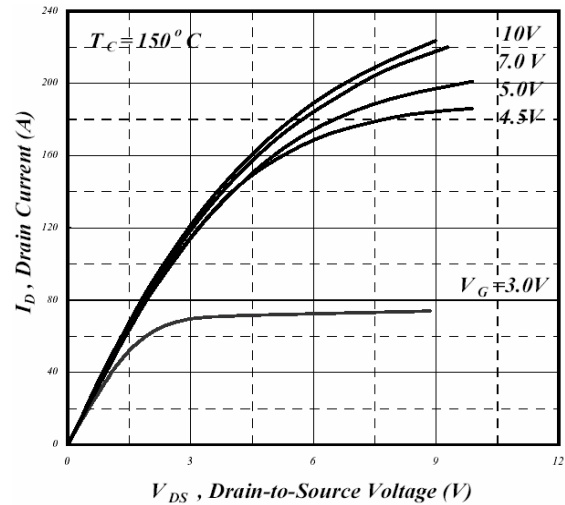


Fig 2. Typical Output Characteristics

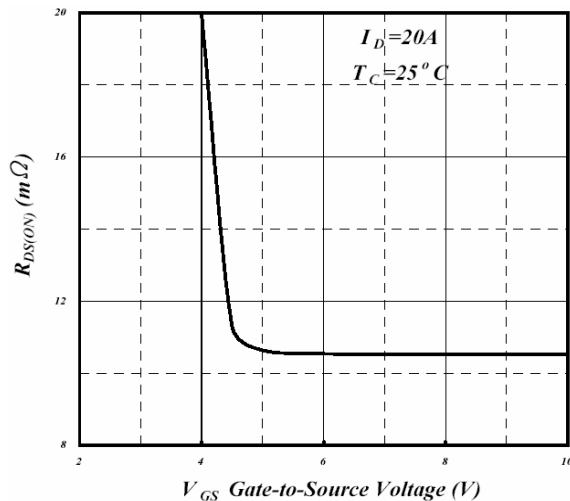


Fig 3. On-Resistance v.s. Gate Voltage

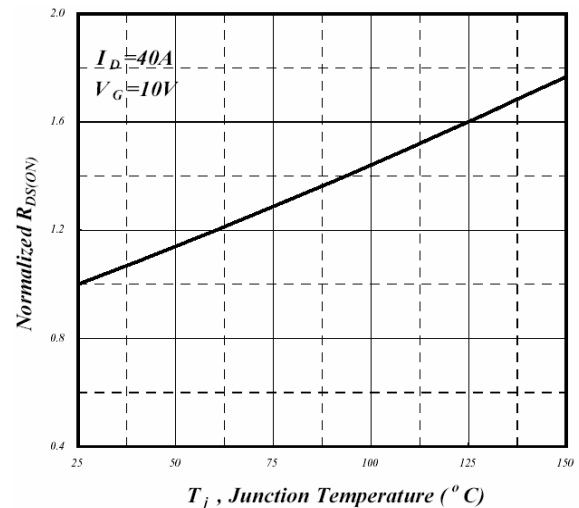


Fig 4. Normalized On-Resistance v.s. Junction Temperature

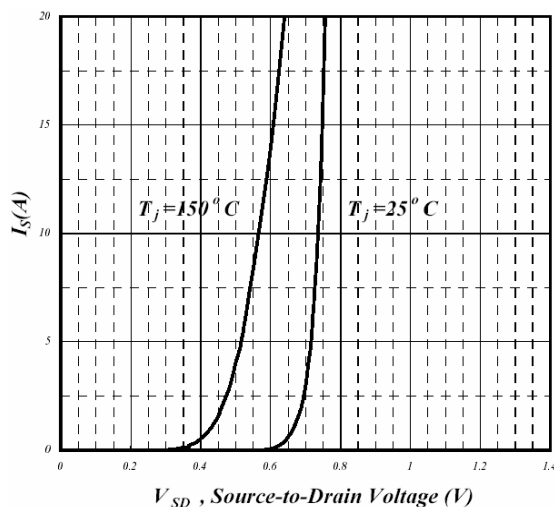


Fig 5. Forward Characteristics of Reverse Diode

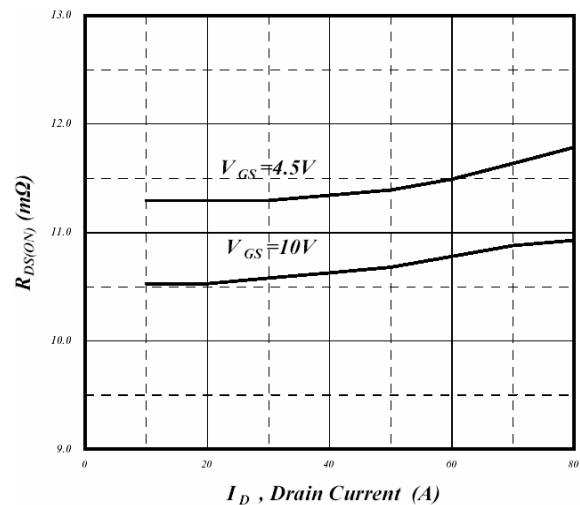


Fig 6. On-Resistance v.s. Drain Current

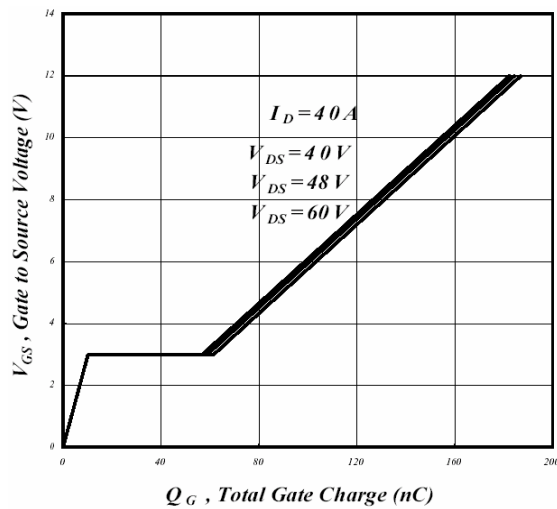


Fig 7. Gate Charge Characteristics

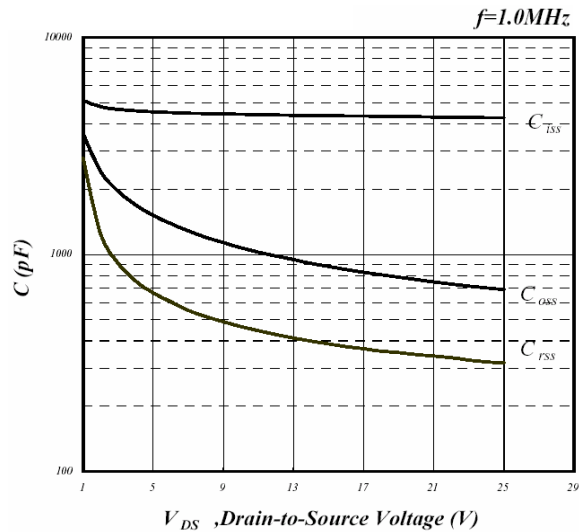


Fig 8. Typical Capacitance Characteristics

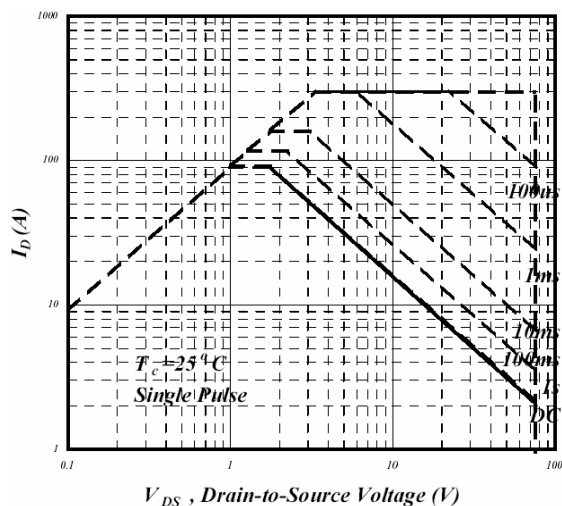


Fig 9. Maximum Safe Operating Area

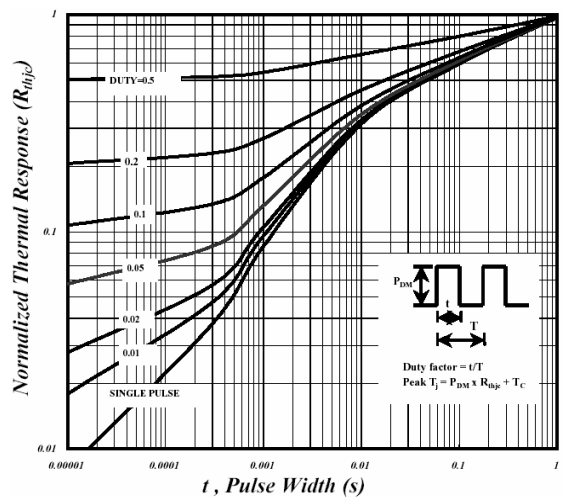


Fig 10. Effective Transient Thermal Impedance

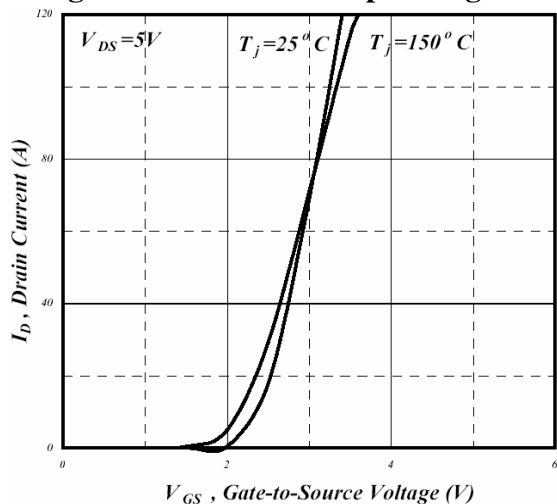


Fig 11. Transfer Characteristics

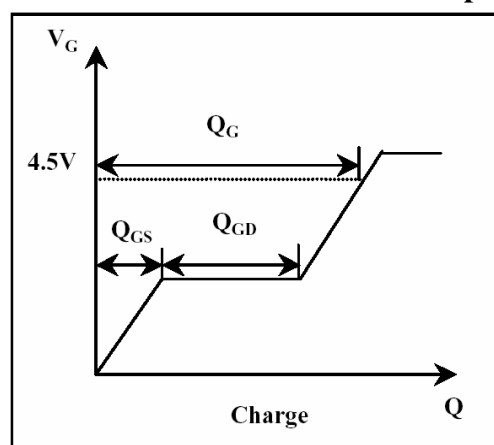


Fig 12. Gate Charge Waveform

Important Notice:

- All rights are reserved. Reproduction in whole or in part is prohibited without the prior written approval of GTM.
- GTM reserves the right to make changes to its products without notice.
- GTM semiconductor products are not warranted to be suitable for use in life-support Applications, or systems.
- GTM assumes no liability for any consequence of customer product design, infringement of patents, or application assistance.

Head Office And Factory:

- **Taiwan:** No. 17-1 Tatung Rd. Fu Kou Hsin-Chu Industrial Park, Hsin-Chu, Taiwan, R. O. C.
TEL : 886-3-597-7061 FAX : 886-3-597-9220, 597-0785
- **China:** (201203) No.255, Jang-Jiang Tsai-Lueng RD. , Pu-Dung-Hsin District, Shang-Hai City, China
TEL : 86-21-5895-7671 ~ 4 FAX : 86-21-38950165