H5N2008P

Silicon N Channel MOS FET
High Speed Power Switching

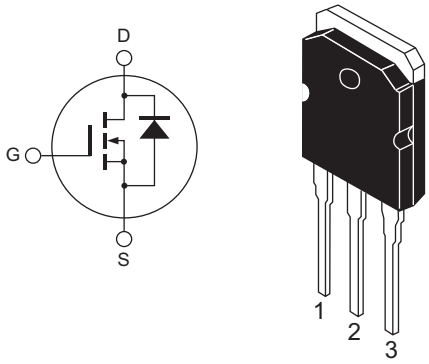
REJ03G0390-0300
Rev.3.00
Nov.24.2004

Features

- Low on-resistance
- Low leakage current
- High speed switching

Outline

TO-3P



1. Gate
2. Drain (Flange)
3. Source

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to Source voltage	V_{DSS}	200	V
Gate to Source voltage	V_{GSS}	±30	V
Drain current	I_D	96	A
Drain peak current	$I_{D\ (pulse)}$ Note1	192	A
Body-Drain diode reverse Drain current	I_{DR}	96	A
Body-Drain diode reverse Drain peak current	$I_{DR\ (pulse)}$ Note1	192	A
Avalanche current	I_{AP} Note3	48	A
Avalanche energy	E_{AR} Note3	153	mJ
Channel dissipation	Pch Note2	150	W
Channel to case thermal impedance	θch-c	0.833	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	–55 to +150	°C

Notes: 1. PW ≤ 10 µs, duty cycle ≤ 1%
2. Value at Tc = 25°C
3. STch = 25°C, Tch ≤ 150°C

Electrical Characteristics

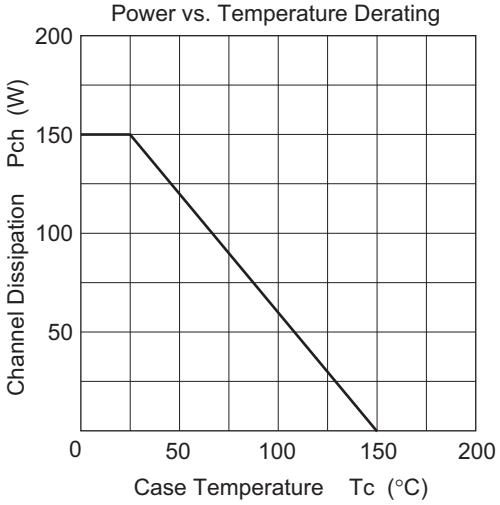
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to Source breakdown voltage	$V_{(BR)DSS}$	200	—	—	V	I_D = 10 mA, V_{GS} = 0
Zero Gate voltage Drain current	I_{DSS}	—	—	1	µA	V_{DS} = 200 V, V_{GS} = 0
Gate to Source leak current	I_{GSS}	—	—	±0.1	µA	V_{GS} = ±30 V, V_{DS} = 0
Gate to Source cutoff voltage	$V_{GS(off)}$	3.0	—	4.5	V	V_{DS} = 10 V, I_D = 1 mA
Forward transfer admittance	|yfs|	35	58	—	S	I_D = 48 A, V_{DS} = 10 V Note4
Static Drain to Source on state resistance	$R_{DS(on)}$	—	0.020	0.023	Ω	I_D = 48 A, V_{GS} = 10 V Note4
Input capacitance	Ciss	—	4900	—	pF	V_{DS} = 25 V V_{GS} = 0 f = 1 MHz
Output capacitance	Coss	—	850	—	pF	
Reverse transfer capacitance	Crss	—	95	—	pF	
Turn-on delay time	$t_{d(on)}$	—	60	—	ns	I_D = 48 A V_{GS} = 10 V R_L = 2.1 Ω Rg = 10 Ω
Rise time	t_r	—	370	—	ns	
Turn-off delay time	$t_{d(off)}$	—	220	—	ns	
Fall time	t_f	—	270	—	ns	
Total Gate charge	Qg	—	98	—	nC	V_{DD} = 160 V V_{GS} = 10 V I_D = 96 A
Gate to Source charge	Qgs	—	25	—	nC	
Gate to Drain charge	Qgd	—	44	—	nC	
Body-Drain diode forward voltage	V_{DF}	—	1.1	1.7	V	I_F = 96 A, V_{GS} = 0 Note4
Body-Drain diode reverse recovery time	trr	—	180	—	ns	I_F = 96 A, V_{GS} = 0 diF/dt = 100 A/µs
Body-Drain diode reverse recovery charge	Qrr	—	1.5	—	µC	

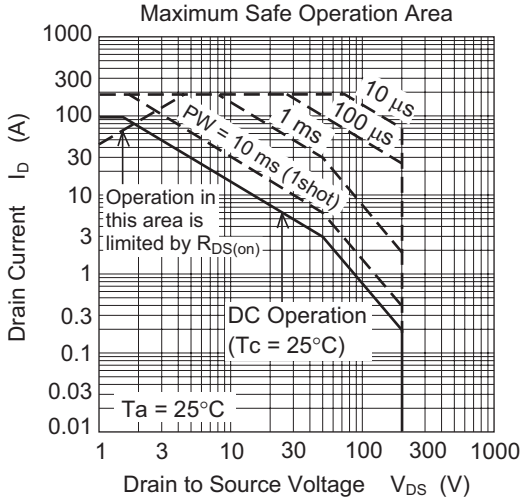
Notes: 4. Pulse test

Main Characteristics

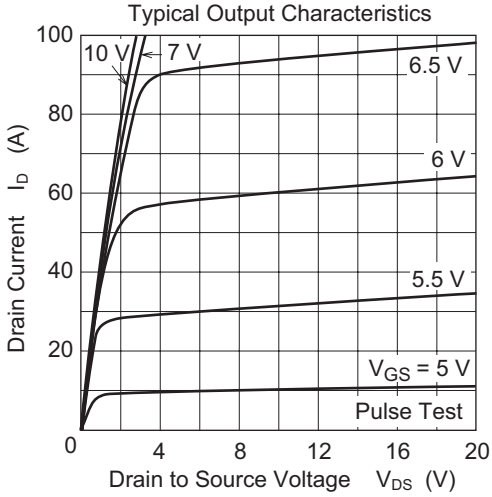
Power vs. Temperature Derating



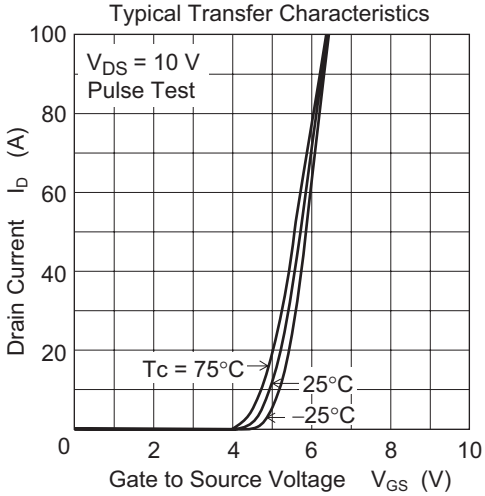
Maximum Safe Operation Area



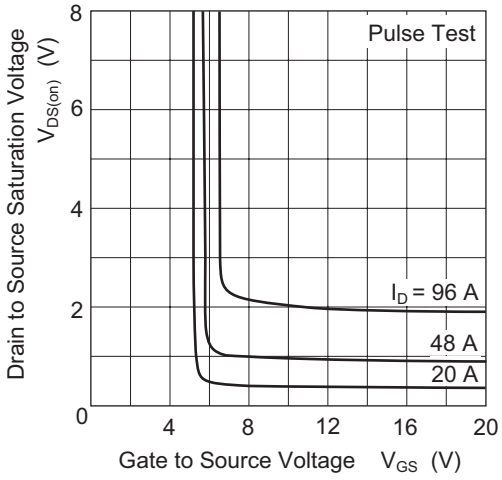
Typical Output Characteristics



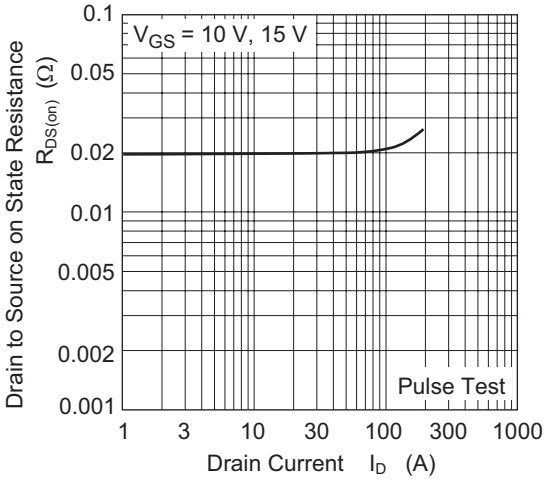
Typical Transfer Characteristics



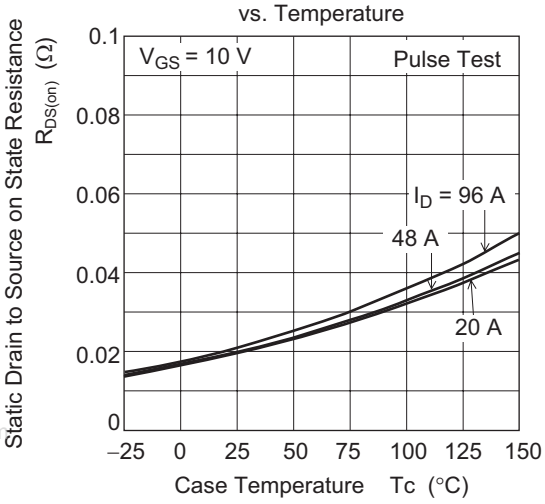
Drain to Source Saturation Voltage vs. Gate to Source Voltage



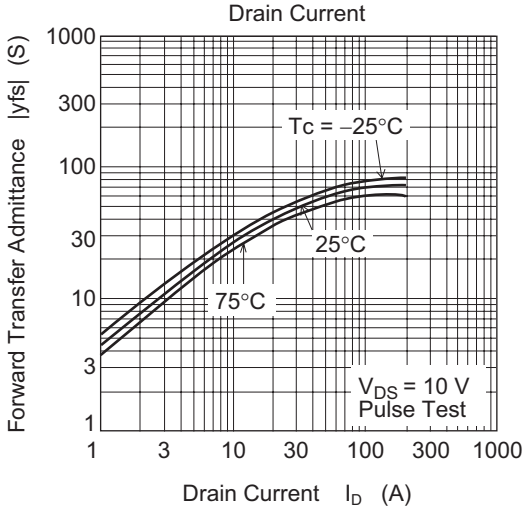
Static Drain to Source on State Resistance vs. Drain Current



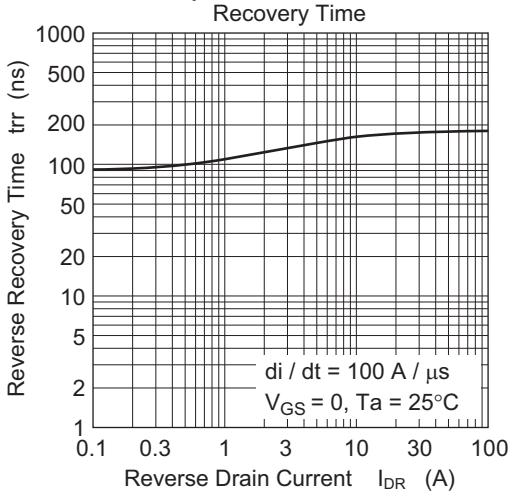
Static Drain to Source on State Resistance vs. Temperature



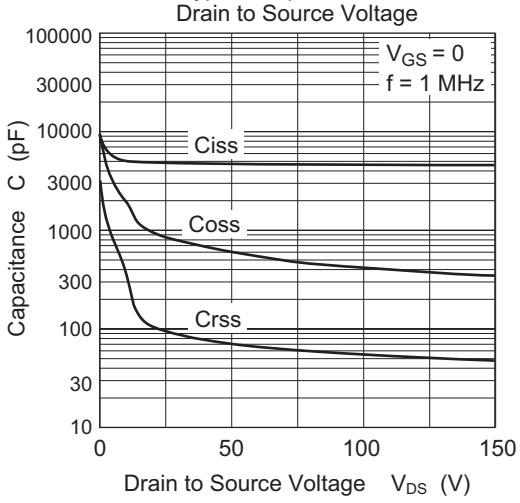
Forward Transfer Admittance vs. Drain Current



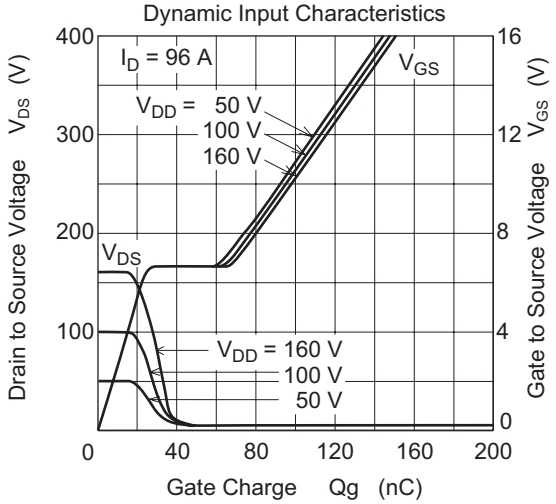
Body-Drain Diode Reverse Recovery Time



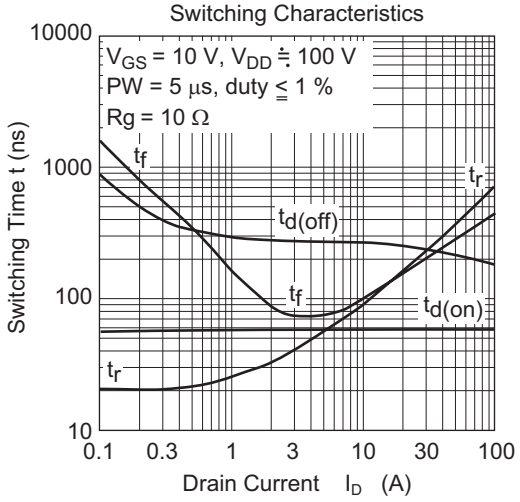
Typical Capacitance vs. Drain to Source Voltage



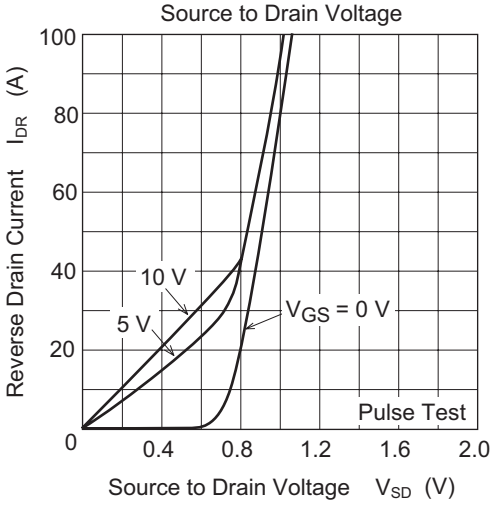
Dynamic Input Characteristics



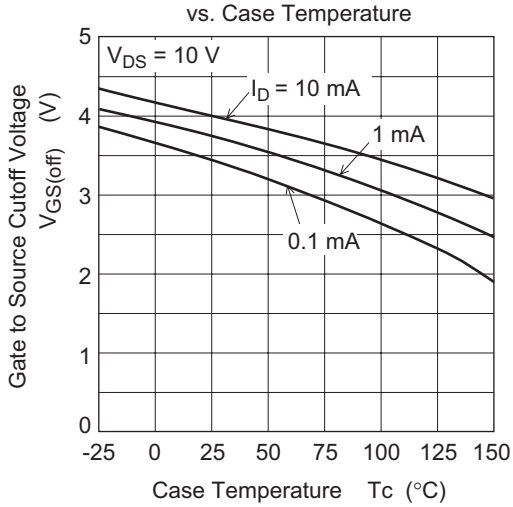
Switching Characteristics



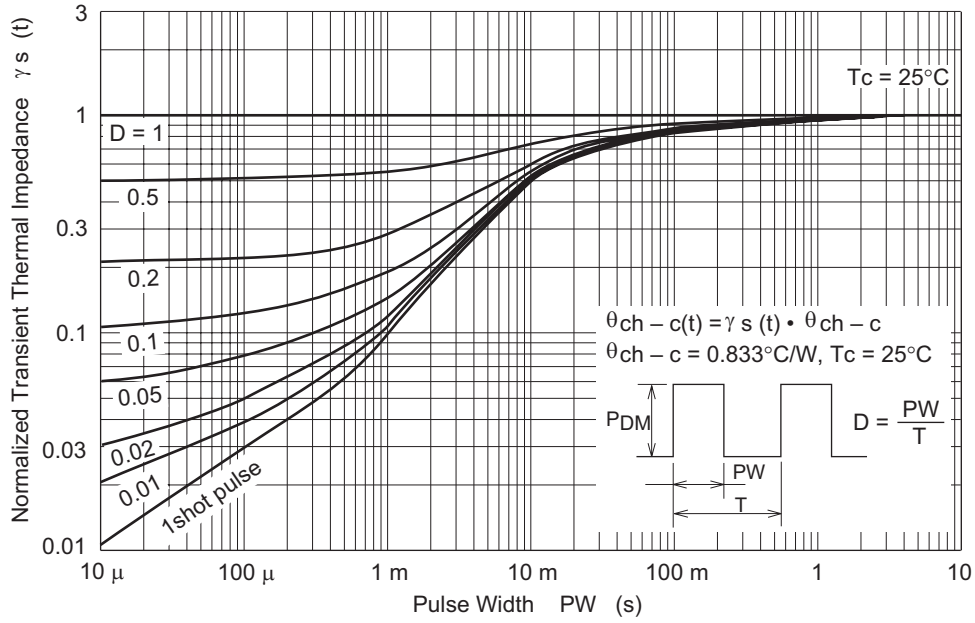
Reverse Drain Current vs. Source to Drain Voltage



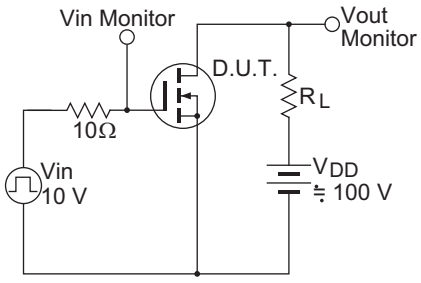
Gate to Source Cutoff Voltage vs. Case Temperature



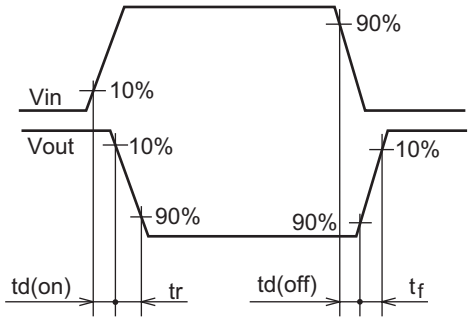
Normalized Transient Thermal Impedance vs. Pulse Width



Switching Time Test Circuit



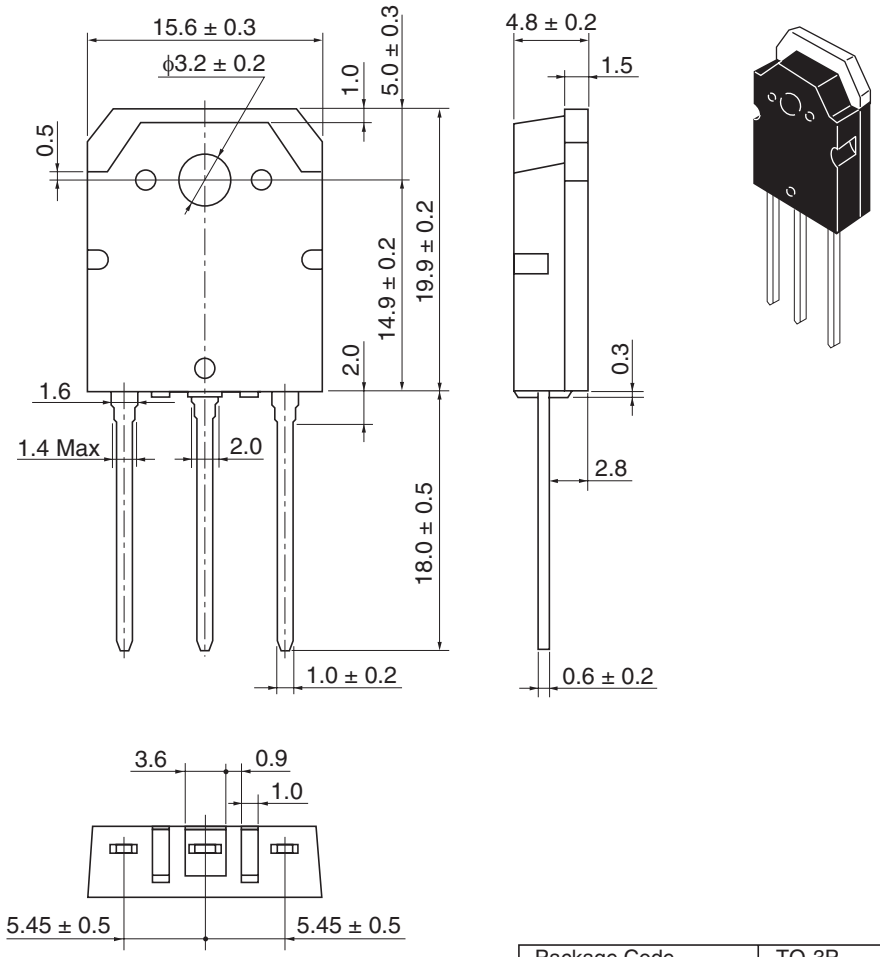
Waveform



Package Dimensions

As of January, 2003

Unit: mm



Package Code	TO-3P
JEDEC	—
JEITA	Conforms
Mass (reference value)	5.0 g

Ordering Information

Part Name	Quantity	Shipping Container
H5N2008P-E	30 pcs	Plastic magazine

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