H5N2504DL, H5N2504DS

Silicon N Channel MOS FET
High Speed Power Switching

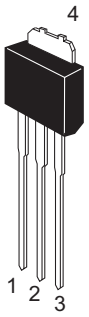
REJ03G1106-0200
(Previous: ADE-208-1375A)
Rev.2.00
Sep 07, 2005

Features

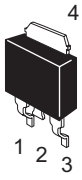
- Low on-resistance
- Low leakage current
- High speed switching
- Low gate charge
- Avalanche ratings

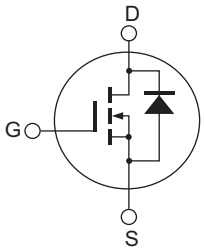
Outline

RENESAS Package code: PRSS0004ZD-B
(Package name: DPAK (L)-(2))



RENESAS Package code: PRSS0004ZD-C
(Package name: DPAK (S))





1. Gate
2. Drain
3. Source
4. Drain

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Drain to source voltage	V_{DSS}	250	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I_D	7	A
Drain peak current	$I_{D\ (pulse)}$ Note 1	28	A
Body-drain diode reverse drain current	I_{DR}	7	A
Body-drain diode reverse drain peak current	$I_{DR\ (pulse)}$ Note 1	28	A
Avalanche current	I_{AP} Note 3	7	A
Channel dissipation	Pch Note 2	30	W
Channel to case thermal Impedance	θ ch-c	4.17	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	–55 to +150	°C

Notes: 1. PW ≤ 10 µs, duty cycle ≤ 1%
2. Value at Tc = 25°C
3. Tch ≤ 150°C

Electrical Characteristics

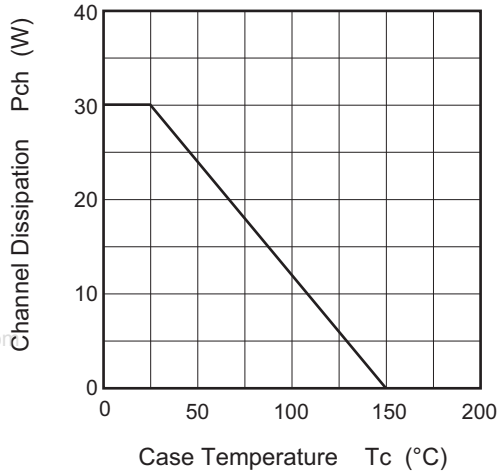
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)\ DSS}$	250	—	—	V	I_D = 10 mA, V_{GS} = 0
Gate to source leak current	I_{GSS}	—	—	±0.1	µA	V_{GS} = ±20 V, V_{DS} = 0
Zero gate voltage drain current	I_{DSS}	—	—	1	µA	V_{DS} = 250 V, V_{GS} = 0
Gate to source cutoff voltage	$V_{GS\ (off)}$	1.0	—	2.5	V	V_{DS} = 10 V, I_D = 1 mA
Static drain to source on state resistance	$R_{DS\ (on)}$	—	0.48	0.63	Ω	I_D = 3.5 A, V_{GS} = 10 V Note 4
	$R_{DS\ (on)}$	—	0.5	0.67	Ω	I_D = 3.5 A, V_{GS} = 4 V Note 4
Forward transfer admittance	$\|y_{fs}\|$	5	8.5	—	S	I_D = 3.5 A, V_{DS} = 10 V Note 4
Input capacitance	Ciss	—	570	—	pF	V_{DS} = 25 V
Output capacitance	Coss	—	60	—	pF	V_{GS} = 0
Reverse transfer capacitance	Crss	—	12	—	pF	f = 1 MHz
Turn-on delay time	$t_{d\ (on)}$	—	13	—	ns	I_D = 3.5 A
Rise time	t_r	—	18	—	ns	V_{GS} = 10 V
Turn-off delay time	$t_{d\ (off)}$	—	70	—	ns	R_L = 35.7 Ω
Fall time	t_f	—	8	—	ns	Rg = 10 Ω
Total gate charge	Qg	—	21	—	nC	V_{DD} = 200 V
Gate to source charge	Qgs	—	2	—	nC	V_{GS} = 10 V
Gate to drain charge	Qgd	—	6	—	nC	I_D = 7 A
Body-drain diode forward voltage	V_{DF}	—	0.85	1.30	V	I_F = 7 A, V_{GS} = 0
Body-drain diode reverse recovery time	t_{rr}	—	120	—	ns	I_F = 7 A, V_{GS} = 0
Body-drain diode reverse recovery charge	Q_{rr}	—	0.48	—	µC	di_F/dt = 100 A/µs

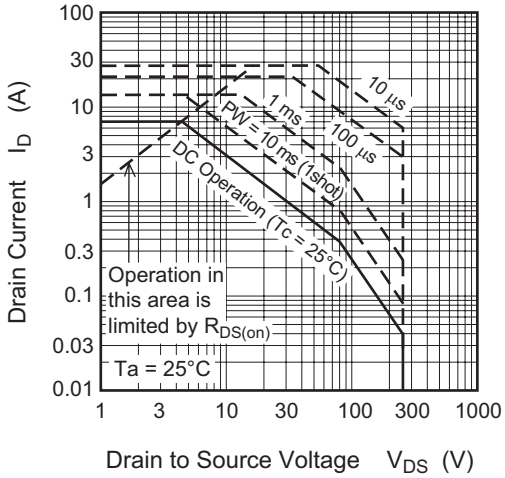
Note: 4. Pulse test

Main Characteristics

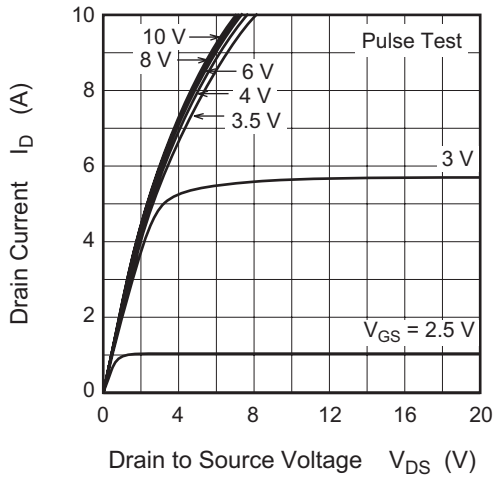
Power vs. Temperature Derating



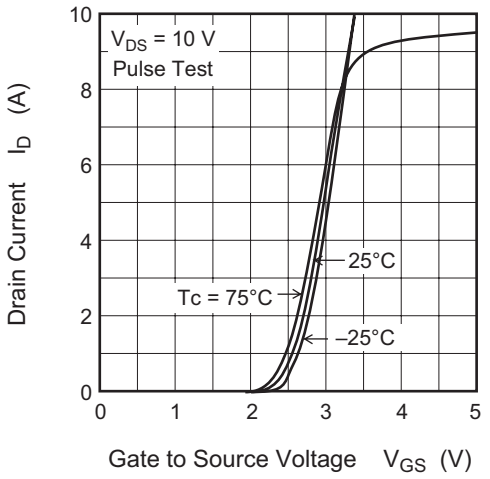
Maximum Safe Operation Area



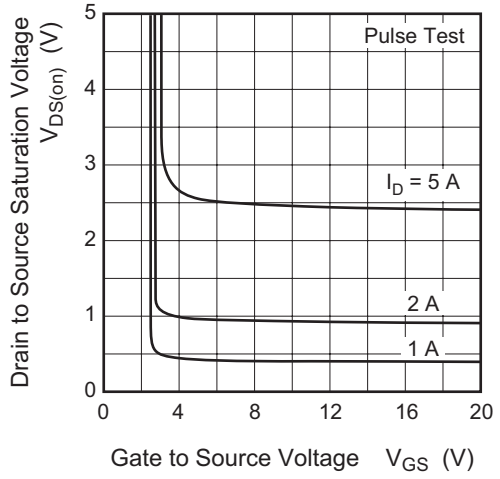
Typical Output Characteristics



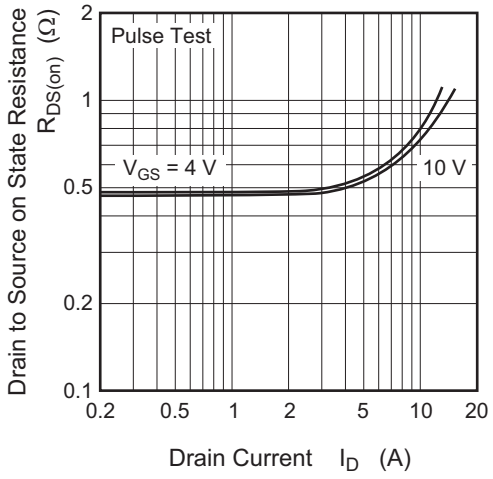
Typical Transfer Characteristics



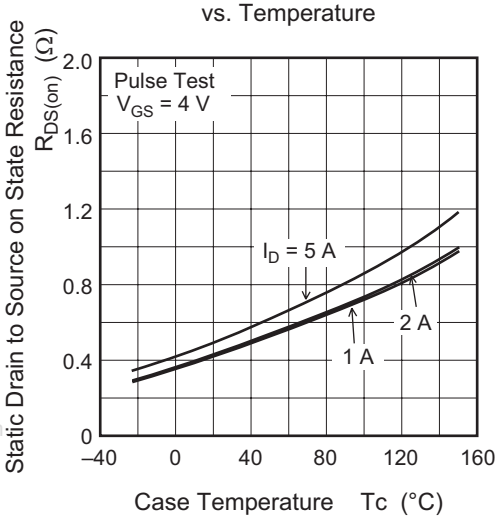
Drain to Source Saturation Voltage vs. Gate to Source Voltage



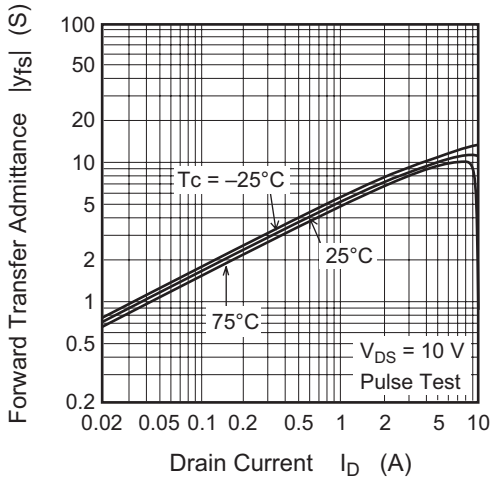
Static Drain to Source on State Resistance vs. Drain Current



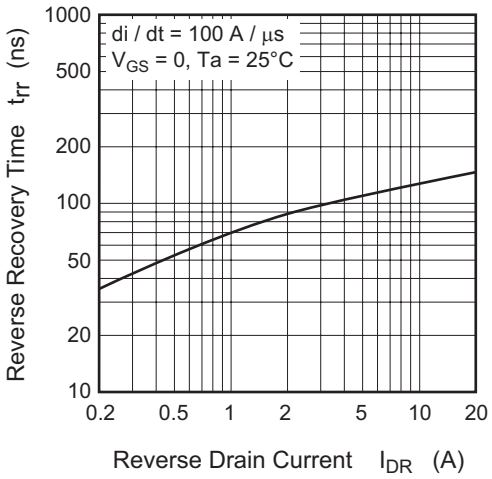
Static Drain to Source on State Resistance vs. Temperature



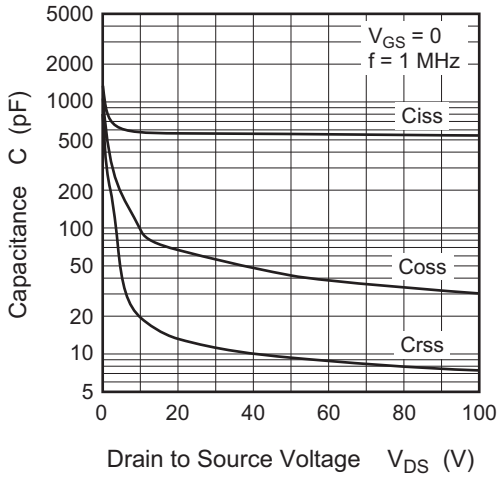
Forward Transfer Admittance vs. Drain Current



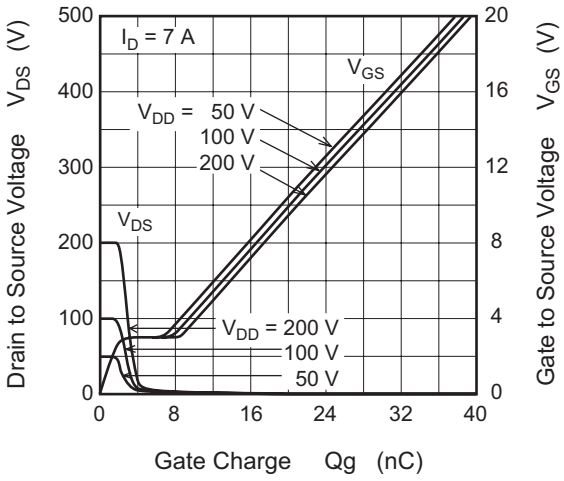
Body-Drain Diode Reverse Recovery Time



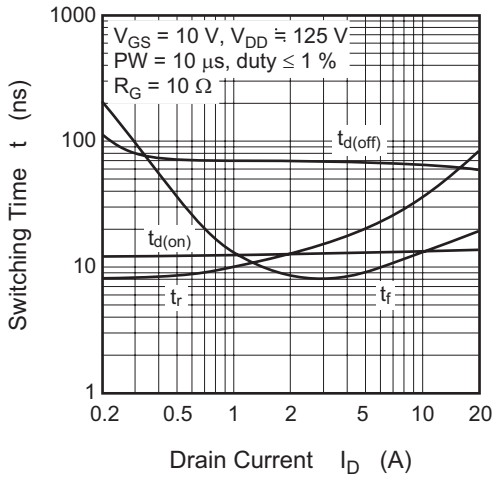
Typical Capacitance vs. Drain to Source Voltage



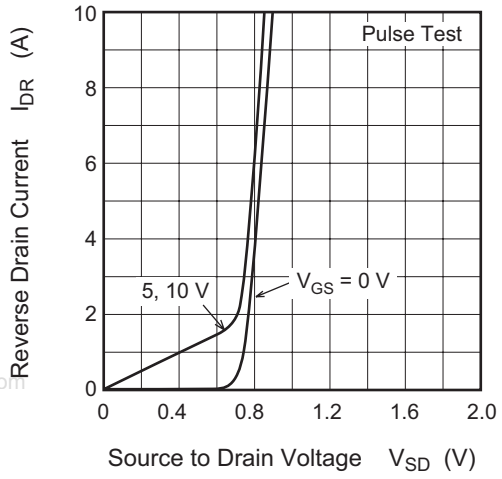
Dynamic Input Characteristics



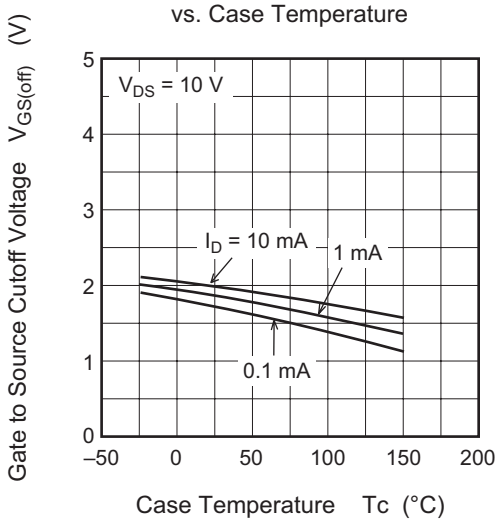
Switching Characteristics



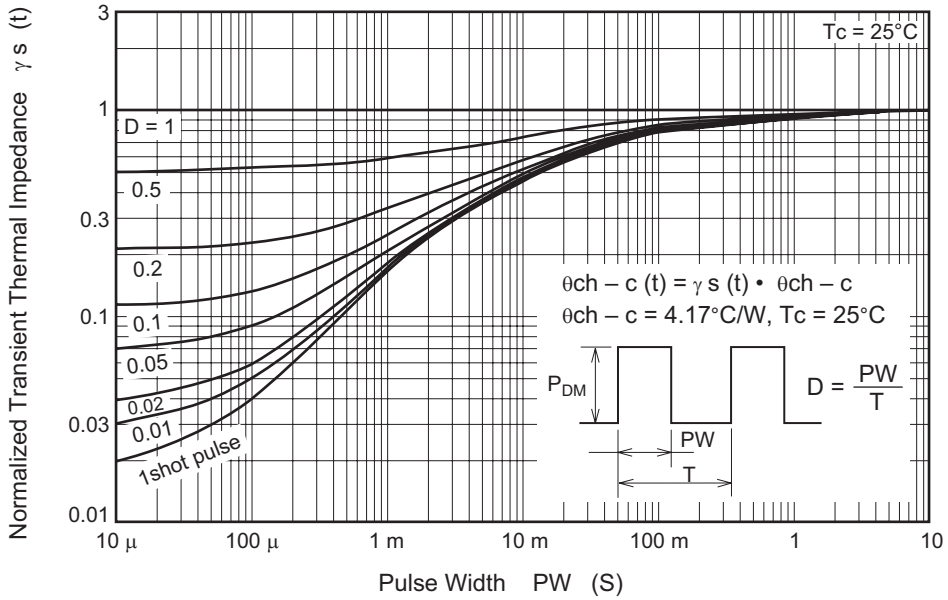
Reverse Drain Current vs. Source to Drain Voltage



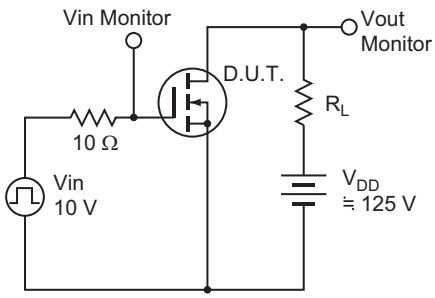
Gate to Source Cutoff Voltage vs. Case Temperature



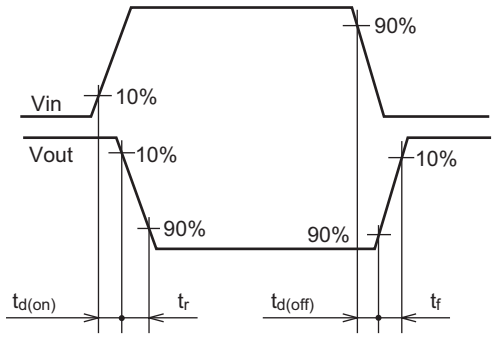
Normalized Transient Thermal Impedance vs. Pulse Width



Switching Time Test Circuit



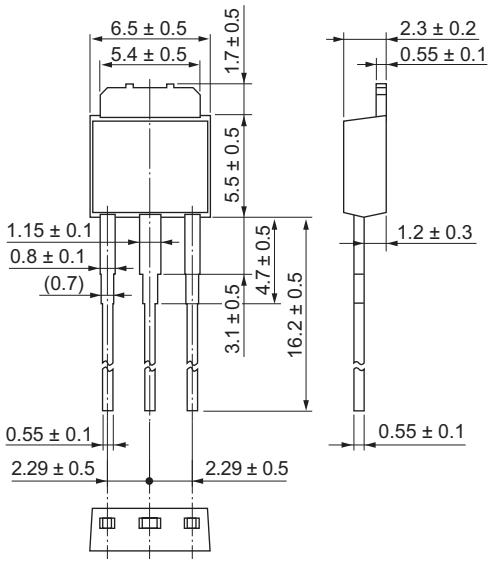
Waveform



Package Dimensions

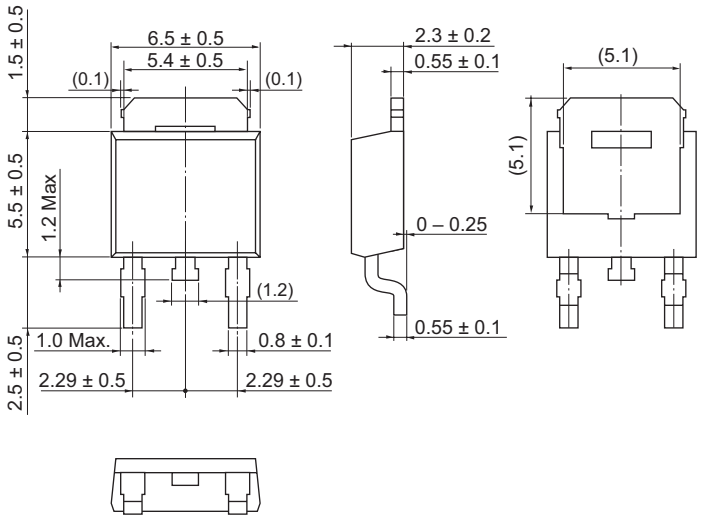
JEITA Package Code	RENESAS Code	Package Name	MASS[Typ.]
—	PRSS0004ZD-B	DPAK(L)-(2) / DPAK(L)-(2)V	0.42g

Unit: mm



JEITA Package Code	RENESAS Code	Package Name	MASS[Typ.]
SC-63	PRSS0004ZD-C	DPAK(S) / DPAK(S)V	0.28g

Unit: mm



Ordering Information

Part Name	Quantity	Shipping Container
H5N2504DL-E	3200 pcs	Box (Sack)
H5N2504DSTL-E	3000 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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