RENESAS

H5N2508DL, H5N2508DS

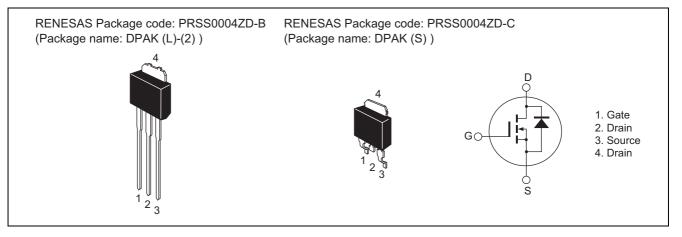
Silicon N Channel MOS FET High Speed Power Switching

> REJ03G1108-0200 (Previous: ADE-208-1377) Rev.2.00 Sep 07, 2005

Features

- Low on-resistance: $R_{DS (on)} = 0.48 \Omega$ typ.
- www.DataSheet U Low leakage current: $I_{DSS} = 1 \ \mu A \ max$ (at $V_{DS} = 250 \ V$)
 - High speed switching: $t_f = 11$ ns typ (at $V_{GS} = 10$ V, $V_{DD} = 125$ V, $I_D = 3.5$ A)
 - Low gate charge: Qg = 13 nC typ (at $V_{DD} = 200 \text{ V}$, $V_{GS} = 10 \text{ V}$, $I_D = 7 \text{ A}$)
 - Avalanche ratings

Outline





Absolute Maximum Ratings

			(Ta = 25°C)
Item	Symbol	Value	Unit
Drain to source voltage	V _{DSS}	250	V
Gate to source voltage	V _{GSS}	±30	V
Drain current	ID	7	А
Drain peak current	I _{D (pulse)} Note 1	28	А
Body-drain diode reverse drain current	I _{DR}	7	А
Body-drain diode reverse drain peak current	Note 1 DR (pulse)	28	А
Avalanche current	AP Note 3	7	А
Channel dissipation	Pch Note 2	30	W
Channel to case thermal Impedance	θ ch-c	4.17	°C/W
Channel temperature	Tch	150	٥°
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. Value at Tc = 25°C

3. Tch \leq 150°C

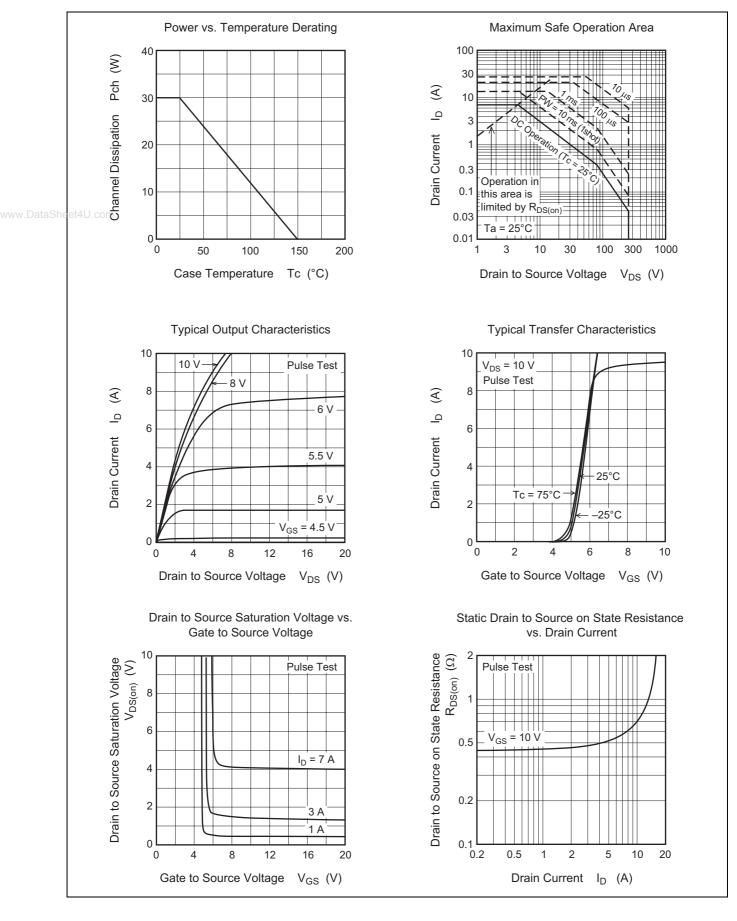
Electrical Characteristics

						(Ta = 25°C)
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V (BR) DSS	250	—	—	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	—	±0.1	μΑ	$V_{GS} = \pm 30$ V, $V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	—	1	μΑ	$V_{DS} = 250 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	V _{GS (off)}	3.0	—	4.5	V	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$
Static drain to source on state resistance	R _{DS (on)}	_	0.48	0.63	Ω	$I_D = 3.5 \text{ A}, V_{GS} = 10 \text{ V}^{Note 4}$
Forward transfer admittance	y _{fs}	3.0	5.0	—	S	$I_D = 3.5 \text{ A}, V_{DS} = 10 \text{ V}^{Note 4}$
Input capacitance	Ciss		450	_	pF	V _{DS} = 25 V
Output capacitance	Coss		60	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss		12	_	pF	f = 1 MHz
Turn-on delay time	t _{d (on)}		19	_	ns	V_{DD} = 125 V, I_{D} = 3.5 A
Rise time	tr		14	_	ns	V _{GS} = 10 V
Turn-off delay time	t _{d (off)}		47	_	ns	R _L = 35.7 Ω
Fall time	t _f		11		ns	Rg = 10 Ω
Total gate charge	Qg		13	_	nC	V _{DD} = 200 V
Gate to source charge	Qgs		2.5		nC	V _{GS} = 10 V
Gate to drain charge	Qgd		6		nC	I _D = 7 A
Body-drain diode forward voltage	V _{DF}		0.9	1.4	V	$I_F = 7 \text{ A}, V_{GS} = 0$
Body-drain diode reverse recovery time	t _{rr}		100		ns	$I_F = 7 \text{ A}, V_{GS} = 0$
Body-drain diode reverse recovery charge	Q _{rr}	—	0.38	—	μC	di _F /dt = 100 A/µs

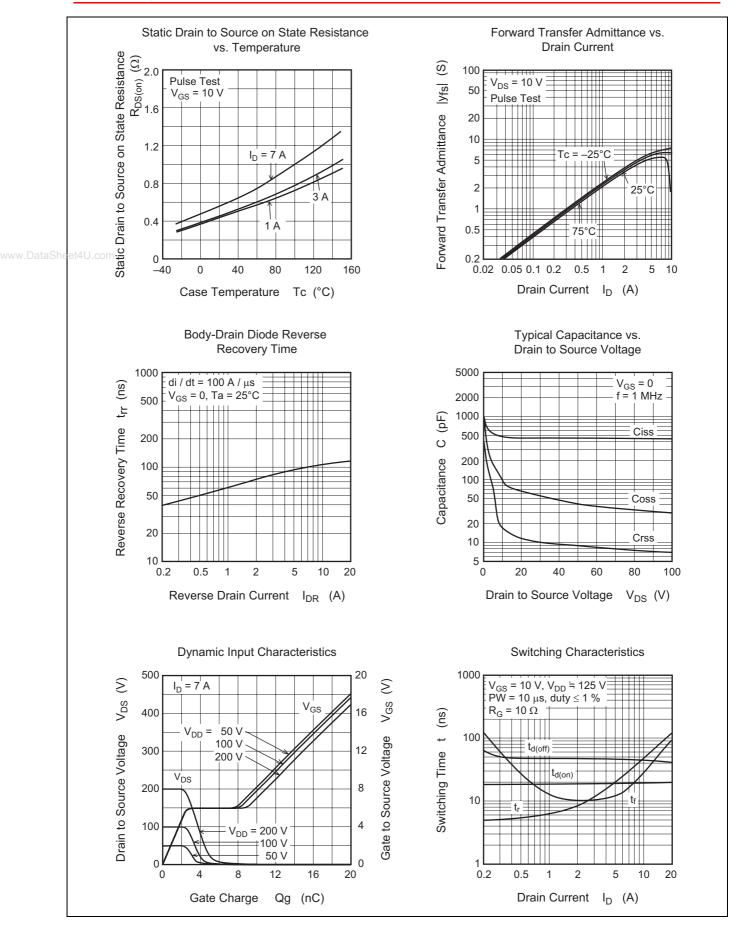
Note: 4. Pulse test

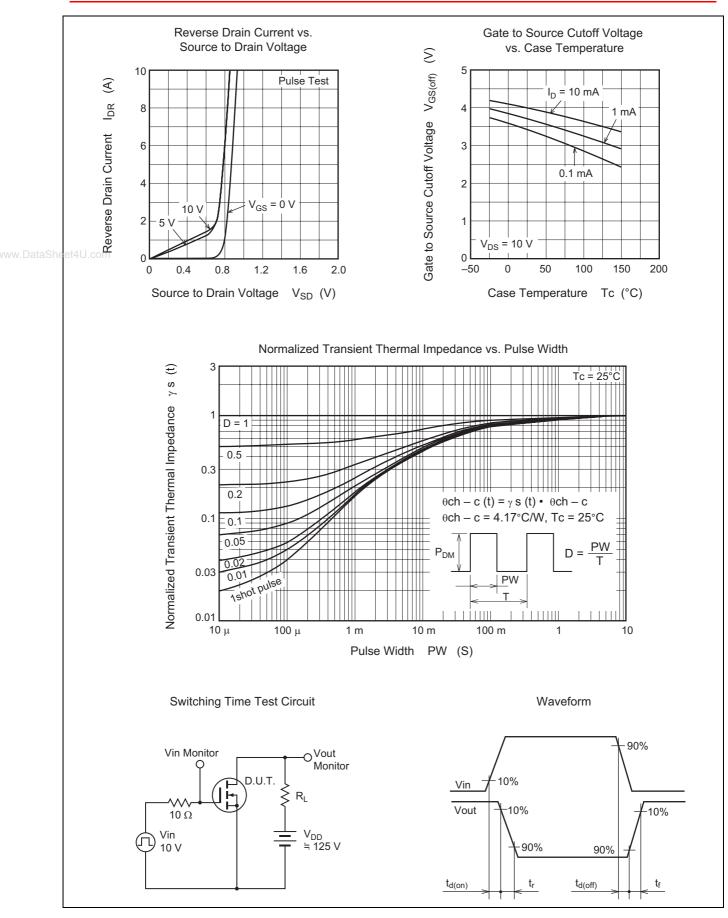


Main Characteristics



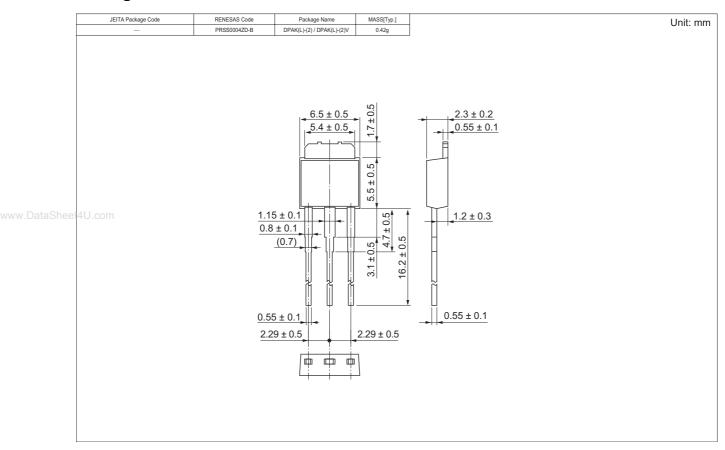


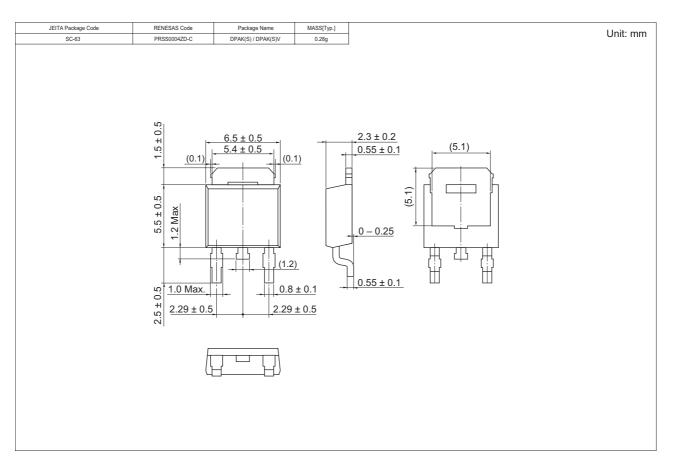






Package Dimensions







Ordering Information

Part Name	Quantity	Shipping Container
H5N2508DL-E	3200 pcs	Box (Sack)
H5N2508DSTL-E	3000 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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