

# H7N0312AB

# Silicon N Channel MOS FET High Speed Power Switching

REJ03G1127-0400 (Previous: ADE-208-1571B)

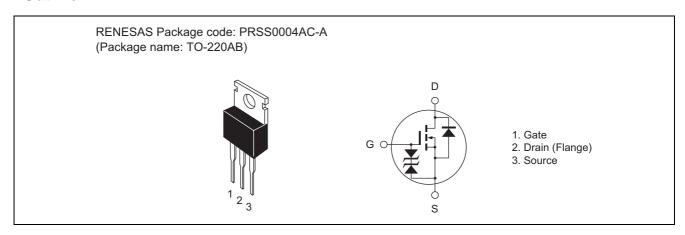
Rev.4.00

Sep 07, 2005

## **Features**

- Low on-resistance  $R_{DS (on)} = 2.6 \text{ m}\Omega \text{ typ.}$
- Low drive current
- 4.5 V gate drive device can be driven from 5 V source

## **Outline**



# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	30	V
Gate to source voltage	$V_{GSS}$	±20	V
Drain current	I <sub>D</sub>	85	Α
Drain peak current	I <sub>D (pulse)</sub> Note 1	340	Α
Body-drain diode reverse drain current	I <sub>DR</sub>	85	Α
Channel dissipation	Pch Note 2	125	W
Channel to case thermal impedance	θ ch-c	1.0	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Value at Tc = 25°C

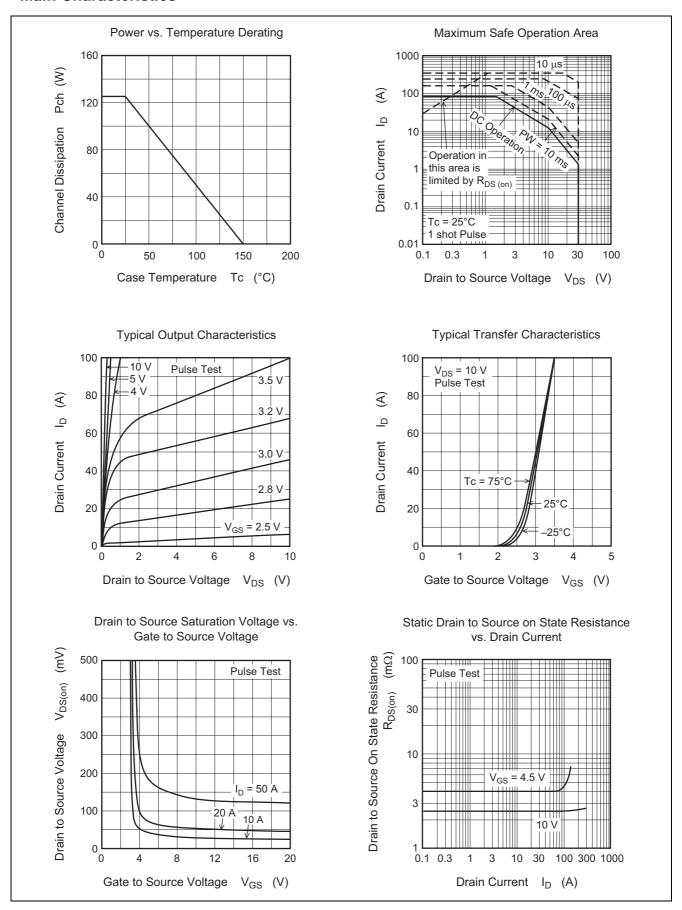
# **Electrical Characteristics**

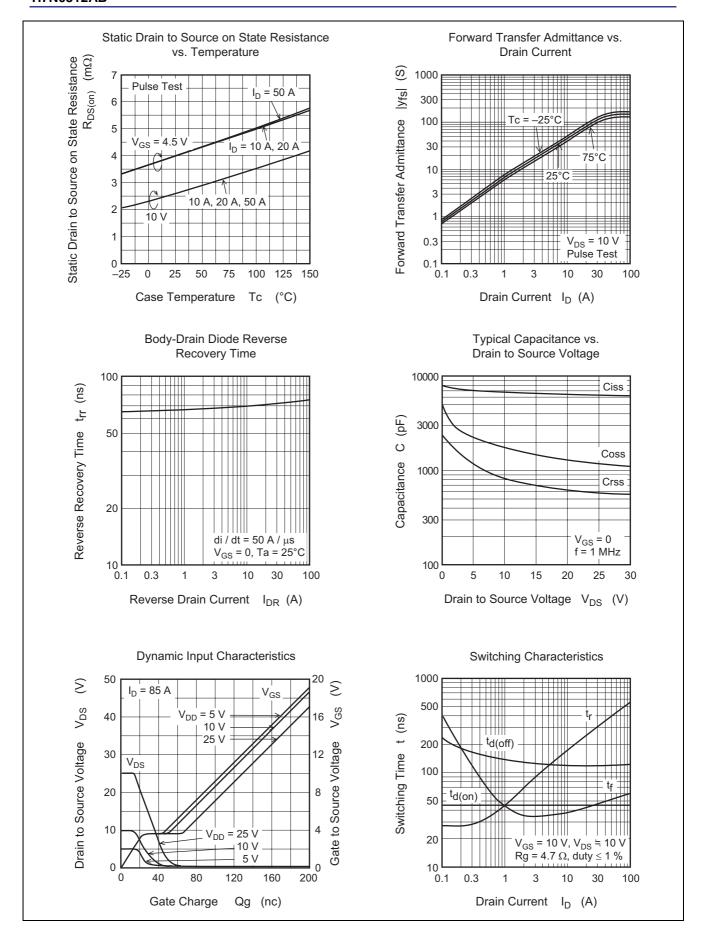
 $(Ta = 25^{\circ}C)$ 

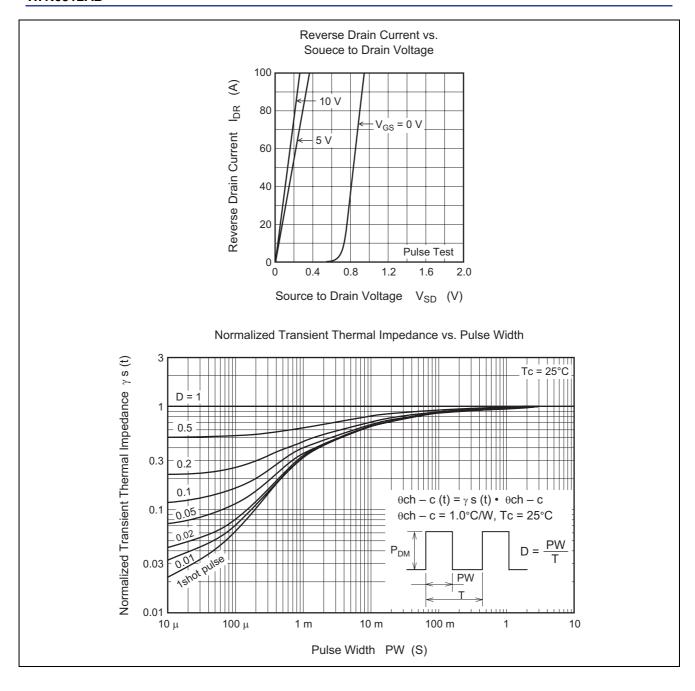
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR) DSS</sub>	30	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V <sub>(BR) GSS</sub>	±20	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	10	μΑ	$V_{DS} = 30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	V <sub>GS (off)</sub>	1.0	_	2.5	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}^{\text{Note 3}}$
Static drain to source on state resistance	R <sub>DS (on)</sub>	_	2.6	3.3	mΩ	$I_D = 42.5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note 3}}$
		_	4.0	5.8	mΩ	$I_D = 42.5 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note 3}}$
Forward transfer admittance	y <sub>fs</sub>	75	125	_	S	$I_D = 42.5 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note 3}}$
Input capacitance	Ciss	_	6900	_	pF	V <sub>DS</sub> = 10 V
Output capacitance	Coss	_	1750	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	820	_	pF	f = 1 MHz
Total gate charge	Qg	_	115	_	nC	V <sub>DD</sub> = 10 V
Gate to source charge	Qgs	_	24	_	nC	V <sub>GS</sub> = 10 V
Gate to drain charge	Qgd	_	24	_	nC	I <sub>D</sub> = 85 A
Turn-on delay time	t <sub>d (on)</sub>	_	45	_	ns	$V_{GS} = 10 \text{ V}, I_D = 42.5 \text{ A}$
Rise time	t <sub>r</sub>	_	380	_	ns	$R_L = 0.24 \Omega$
Turn-off delay time	t <sub>d (off)</sub>	_	125	_	ns	$Rg = 4.7 \Omega$
Fall time	t <sub>f</sub>	_	50	_	ns	
Body-drain diode forward voltage	$V_{DF}$	_	0.92	_	V	I <sub>F</sub> = 85 A, V <sub>GS</sub> = 0
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	75	_	ns	I <sub>F</sub> = 85 A, V <sub>GS</sub> = 0
						$di_F/dt = 50 A/\mu s$

Note: 3. Pulse test

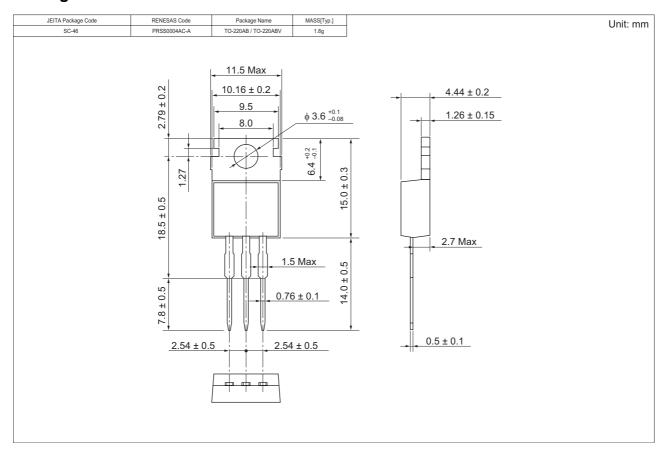
## **Main Characteristics**







# **Package Dimensions**



# **Ordering Information**

Part Name	Quantity	Shipping Container
H7N0312AB-E	500 pcs	Box (Sack)

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## Renesas Technology Malaysia Sdn. Bhd.

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