

HA12441V

Narrow Band Width FM-IF

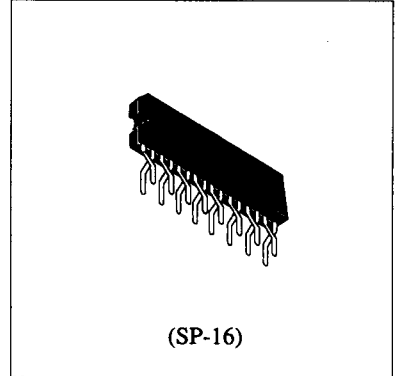
The HA12441V is an IC designed for narrow band width FM-IF. It provides the following functions and features.

Functions

- Local Oscillator
- Mixer
- IF Amplifier
- FM Detector
- Electric Field-strength Detector
- Operational Amp (Filter Amplifier)
- Squelch Trigger

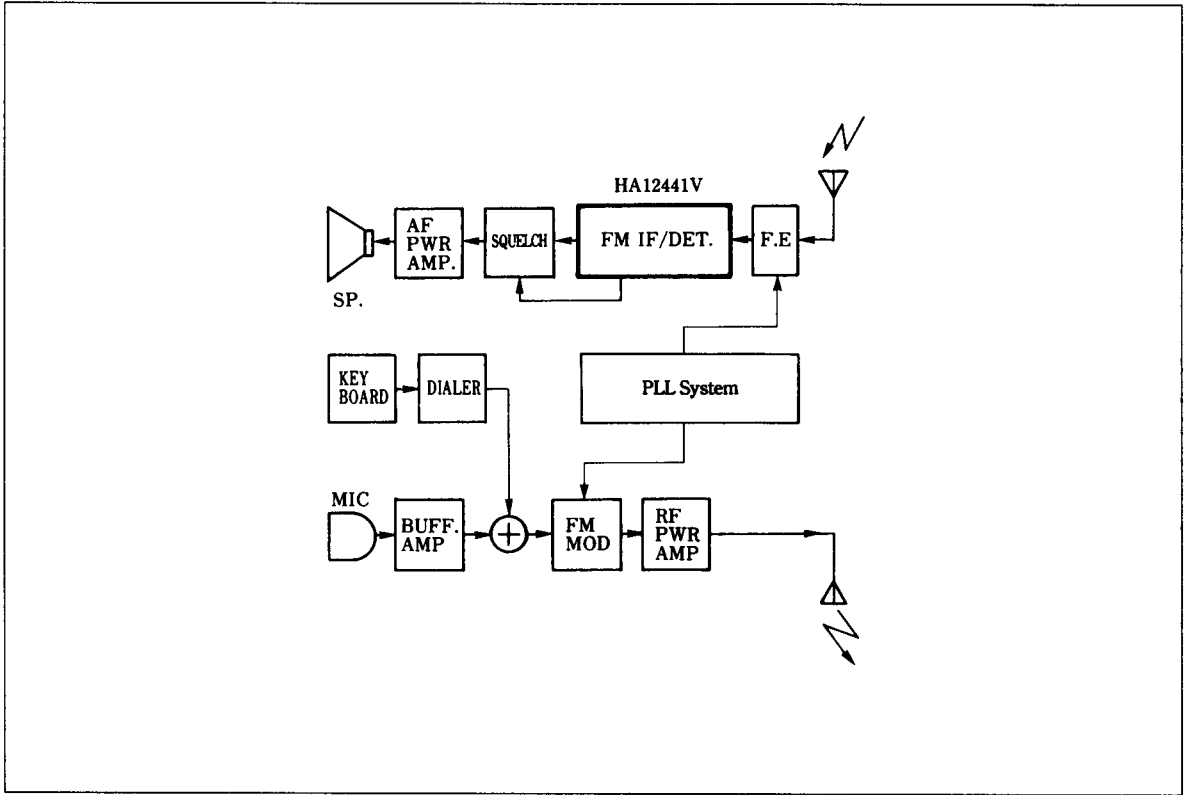
Features

- The smaller sized P.C.B. by applying vertical type package
- Wide range supply voltage operation
- Small quiescent current (4.2 mA typ)
- Small external parts count
- Possible to use both the noise squelch by the operational amplifier and electric field-strength squelch
- Small electrical characteristics change to supply voltage change
- The HA12442V pin compatible with this device for operation over frequency specification range of 10.7 MHz to 58 MHz

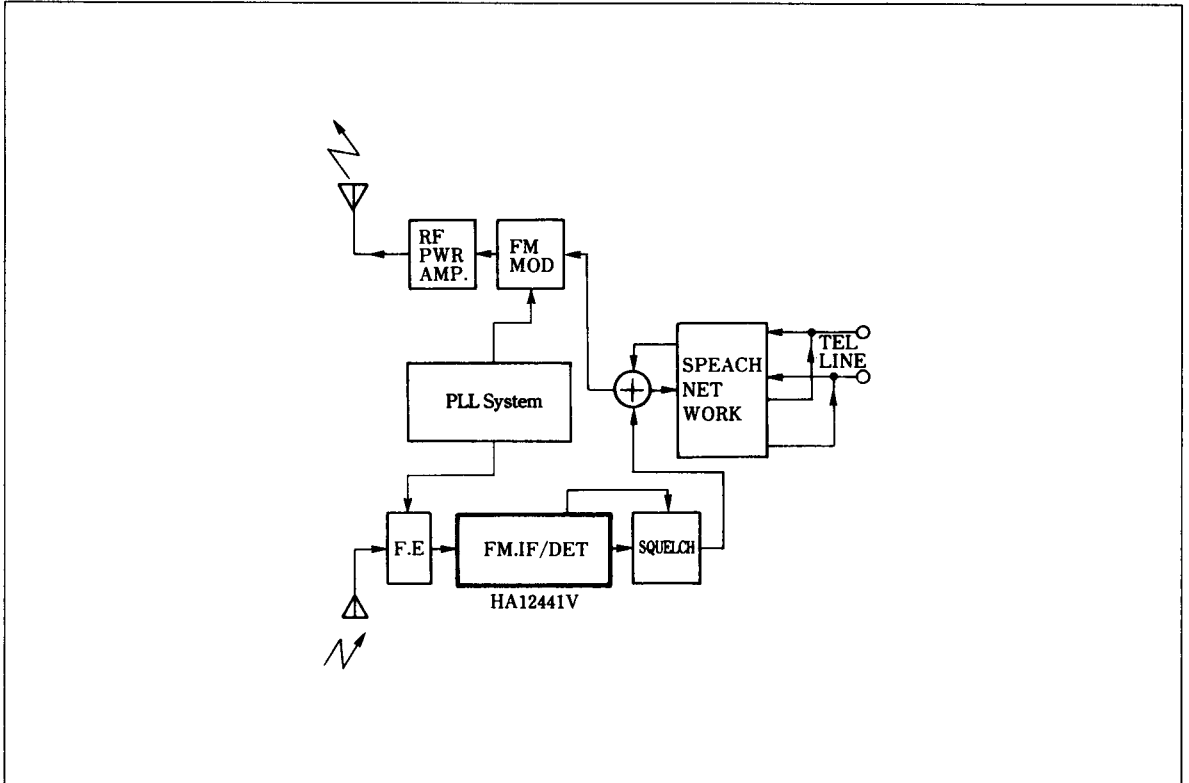


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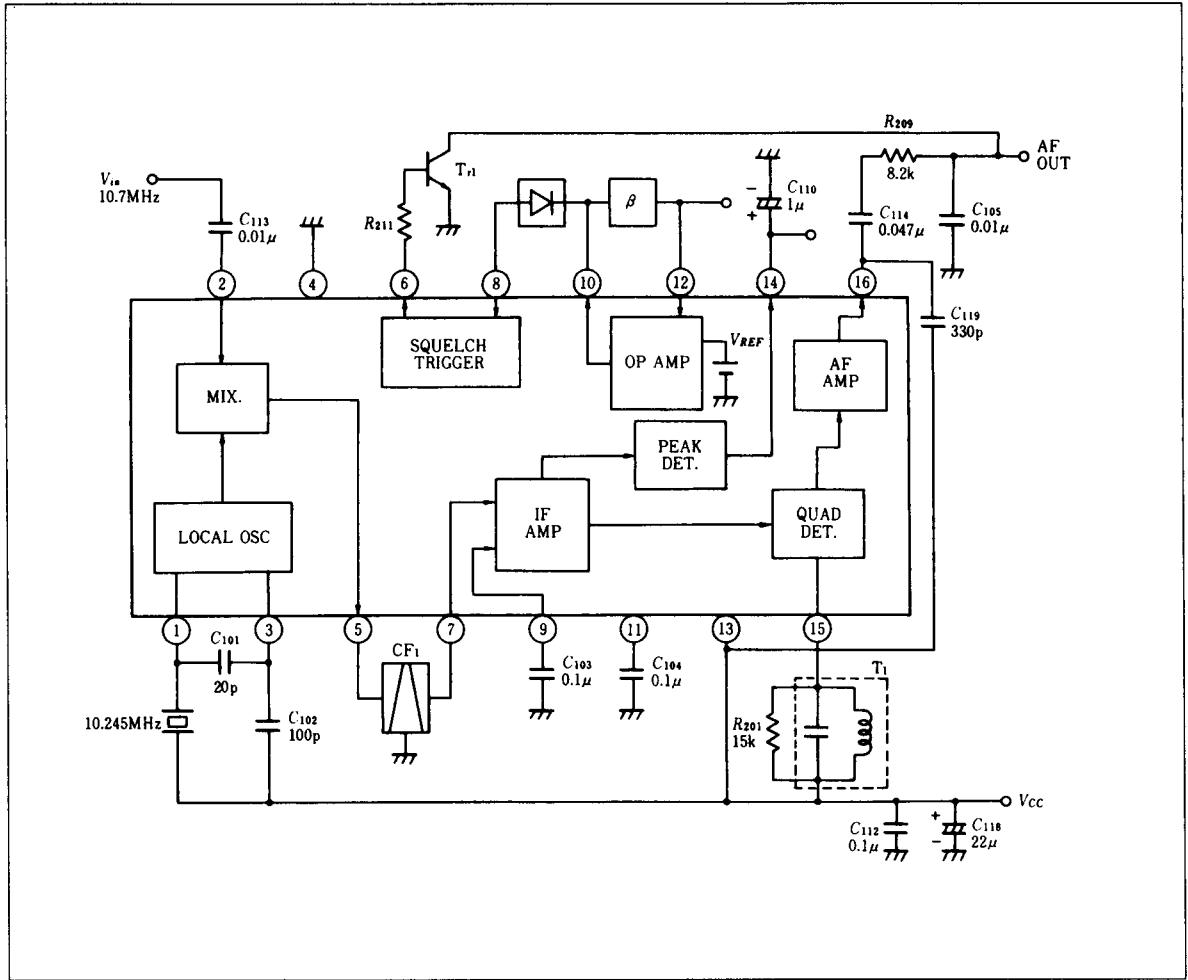
Cordless Telephone Block-Diagram Portable Unit



Base Unit



Block Diagram



HA12441V

Absolute Maximum Ratings (Ta = 25°C, unless otherwise specified.)

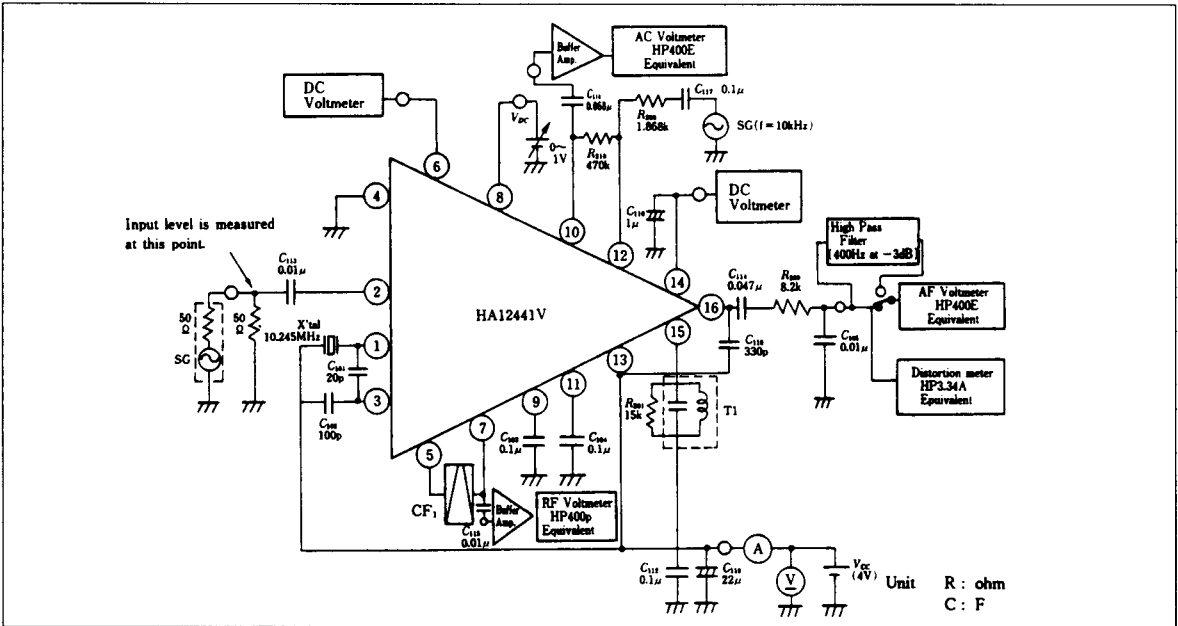
Item	Symbol	Rating	Unit	Note
Supply Voltage	Vcc	8	V	
Power Dissipation	Pr	100	mW	Ta = 75°C
Operating Temperature Range	Topt	-30 to +75	°C	
Storage Temperature Range	Tstg	-55 to +125	°C	

Electrical Characteristics (Ta = 25°C, Vcc = 4 V, fc = 10.7 MHz, fm = 1 kHz, Δf = 3 KHz, and Vin = 100 dBμ unless otherwise specified.)

Adjust the core of T₁ to minimize distortion under the above conditions.

Item	Symbol	Test Condition	Min	Typ	Max	Unit
Quiescent Current	Squelch OFF	Icc (1) no input	—	4.2	5.4	mA
	Squelch ON	Icc (2) no input	—	4.6	6.3	mA
Limiting Sensitivity	Vin (lim)	at the point of -3 dB Vo (AF)	—	0	6	dBu
Recovered AF Voltage	Vo (AF)	Vin = 100 dBμ	110	160	210	mV
Mixer Gain	Gv (Mix)	Vin = 60 dBμ	22	26	29	dB
Mixer Input Impedance	Zin (Mix)	DC Test	—	3	—	kΩ
Mixer Output Impedance	Zout (Mix)	DC Test	—	2.2	—	kΩ
IF Input Impedance	Zin (IF)	DC Test	—	2.2	—	kΩ
Squelch High Level Input Voltage	V6 (Hi)		—	3.9	—	V
Squelch Low Level Output Voltage	V6 (Lo)		—	0	0.2	V
Signal to Noise Ratio	S/N	Vin = 100 dBμ H.P.Filter (400 Hz at -3 dB)	58	65	—	dB
Squelch Hysteresis	HYST		50	90	130	mV
Lower Limit Operating Voltage	Vcc (-3 dB)	Detector Output at Vcc = 4 V is the reference level. Vcc when detector output drops by -3dB at supply voltage down.	—	—	2.7	V
Filter Amp Gain	Gv (amp)	Vin = 0.15 mV, f = 10 kHz	45	48	—	dB
Signal Meter Voltage	VSM (100)	Vin = 100 dBμ	1.8	2.2	2.7	V

Test Circuit

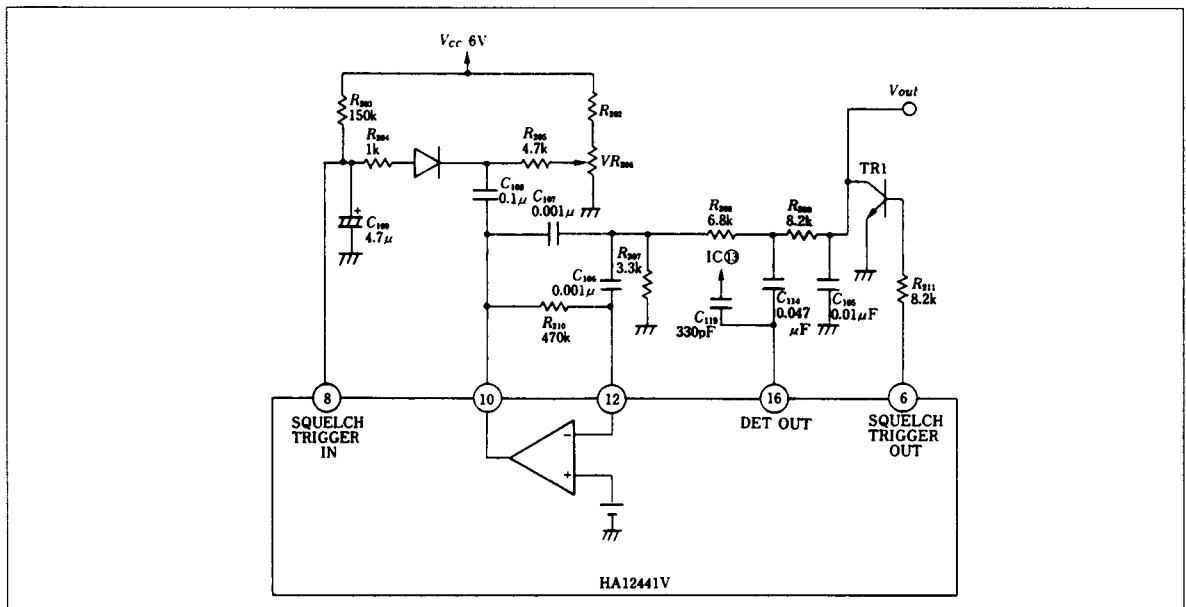


Squelch Application Circuit

The internal Op amp and squelch trigger circuit can be used to construct a noise squelch circuit. An example of the application circuit usage is shown in the following figure. The center frequency for the band

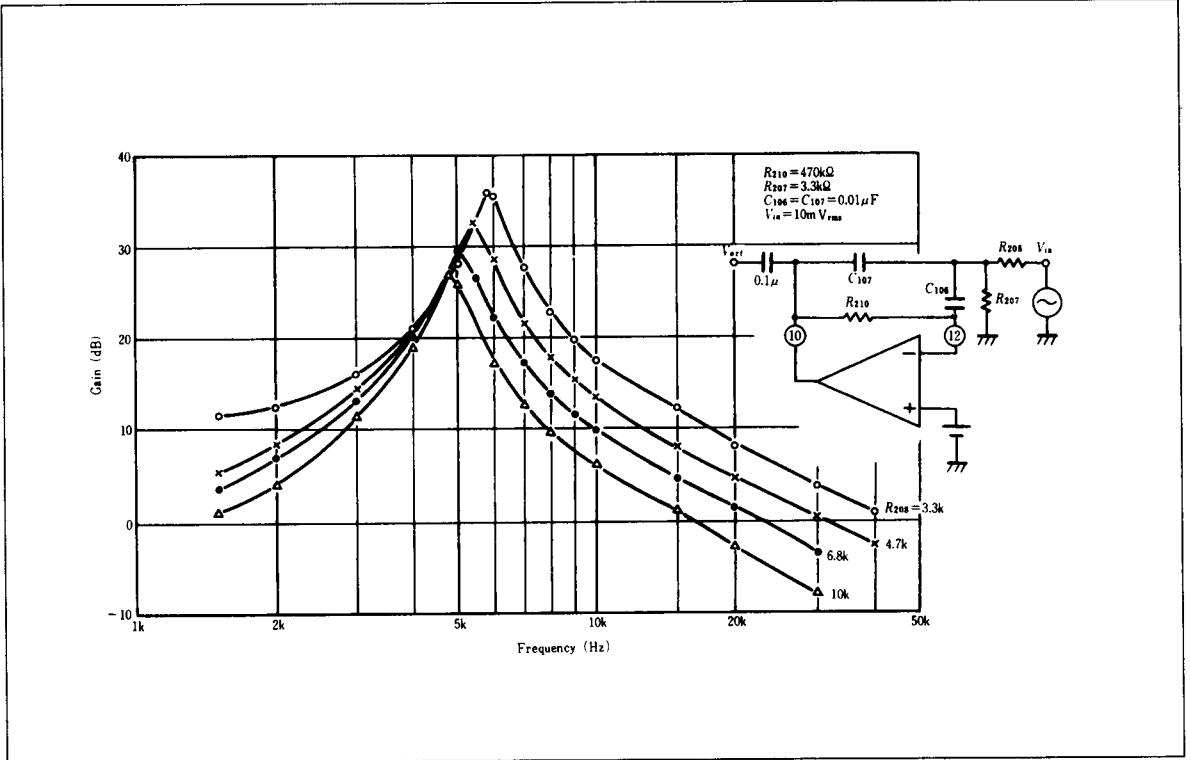
pass filter amp is chiefly determined by C106 and C107, while the gain for that amp by R208 and R210.

Noise Squelch Circuit (β Circuit for Op Amp and Noise Detector)

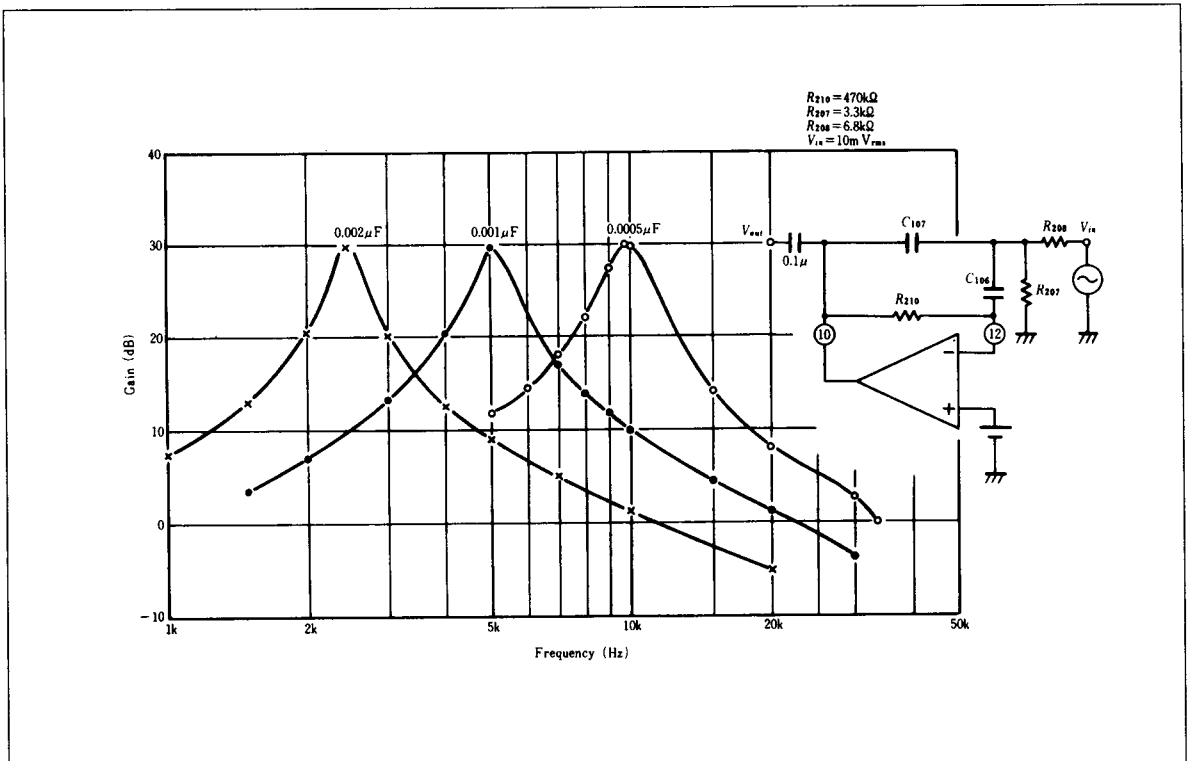


Notes: Connect C119 to Vcc line as near to Pin 13 as possible.

Filter Amplifier Frequency Characteristics

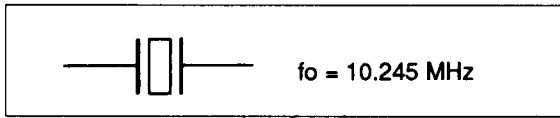


Filter Amplifier Frequency Characteristics



External Parts

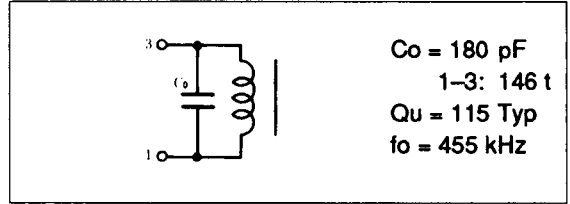
X-tal Oscillator — NIHON DANPA TYPE NO. NC18C



Ceramic Filter — MURATA TYPE NO. CFW-455E



Detector Coil — TOKO (TRIAL PRODUCT) NO. 7MC-101000Z0



Parts for resistors and capacitors are as follows.

Part No.	Recommended Value	Function	Influence	
			Greater than Recommended Value	Smaller than Recommended Value
C101	20 pF	Local Feedback Circuit	High Level Oscillation	Low Level Oscillation
C102	100 pF		Low Level Oscillation	High Level Oscillation
C103	0.1 μF	IF Amp DC Feedback Decoupling	—	Decrease in IF Gain
C104	0.1 μF			
C105	0.01 μF	Recovered AF Carrier Attenuation	Load characteristics are influenced.	Load characteristics are influenced.
C106	0.001 μF	β Circuit for Op Amp	Decrease in Center Frequency for Band Pass Filter	Increase in Center Frequency for Band Pass Filter
C107	0.001 μF			
C108	0.1 μF	Detector for Noise Squelch Circuit	—	—
C109	4.7 μF		Poor Squelch Response	—
C110	1 μF	Meter Output Decoupling	Poor Meter Response	—
C112	0.1 μF	Power Supply Bypass Capacitor	—	—
C113	0.01 μF	Input DC Cut	—	—
R201	15 k	Damping of Detector Coil	Increase in Detector Output	Decrease in Detector Output
R202	33 k	Detector for Noise Squelch Circuit	Decrease in Vth Setting	Increase in Vth Setting
R203	150 k			
R204	1 k	Setting of Squelch Vth	Poor Squelch Response	Poor Squelch Stability
R205	4.7 k		—	—
VR206	22 k		—	—
R207	3.3 k	β Circuit for Op Amp	—	—
R208	6.8 k		Decrease in Amp Gain	Increase in Amp Gain
R209	8.2 k	Recovered AF Voltage Carrier Attenuation	Frequency response is influenced.	Frequency response is influenced.
R210	470 k	β Circuit for Op Amp	Increase in Amp Gain	Decrease in Amp Gain
R211	8.2 k	Current Limitation of Squelch Transistor	Saturation of TR1 is stopped.	Poor Circuit Limiter effect
C114	0.047 μF	Recovered AF Voltage DC Cut	—	Low level frequency response is influenced.
C119	330 p	High Frequency Rejection	Poor stability	Poor S/N

TRS, DIODE

Tr1: 2SC 458

D1: 1S2076

Function Description

Comparator (Local Oscillator & Mixer)

The local oscillator and mixer are separated in this device. The former performs local oscillation by positively feeding the output on pin 3 back to pin 1 through C101. A double balance mixer is employed for the latter. The mixer yields a gain of 26 dB(typ.) (measured after the output on pin 5 passed the ceramic filter 1 (CF1)).

IF Amplifier

The IF amplifier is made up of five differential amplifiers.

The output on pin 5 from the mixer is applied to IF input pin (pin 7) through CF1. The input impedance on pin 7 is 2.2 kΩ (typ)

Detector

Quadrature detection method is employed for the detector. This detector performs FM detection by the multiplication of the signal amplified by the IF amplifier and the signal 90° phase shifted by the internal capacitor.

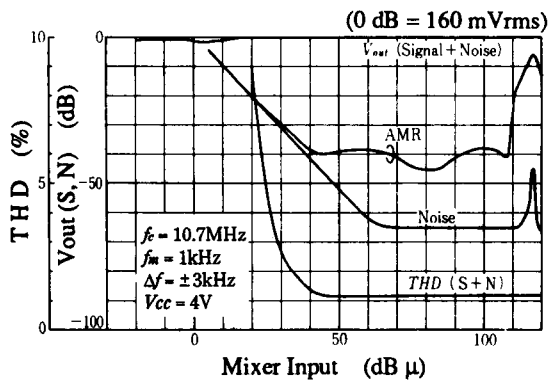
Signal Meter Driving Circuit

This device provides a method of detecting voltage peak of the signal from the IF amplifier for signal meter driving. C110 connected to pin 14 (output pin) is utilized for output voltage smoothing.

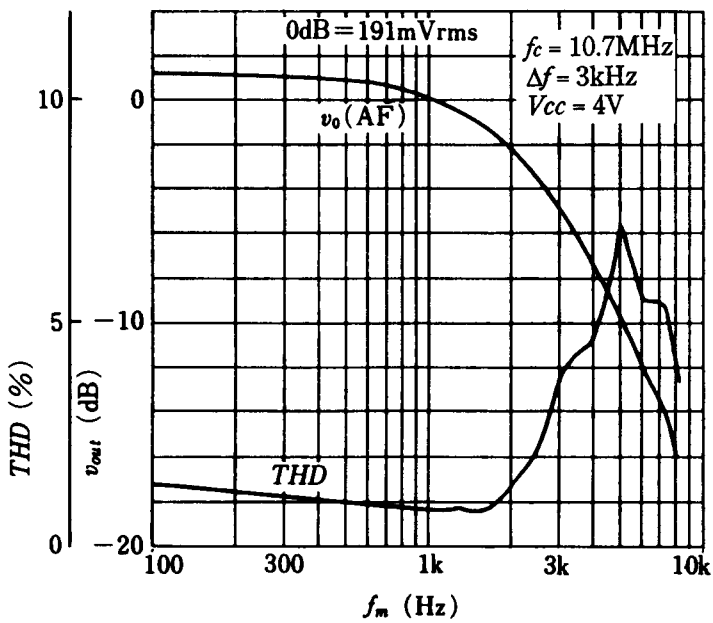
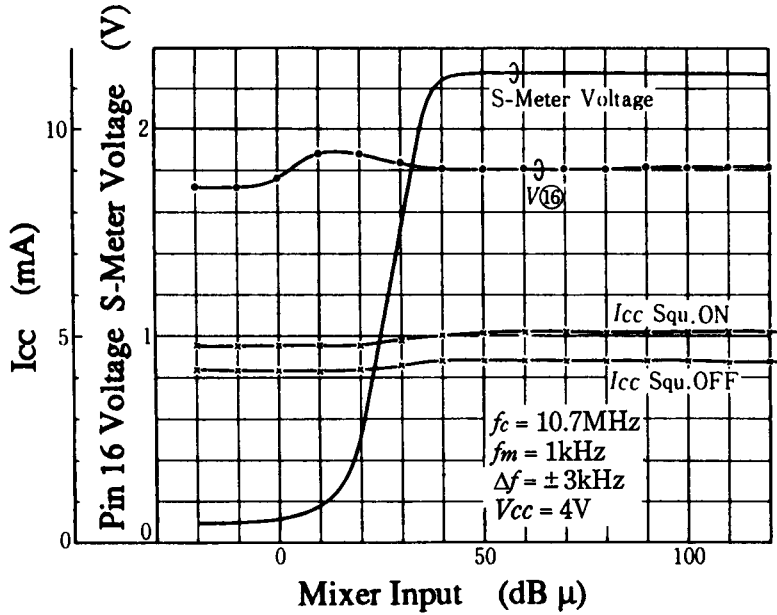
Squelch Circuit (Operational Amplifier & Squelch Trigger)

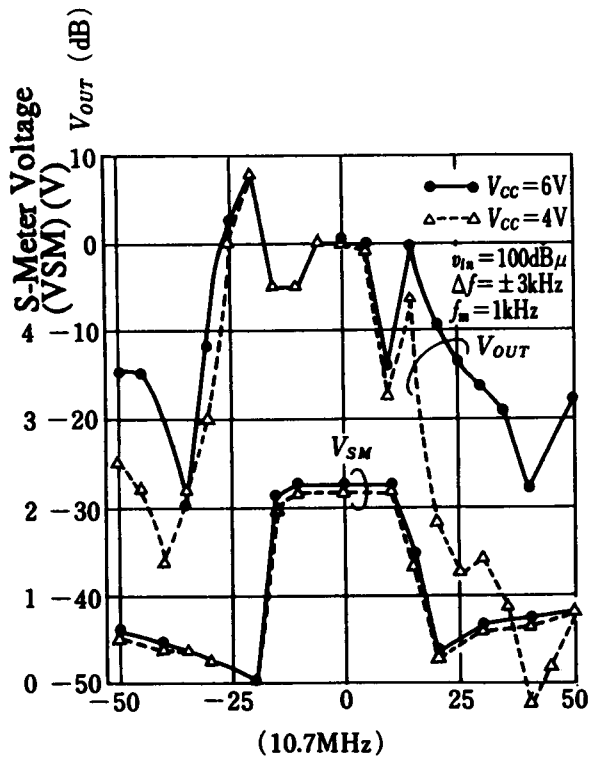
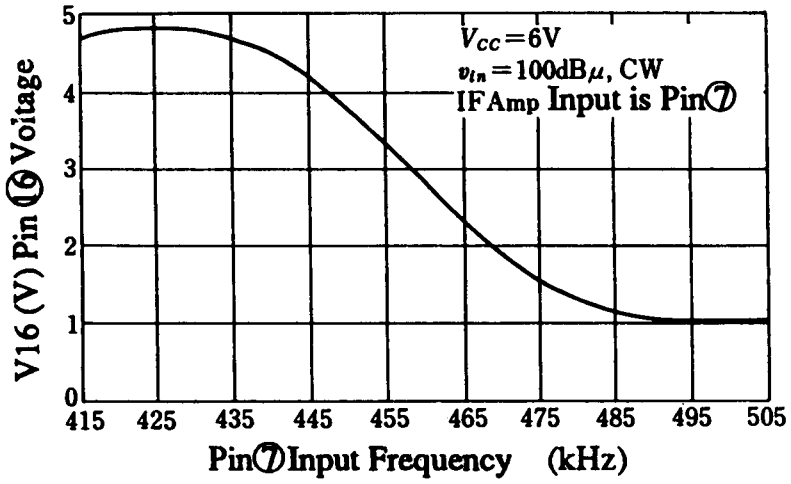
The noise squelch circuit is comprised of the internal operational amplifier and squelch trigger. The operational amplifier uses pins 12 and 10 for input and output, respectively. The filter amplifier has an input pin (pin 8) and output pin (pin 6) and causes a hysteresis of 90 mV (typ) at $V_{CC} = 4\text{ V}$.

Input/Output Characteristics

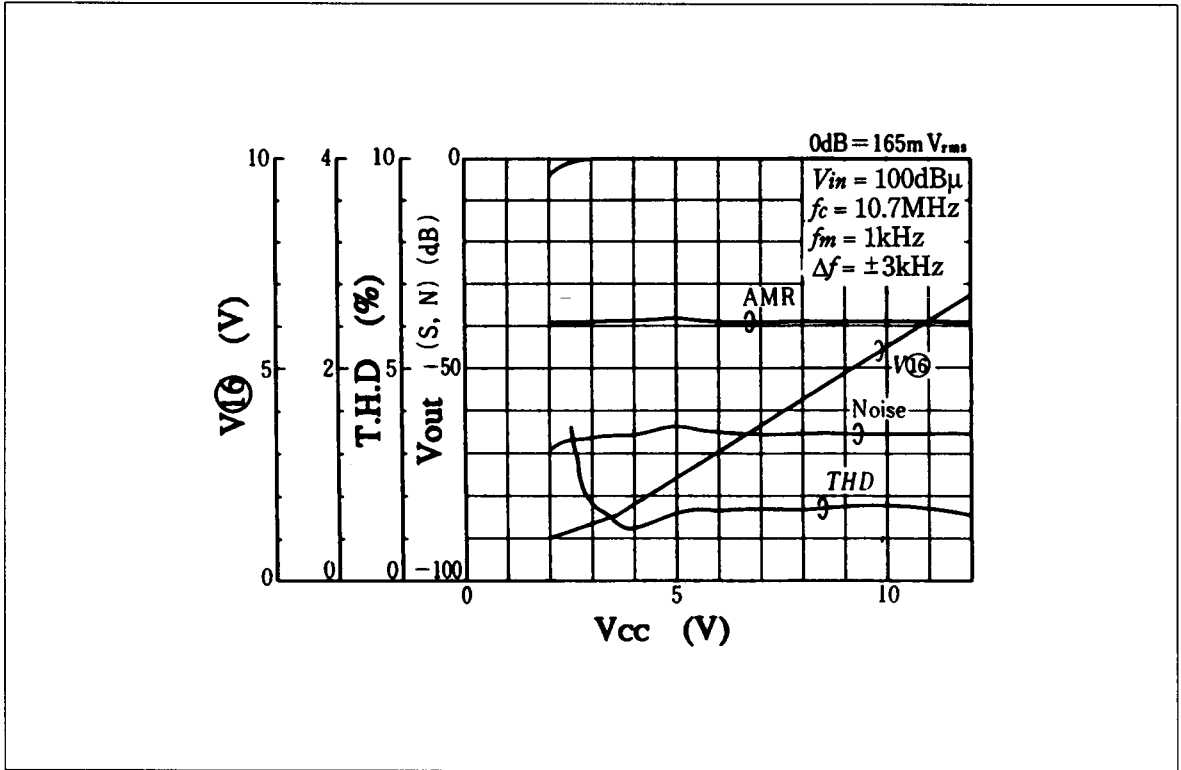


Input/Output Characteristics

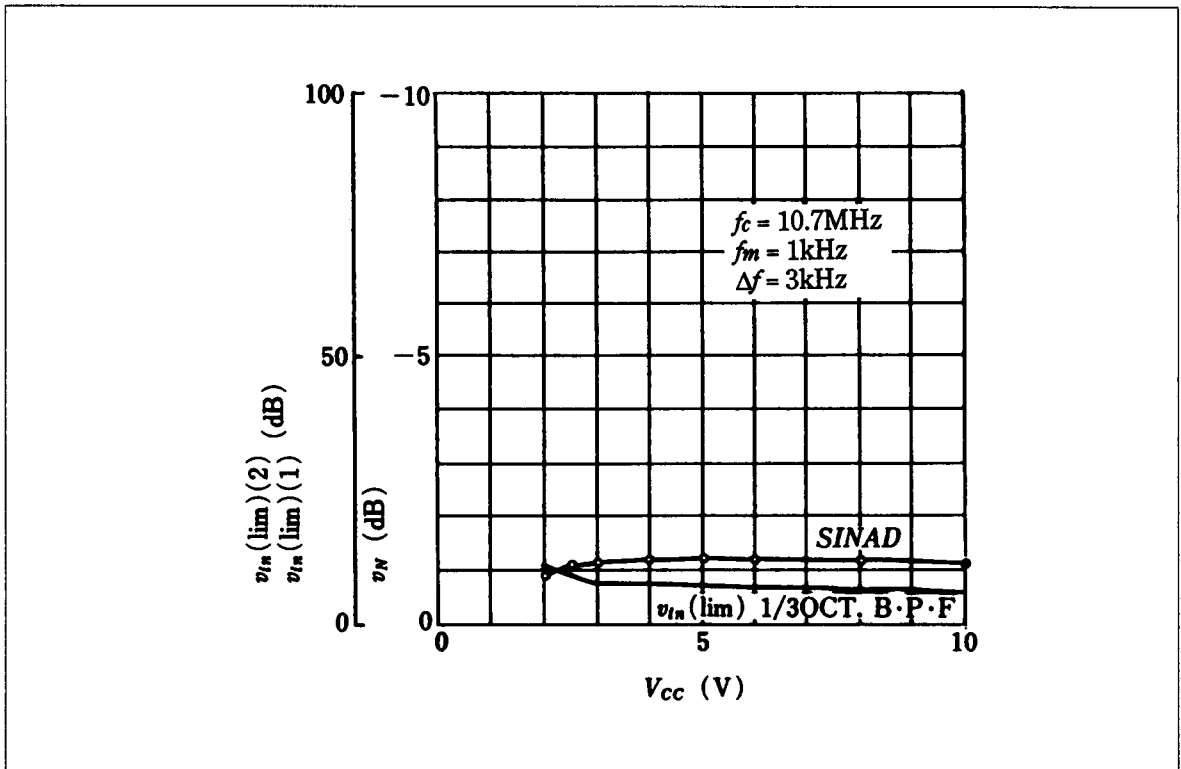




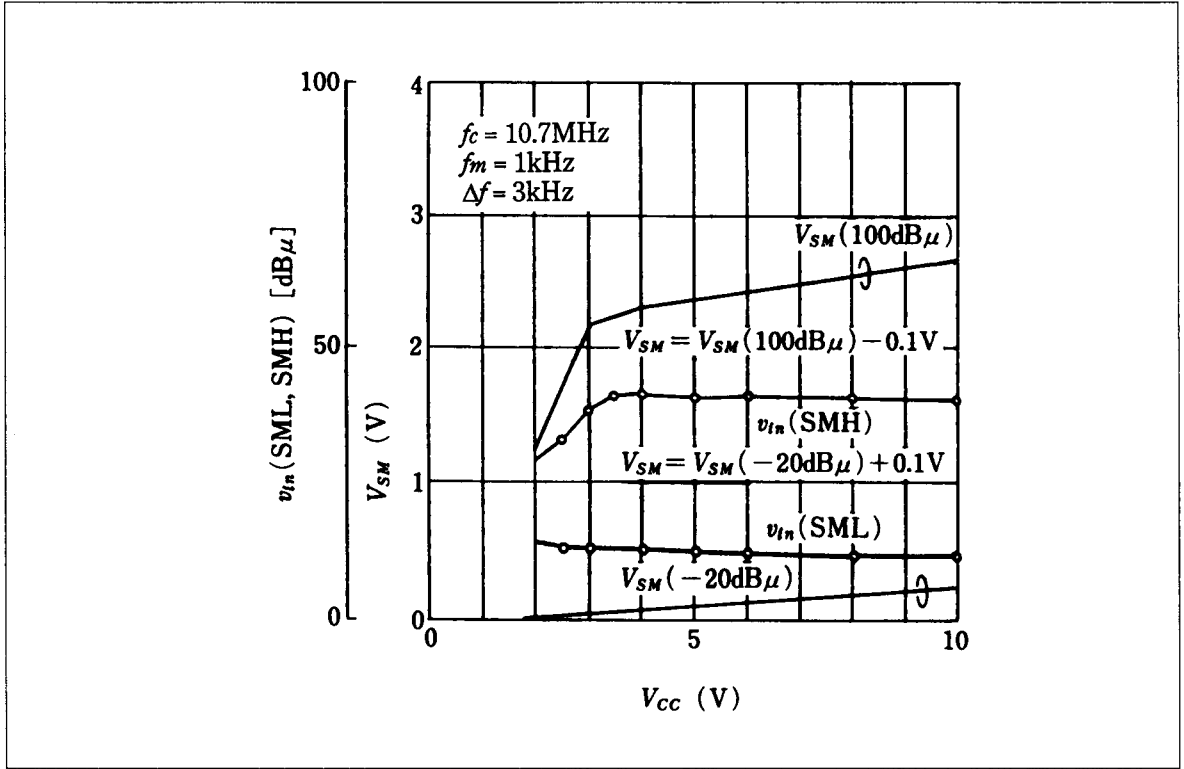
Supply Voltage Characteristics



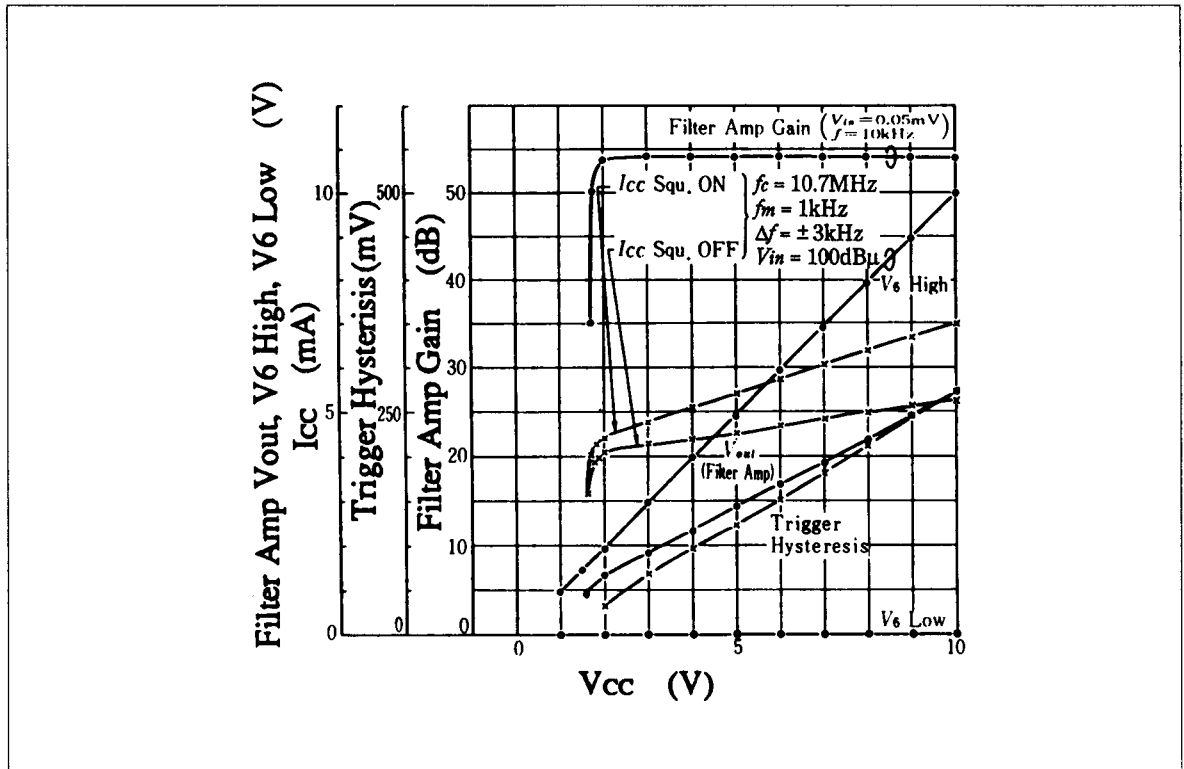
Input Sensitivity-Supply Voltage Characteristics



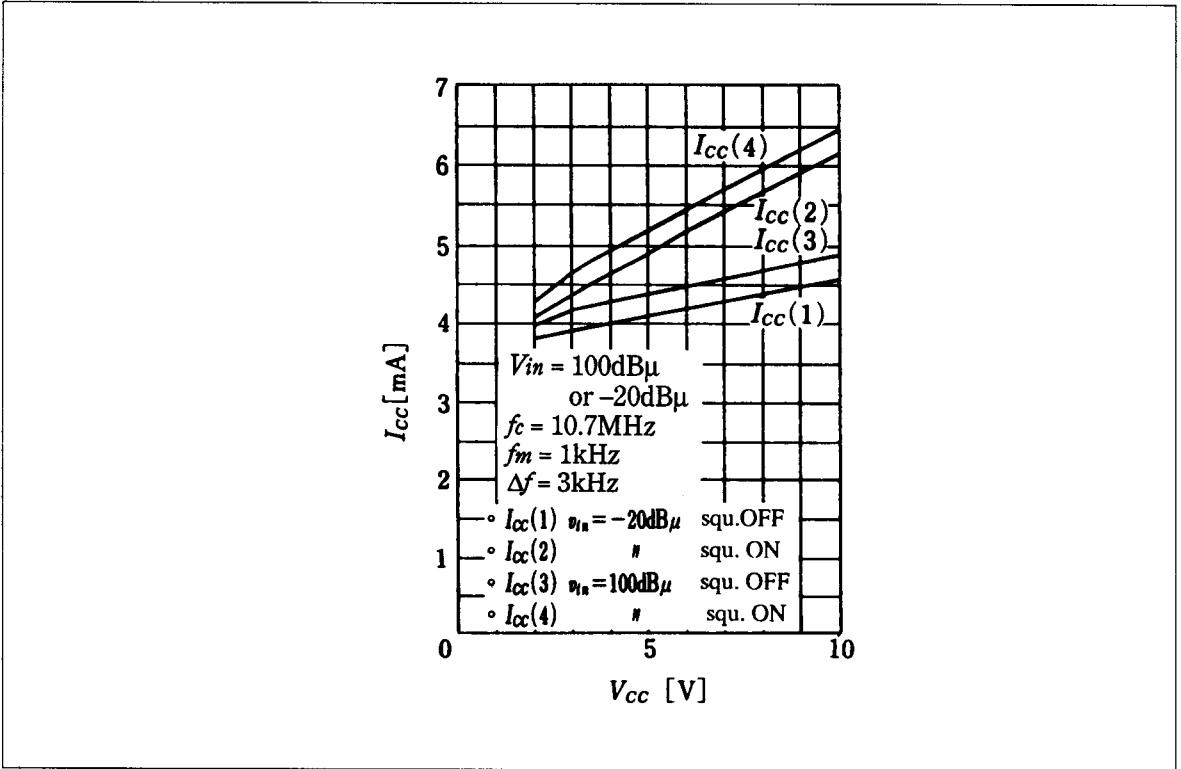
Signal Meter-Supply Voltage Characteristics



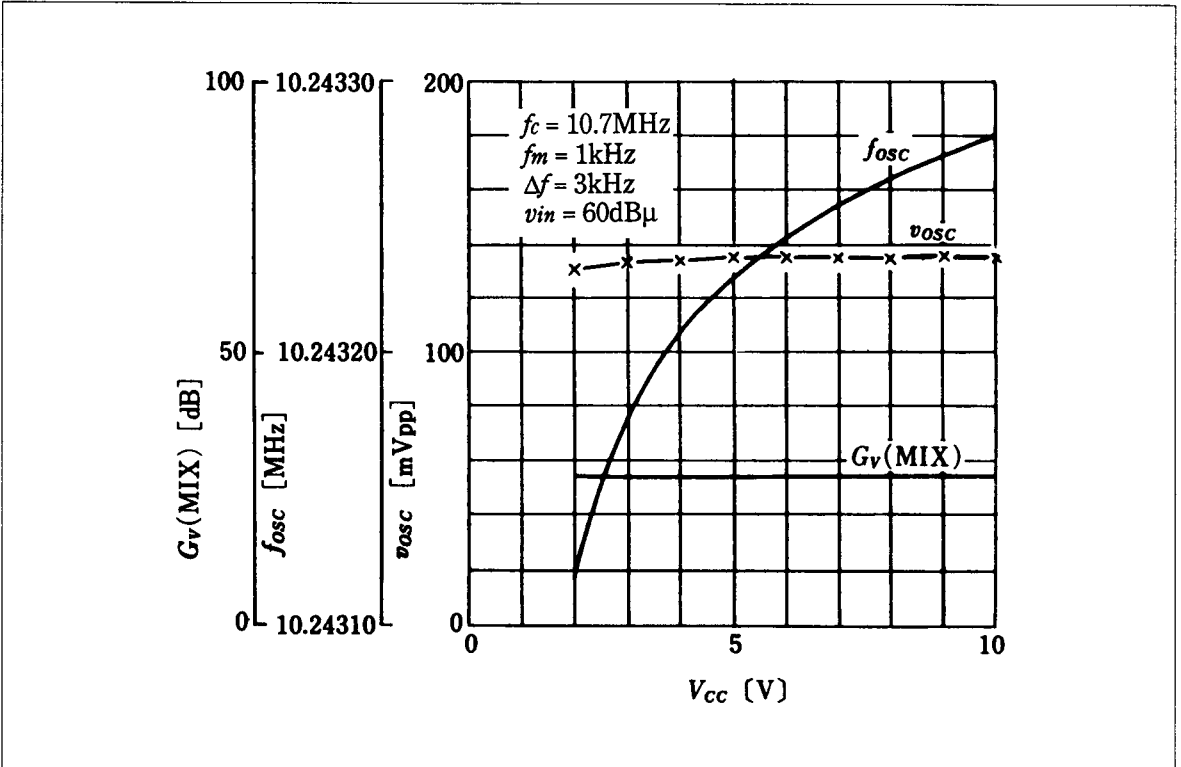
Supply Voltage Characteristics



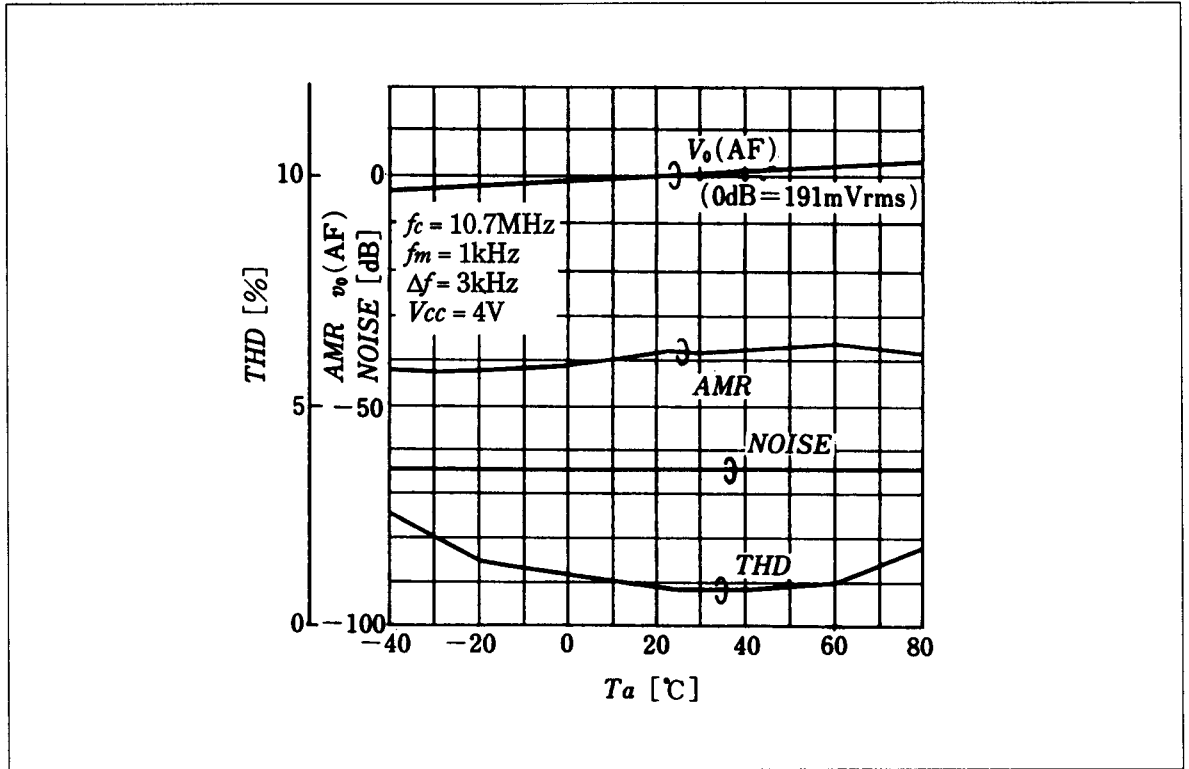
Icc-Supply Voltage Characteristics



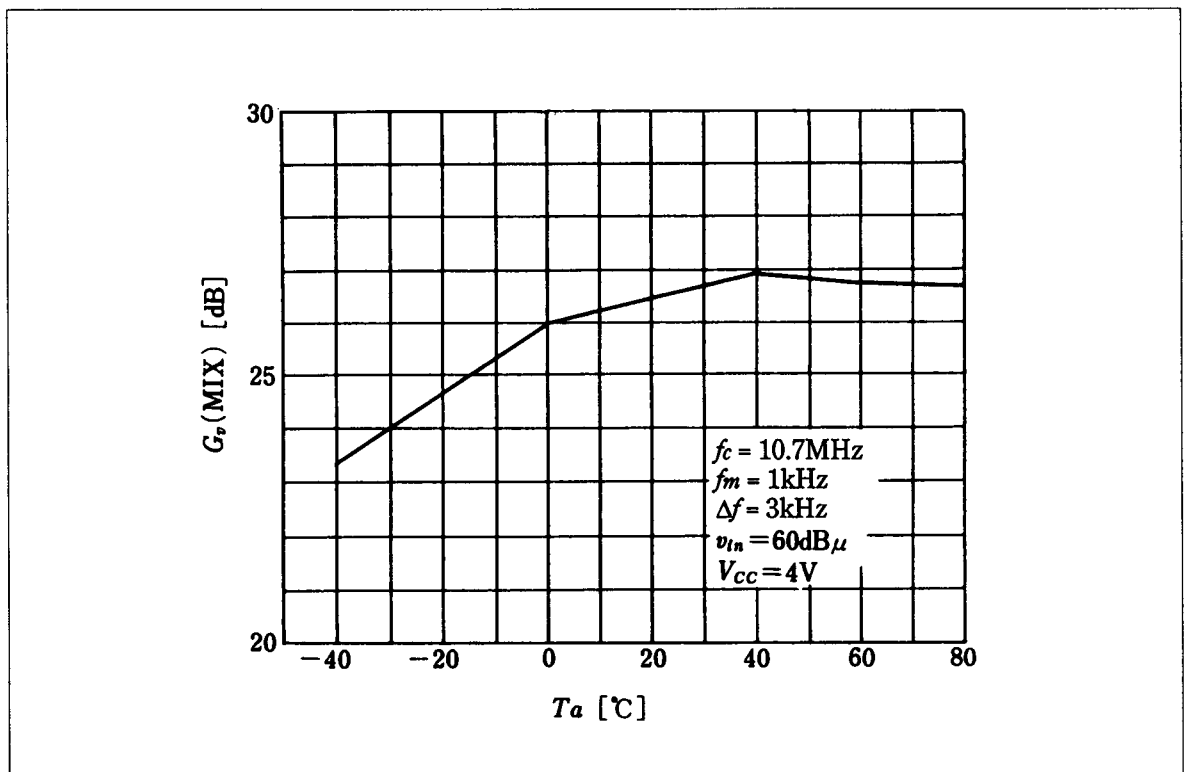
Converter Gain-Supply Voltage Characteristics



Output-Temperature Characteristics



Converter Gain-Temperature Characteristics



Input Sensitivity-Temperature Characteristics

