

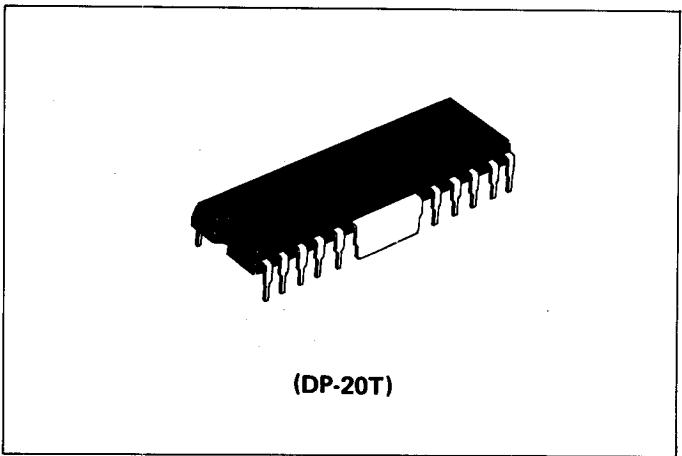
HA1367

AMPLIFIER SYSTEM FOR CASSETTE TAPE RECORDER WITH 2.2W POWER AMPLIFIER

HA1367 is an IC for tape recorders with radio which consist of a pre-amplifier, AGC circuit, a line amplifier and a BTL power amplifier. It is possible to make the audio frequency of the cassette tape recorder that turns out 2.2W output at 6V power source to operate as an IC by use of one IC of this type.

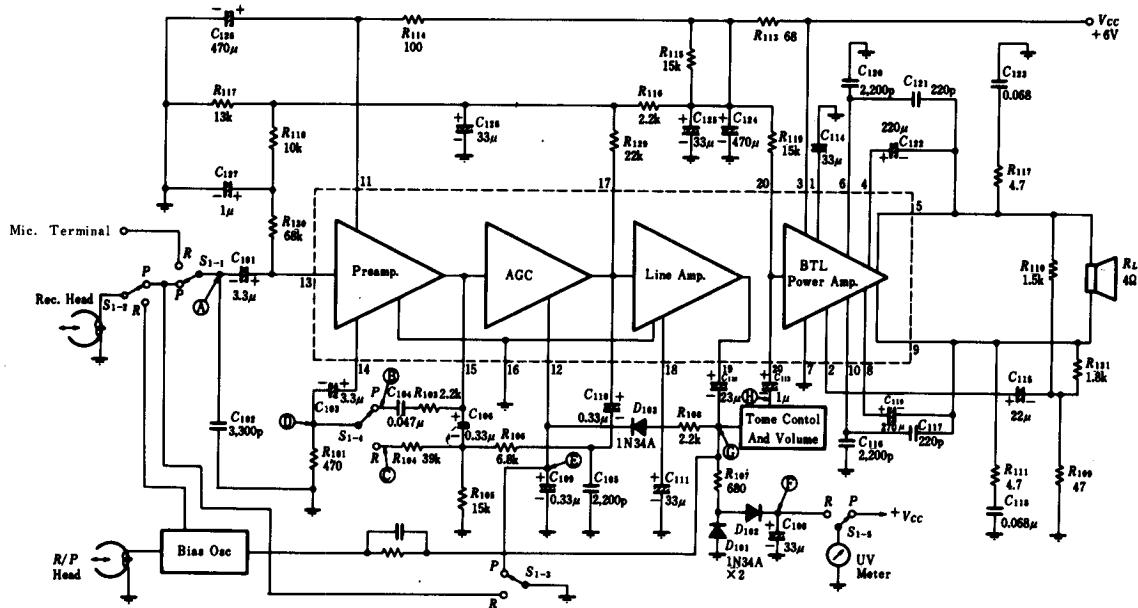
■ FEATURES

1. It is possible to turn out 2W output by 6V supply voltage using BTL circuit. (When load resistance is 4Ω)
2. The pre-amplifier, AGC circuit, line amplifier and BTL are inputed to 1 chip and thus bring about this high integrated IC.
3. Since it does not use an output transformer, the low distortion can be made even at the low and high frequencies.



(DP-20T)

■ BLOCK DIAGRAM AND TYPICAL EXTERNAL COMPONENTS



Notes: 1. S_{1-1} to S_{1-5} ; switch Rec./Play
2. Unit R: Ω , C: μ F

■ ABSOLUTE MAXIMUM RATINGS

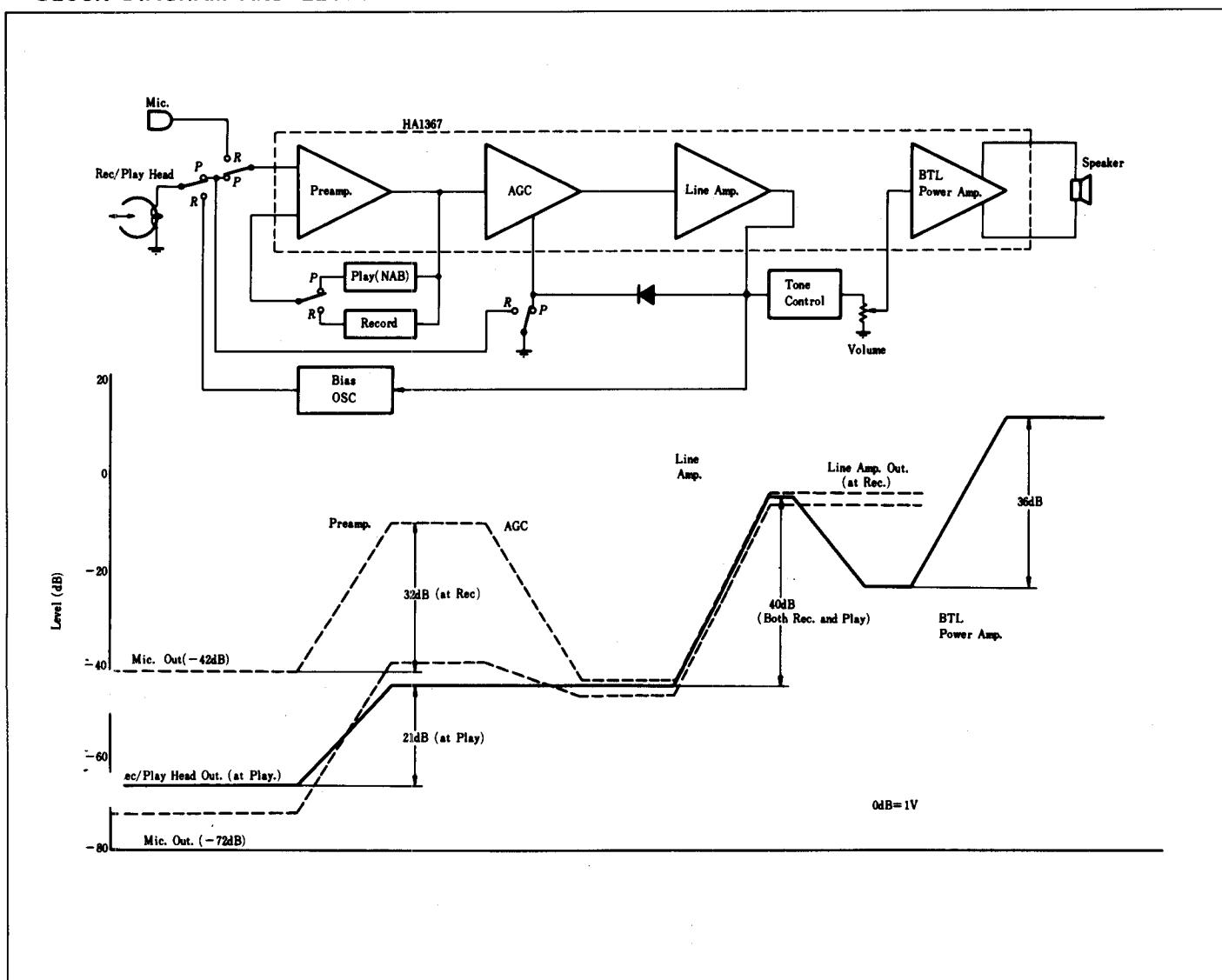
Item	Symbol	Rating	Unit
Supply Voltage	V_{CC}	9	V
Output Current	$I_{O(\text{peak})}$	2.25	A
Power Dissipation*	P_T	3.1	W
Junction Temperature	T_j	150	$^{\circ}\text{C}$
Operating Temperature	T_{opr}	-20 to +70	$^{\circ}\text{C}$
Storage Temperature	T_{stg}	-55 to +125	$^{\circ}\text{C}$

* Value at $T_a=25^{\circ}\text{C}$ ($\theta_{j-c} \leq 15^{\circ}\text{C/W}$)

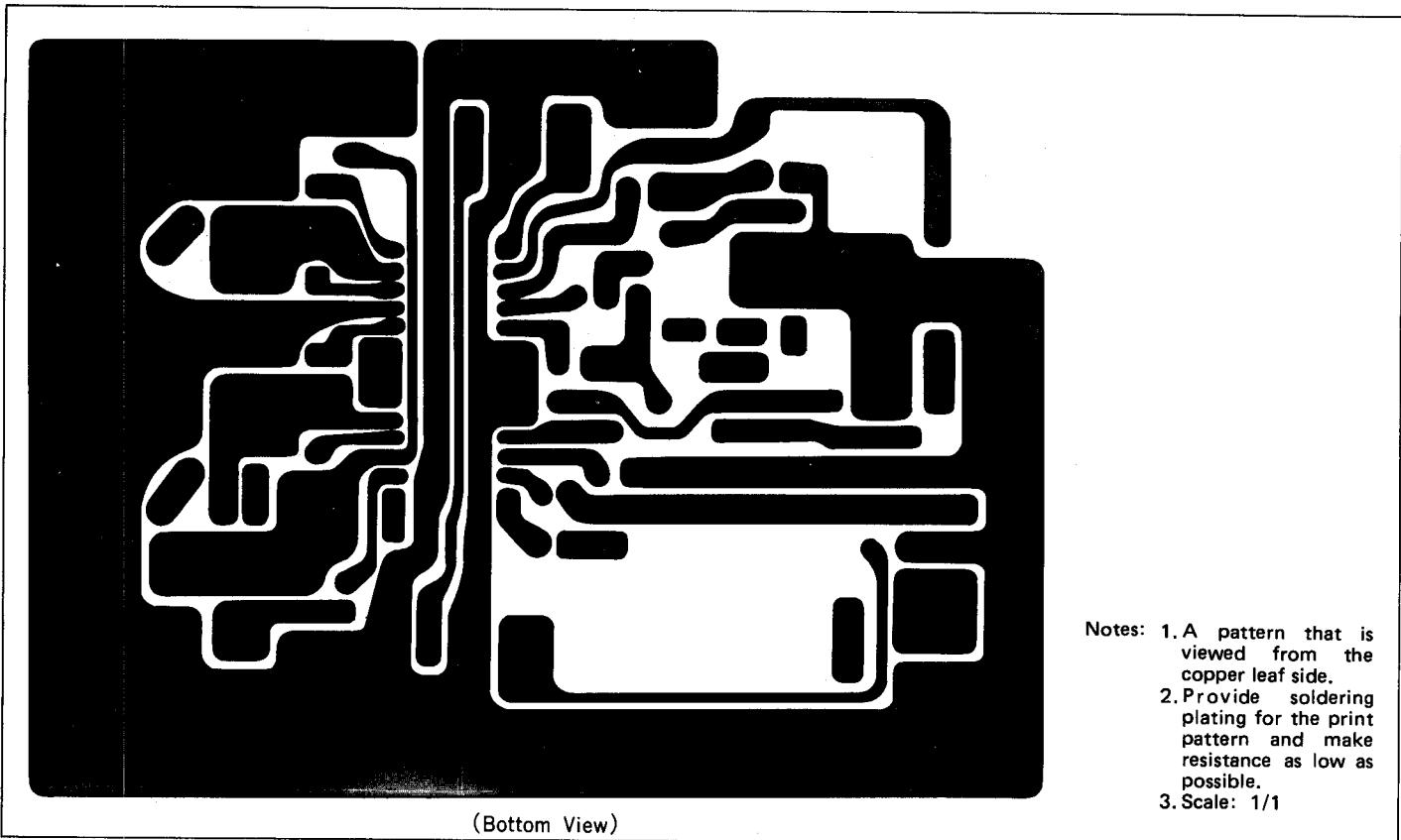
■ ELECTRICAL CHARACTERISTICS ($T_a = 25^\circ\text{C}$, $V_{cc} = 6\text{V}$, $f = 1\text{kHz}$, $R_L = 4\Omega$)

	Item	Symbol	Test Condition	min	typ	max	Unit
Pre amp	Voltage Gain	G_{v1}	$V_{in} = -50\text{dBm}$ (Rec)	—	32	—	dB
	Output Voltage	V_{out1}	$T.H.D = 1\%$	0.7	—	—	V
	Total Harmonic Distortion	THD_1	$V_{out1} = 0.5\text{V}$	—	—	1.0	%
Line amp	Voltage Gain	G_{v2}	$V_{in} = -50\text{dBm}$	—	40	—	dB
	Output Voltage	V_{out2}	$T.H.D = 1\%$	0.7	—	—	V
	Total Harmonic Distortion	THD_2	$V_{out2} = 0.5\text{V}$	—	—	1.0	%
AGC	Distortion at AGC	AGC, THD	$V_{in} = -40\text{dBm}$	—	—	3.0	%
Power amp	Voltage Gain	G_{v3}	$V_{in} = -40\text{dBm}$	—	36	—	dB
	Output Power	P_{out3}	$T.H.D_3 = 10\%$	1.9	2.2	—	W
	Total Harmonic Distortion	THD_3	$P_{out3} = 1\text{W}$	—	—	1.0	%
Total	Offset Voltage	V_{offset}	Pin 11 to 12	—	—	± 40	mV
	Output Noise Voltage	V_n		—	10	20	mV
	Quiescent Current	I_q	$V_{in} = 0$	—	50	80	mA

■ BLOCK DIAGRAM AND LEVEL DIAGRAM OF CASSETTE TAPE RECORDER

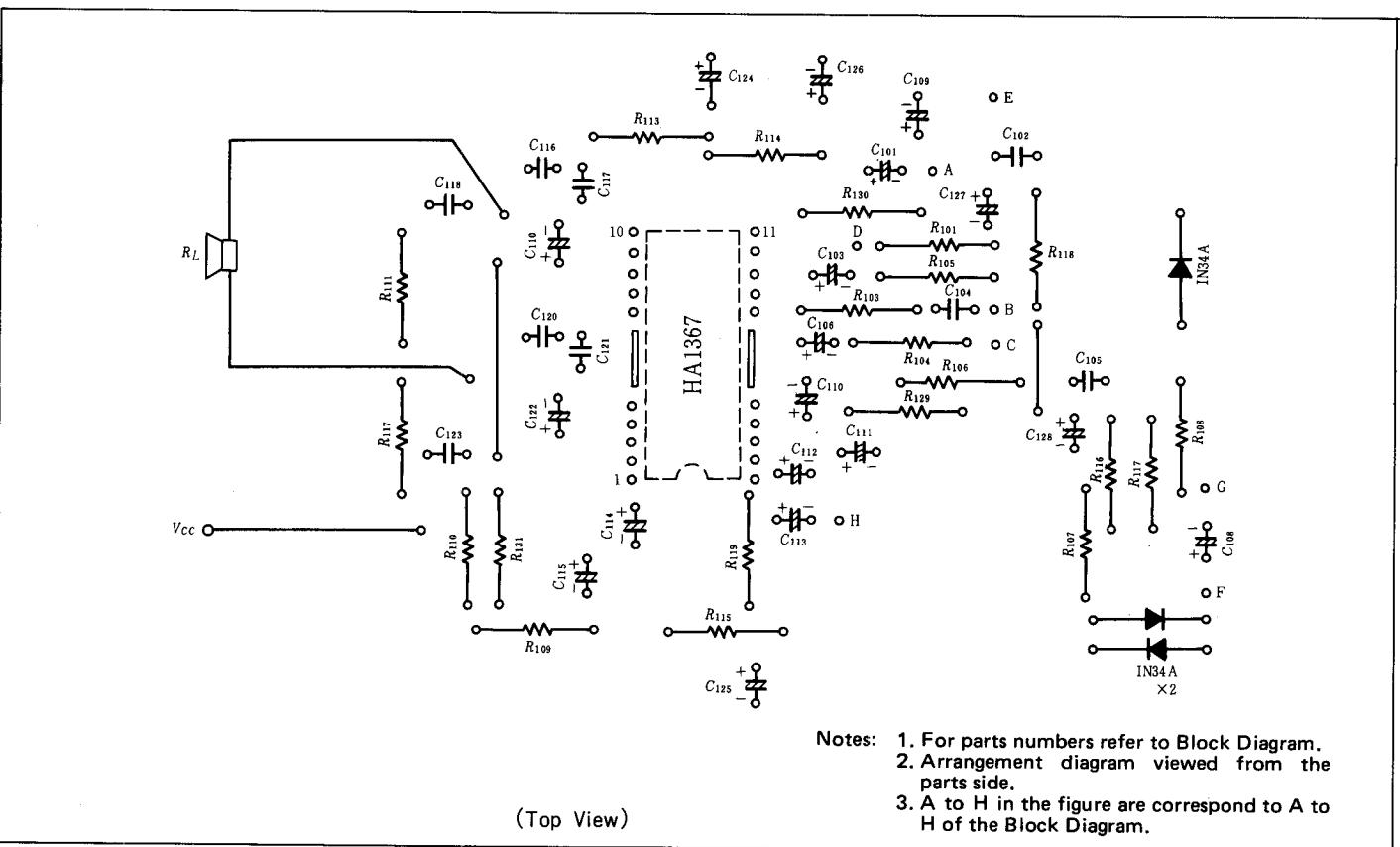


■ PC-BOARD LAYOUT PATTERN



(Bottom View)

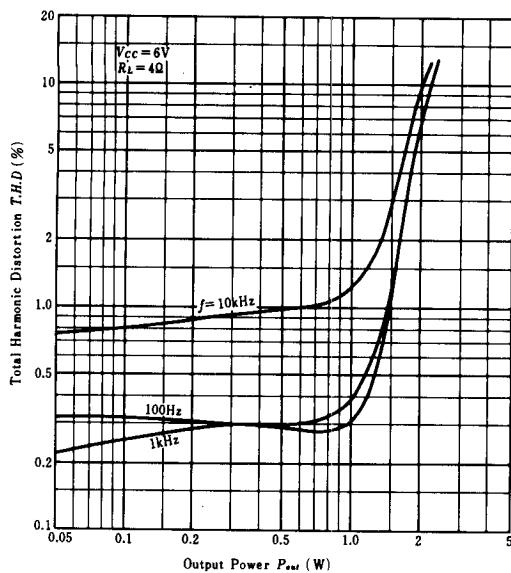
Notes: 1. A pattern that is viewed from the copper leaf side.
 2. Provide soldering plating for the print pattern and make resistance as low as possible.
 3. Scale: 1/1



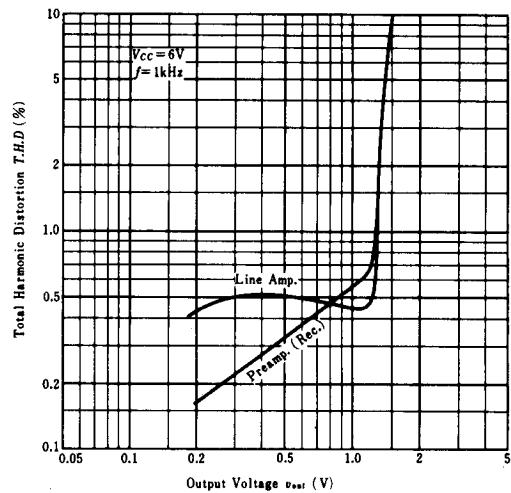
(Top View)

Notes: 1. For parts numbers refer to Block Diagram.
 2. Arrangement diagram viewed from the parts side.
 3. A to H in the figure are correspond to A to H of the Block Diagram.

**TOTAL HARMONIC DISTORTION VS.
OUTPUT POWER**



**TOTAL HARMONIC DISTORTION VS.
OUTPUT VOLTAGE**



VOLTAGE GAIN VS. FREQUENCY

