

HA17904A Series

Dual Operational Amplifier

REJ03D0689-0200

Rev.2.00

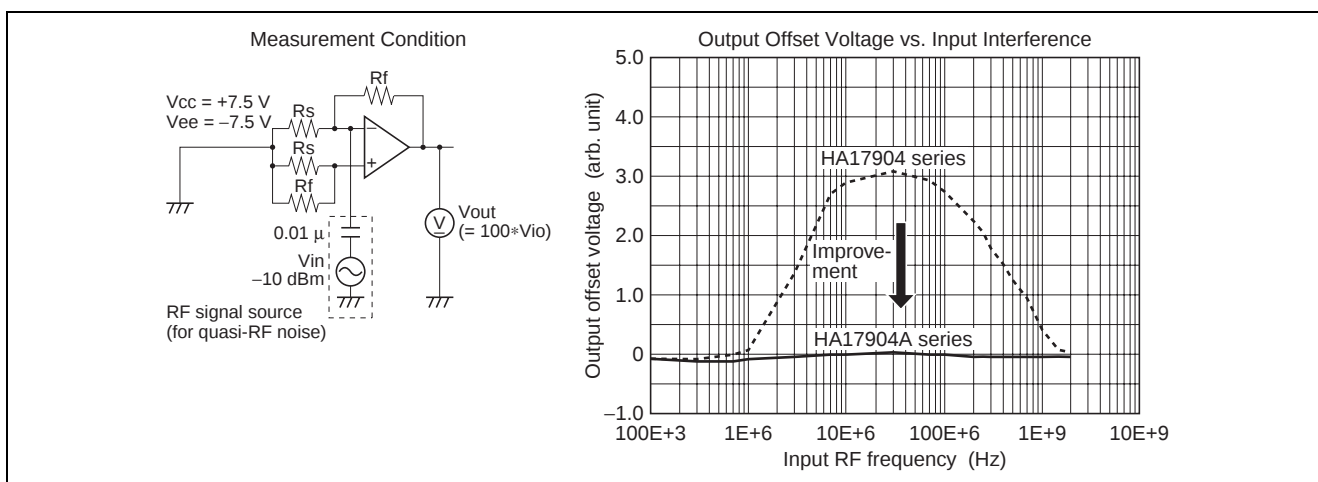
Mar 10, 2006

Description

HA17904A series are dual operational amplifier that provide high gain and internal phase compensation, with single power supply. They can be widely applied to control equipments and to general use.

Features

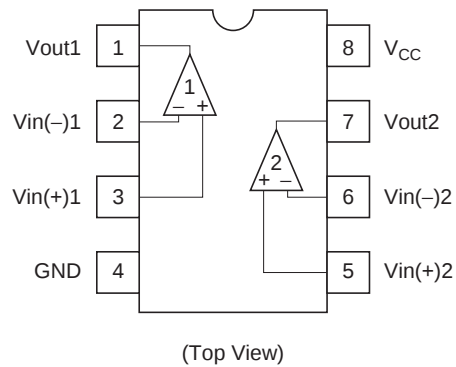
- Wide range of supply voltage, and single power supply used
- Wide range of common mode voltage, and possible to operate with an input about 0 V, and output around 0 V is available
- Frequency characteristics and input bias current are temperature compensated
- Low electro-magnetic susceptibility level



Ordering Information

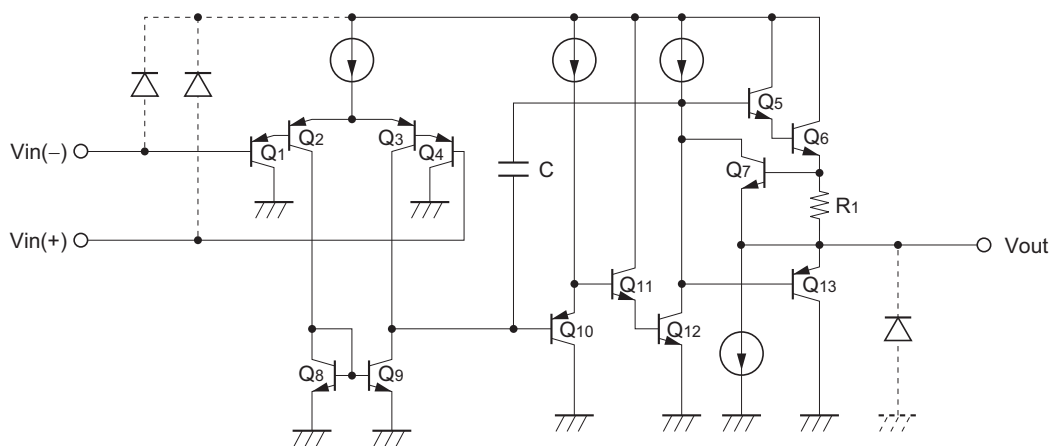
Type No.	Application	Package Name	Package Code
HA17904APS	Industrial use	DIP-8 pin	PRDP0008AF-B
HA17904AFP		SOP-8 pin (JEITA)	PRSP0008DE-B
HA17904ARP		SOP-8 pin (JEDEC)	PRSP0008DD-C
HA17904AT		TSSOP-8 pin	PTSP0008JC-B

Pin Arrangement



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Circuit Schematic (1/2)



Note: If Input/Output terminals voltage over the absolute maximum ratings, there is possibility of mis-operation, characteristics deterioration and destruction, because of the current's flowing to parasitic diode in IC. The Input/Output terminals are recommended to be protected with the clamp circuit which using the diode with low forward voltage (like schottky barrier diode) when there is a possibility for the Input/Output terminals voltage exceeds the absolute maximum ratings.

Absolute Maximum Ratings

(Ta = 25°C)

Item		Symbol	Ratings	Unit
Power supply voltage		V _{CC}	32	V
Output sink current		I _{osink}	50	mA
Common mode input voltage		V _{CM}	-0.3 to +V _{CC}	V
Differential input voltage		V _{in(diff)}	±V _{CC}	V
Output voltage		V _{out}	-0.3 to +V _{CC}	V
Allowable power dissipation	DIP	P _T	570 * ¹	mW
	SOP		385 * ²	
	TSSOP		192 * ³	
Operating temperature		Topr	-40 to +85	°C
Storage temperature		Tstg	-55 to +125	°C

Notes: 1. HA17904APS:

This is the allowable values up to Ta = 50°C. Derate by 8.3 mW/°C.

2. HA17904AFP/ARP:

These are the allowable values up to Ta = 25°C mounting in air.

When it is mounted on glass epoxy board of 40 mm × 40 mm × 1.5 mm with 30% wiring density, the allowable value is 570 mW up to Ta = 45°C. If Ta > 45°C, derate by 7.14 mW/°C.

3. HA17904AT:

These are the allowable values up to Ta = 25°C. Derate by 1.92 mW/°C above that temperature.

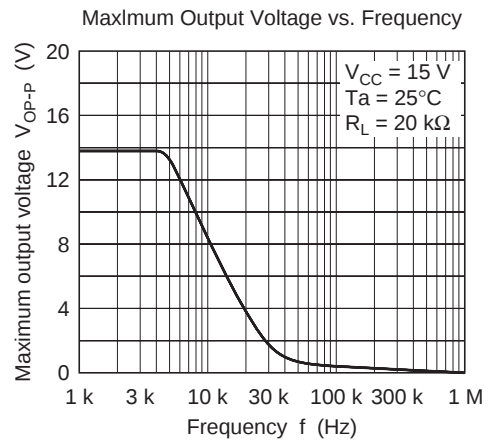
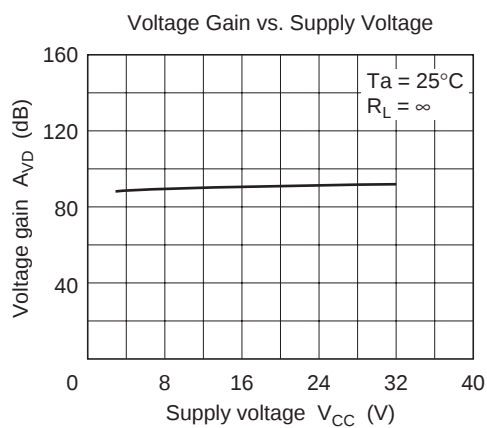
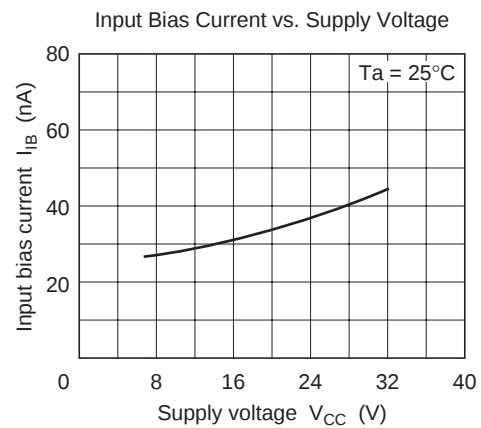
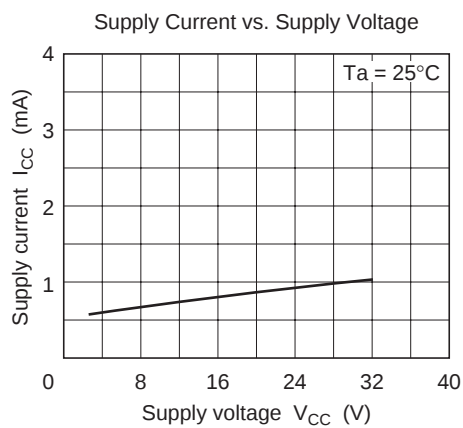
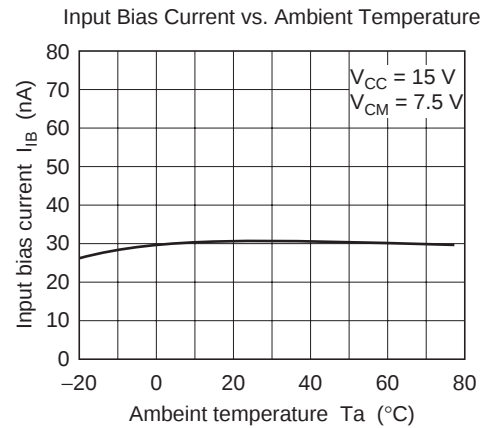
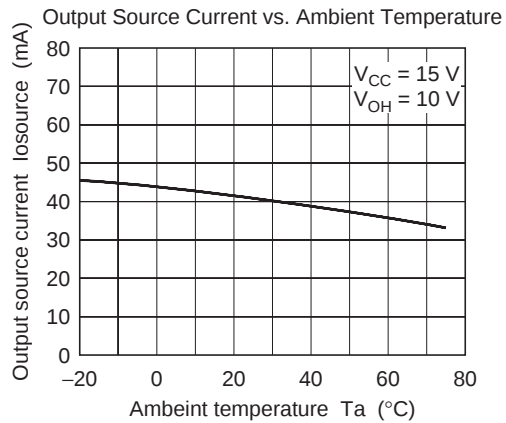
Electrical Characteristics

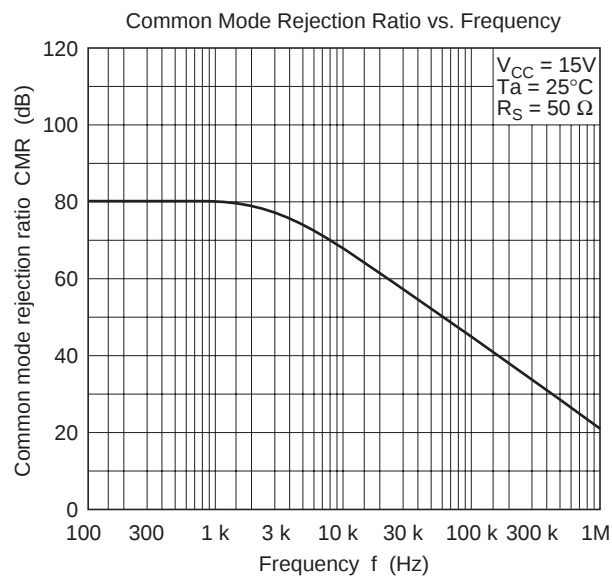
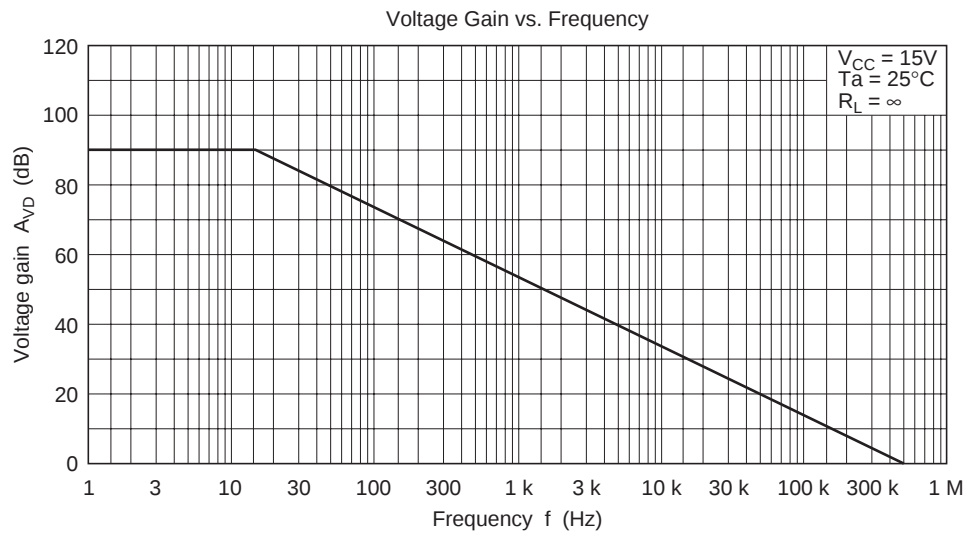
(V_{CC} = +15 V, Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Input offset voltage	V _{IO}	—	3	7	mV	V _{CM} = 7.5V, R _S = 50Ω, R _f = 50kΩ
Input offset current	I _{IO}	—	5	50	nA	V _{CM} = 7.5V, I _{IO} = I _{I(+)} - I _{I(-)}
Input bias current	I _{IB}	—	30	250	nA	V _{CM} = 7.5V
Power source rejection ratio	PSRR	—	93	—	dB	R _S = 1kΩ, R _f = 100kΩ
Voltage gain	A _{VD}	75	90	—	dB	R _L = ∞, R _S = 1kΩ, R _f = 100kΩ
Common mode rejection ratio	CMR	—	80	—	dB	R _S = 50Ω, R _f = 5kΩ
Common mode input voltage range	V _{CM(+)}	13.5	—	—	V	R _S = 1kΩ, R _f = 100kΩ
	V _{CM(-)}	—	—	-0.3	V	R _S = 1kΩ, R _f = 100kΩ
Peak-to-peak output voltage	V _{OP-P}	—	13.6	—	V	f = 100Hz, R _L = 20kΩ, R _S = 1kΩ, R _f = 100kΩ
Output source current	I _{osource}	20	40	—	mA	V _{IN+} = 1V, V _{IN-} = 0V, V _{OH} = 10V
Output sink current	I _{osink}	10	20	—	mA	V _{IN-} = 1V, V _{IN+} = 0V, V _{OL} = 2.5V
		15	50	—	μA	V _{IN-} = 1V, V _{IN+} = 0V, V _{out} = 200mV
Supply current	I _{CC}	—	0.8	2	mA	V _{IN} = GND, R _L = ∞
Slew rate	SR	—	0.2	—	V/μs	R _L = ∞, V _{CM} = 7.5V, f = 1.5kHz
Channel separation * ¹	CS	—	(120)	—	dB	f = 1kHz

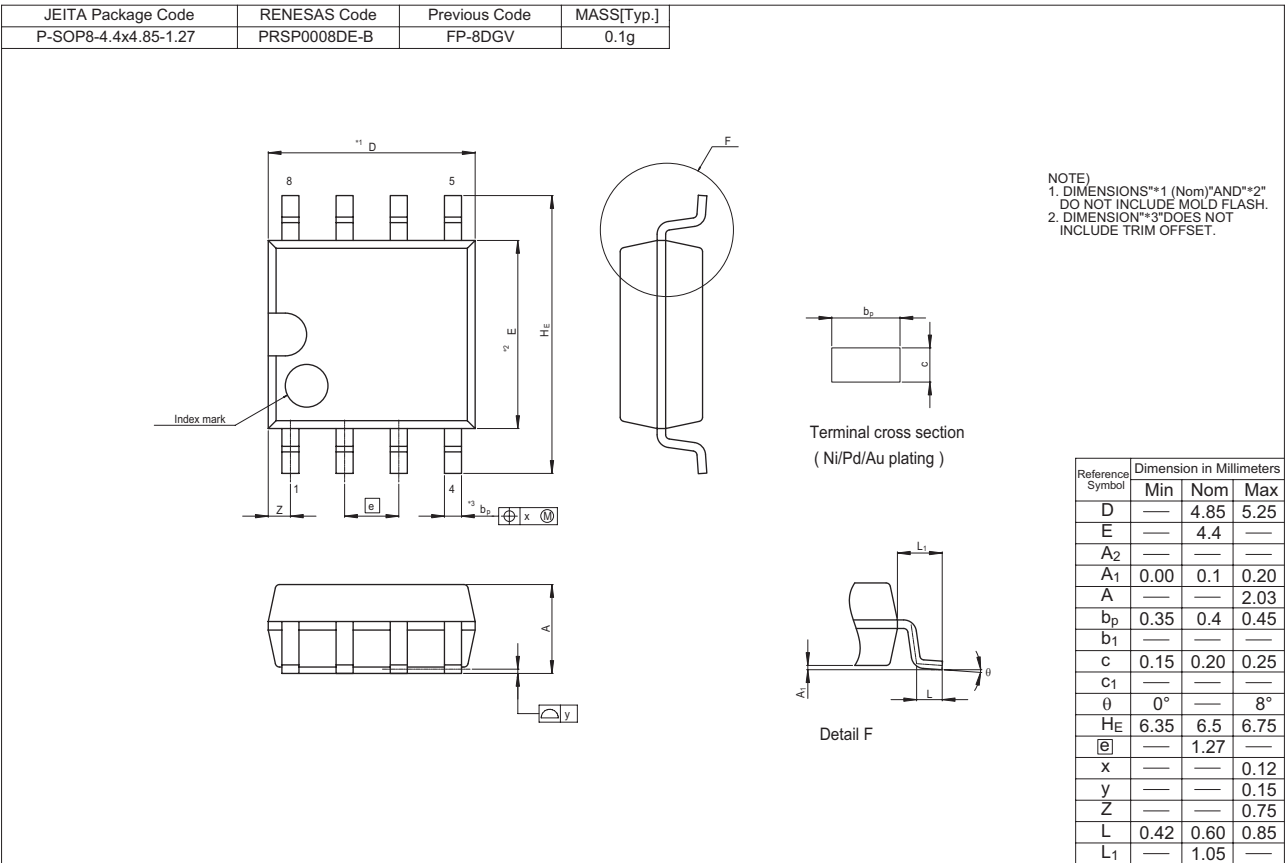
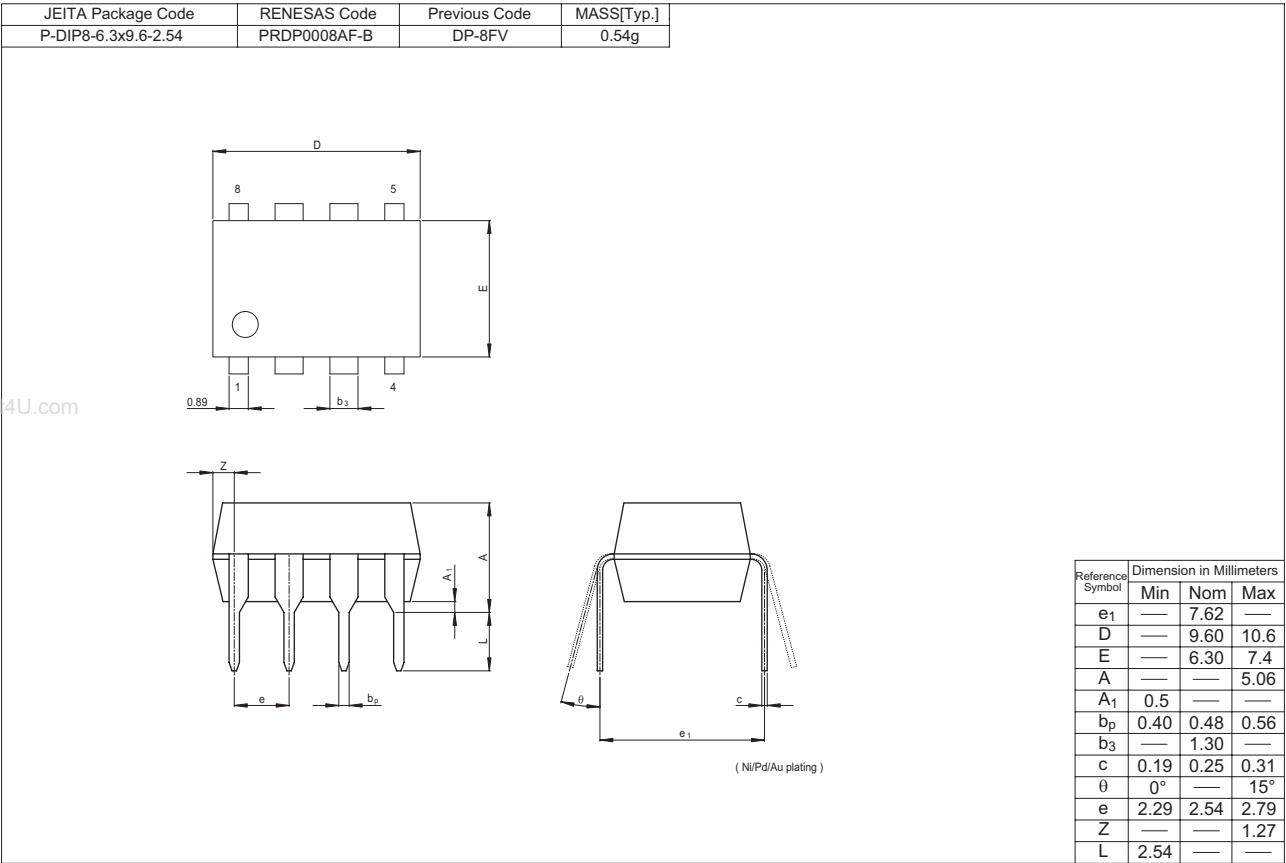
Note: 1. Design spec.

Characteristic Curves



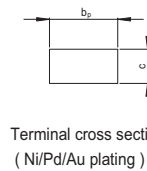
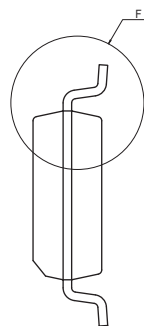
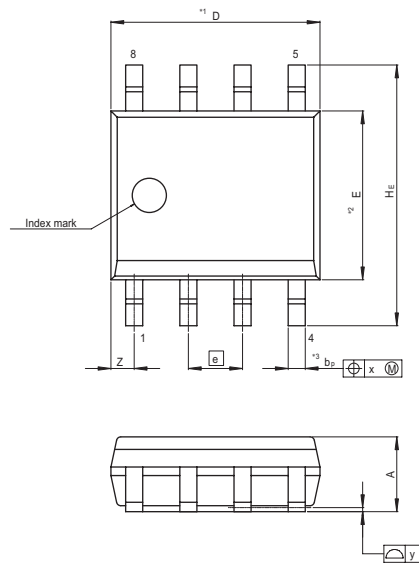


Package Dimensions

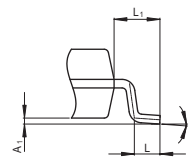


HA17904A Series

JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
P-SOP8-3.95x4.9-1.27	PRSP0008DD-C	FP-8DCV	0.085g



Terminal cross section
(Ni/Pd/Au plating)

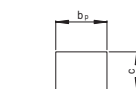
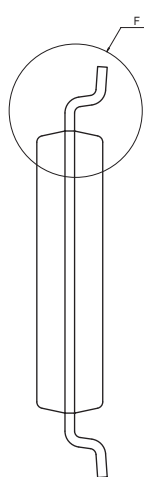
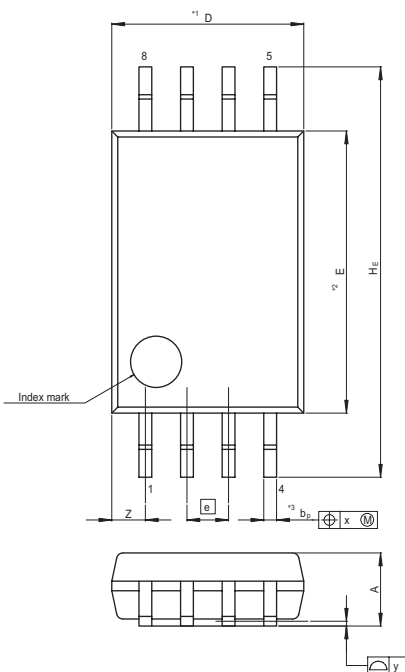


Detail F

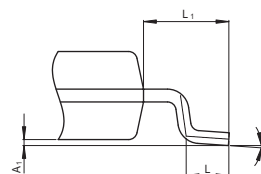
NOTE)
1. DIMENSIONS**1 (Nom)**AND**2*
DO NOT INCLUDE MOLD FLASH.
2. DIMENSION**3*DOES NOT
INCLUDE TRIM OFFSET.

Reference Symbol	Min	Nom	Max
D	—	4.90	5.30
E	—	3.95	—
A ₂	—	—	—
A ₁	0.10	0.14	0.25
A	—	—	1.75
b _p	0.34	0.40	0.46
b ₁	—	—	—
c	0.15	0.20	0.25
c ₁	—	—	—
θ	0°	—	8°
H _E	5.80	6.10	6.20
⌀	—	1.27	—
x	—	—	0.25
y	—	—	0.10
Z	—	—	0.75
L	0.40	0.60	1.27
L ₁	—	1.08	—

JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
P-TSSOP8-4.4x3-0.65	PTSP0008JC-B	TTP-8DAV	0.034g



Terminal cross section
(Ni/Pd/Au plating)



Detail F

NOTE)
1. DIMENSIONS**1 (Nom)**AND**2*
DO NOT INCLUDE MOLD FLASH.
2. DIMENSION**3*DOES NOT
INCLUDE TRIM OFFSET.

Reference Symbol	Min	Nom	Max
D	—	3.00	3.30
E	—	4.40	—
A ₂	—	—	—
A ₁	0.03	0.07	0.10
A	—	—	1.10
b _p	0.15	0.20	0.25
b ₁	—	—	—
c	0.10	0.15	0.20
c ₁	—	—	—
θ	0°	—	8°
H _E	6.20	6.40	6.60
⌀	—	0.65	—
x	—	—	0.13
y	—	—	0.10
Z	—	—	0.805
L	0.40	0.50	0.60
L ₁	—	1.00	—

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