

# HAT1043M

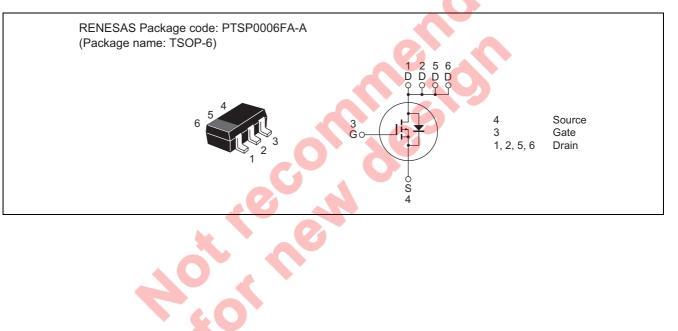
# Silicon P Channel Power MOS FET Power Switching

REJ03G1151-0600 (Previous: ADE-208-754D) Rev.6.00 Sep 07, 2005

### Features

- Low on-resistance
- Low drive current
- High density mounting
- 2.5 V gate drive device can be driven from 3 V source

### Outline





# **Absolute Maximum Ratings**

			$(Ta = 25^{\circ}C)$
Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	-20	V
Gate to source voltage	V <sub>GSS</sub>	±12	V
Drain current	ID	-4.4	A
Drain peak current	I <sub>D (pulse)</sub> Note 1	-17.6	A
Body-drain diode reverse drain current	I <sub>DR</sub> Note 2	-4.4	A
Channel dissipation	Pch (pulse) Note 2	2.0	W
Channel dissipation	Pch (continuous) Note 3	1.05	W
Channel temperature	Tch	150	۵°
Storage temperature	Tstg	-55 to +150	۵°

Notes: 1. PW  $\leq$  10  $\mu s,\,duty\,cycle \leq$  1%

- 2. When using the alumina ceramic board (50  $\times$  50  $\times$  0.7 mm), PW  $\leq$  5 s, Ta = 25^{\circ}C
- 3. When using the alumina ceramic board (50  $\times$  50  $\times$  0.7 mm), Ta = 25  $^{\circ}\text{C}$

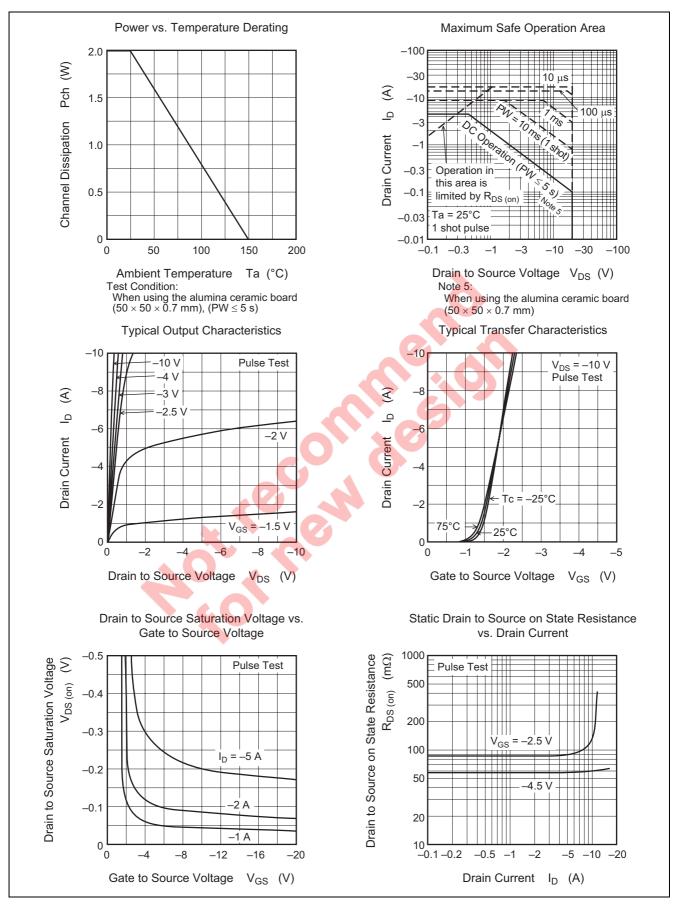
### **Electrical Characteristics**

						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V (BR) DSS	-20	_	<b>5</b> -	V	$I_{\rm D} = -10 \text{ mA}, V_{\rm GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_		±0.1	μA	$V_{GS} = \pm 12 \text{ V}, \text{ V}_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>			-1	μA	$V_{DS} = -20 V, V_{GS} = 0$
Gate to source cutoff voltage	V <sub>GS (off)</sub>	-0.4	_	-1.4	V	$I_D = -1 \text{ mA}, V_{DS} = -10 \text{ V}$
Static drain to source on state resistance	R <sub>DS (on)</sub>		55	65	mΩ	$I_D = -3 \text{ A}, V_{GS} = -4.5 \text{ V}^{Note 4}$
	R <sub>DS (on)</sub>	-	85	110	mΩ	$I_D = -3 \text{ A}, V_{GS} = -2.5 \text{ V}^{Note 4}$
Forward transfer admittance	y <sub>fs</sub>	4	7		S	$I_D = -3 \text{ A}, V_{DS} = -10 \text{ V}^{Note 4}$
Input capacitance	Ciss	ľ	750	—	pF	$V_{DS} = -10 V$
Output capacitance	Coss	1	310	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss		220	—	pF	f = 1 MHz
Total gate charge	Qg	<u> </u>	11	—	nC	V <sub>DD</sub> = -10 V
Gate to source charge	Qgs	_	2	—	nC	$V_{GS} = -4.5 V$
Gate to drain charge	Qgd		3.5	—	nC	$I_{\rm D} = -4.4 \ {\rm A}$
Turn-on delay time	t <sub>d (on)</sub>	-	15	—	ns	$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -3 \text{ A},$
Rise time	tr	-	100	—	ns	$R_L = 3.3 \Omega$
Turn-off delay time	t <sub>d (off)</sub>	-	85	—	ns	
Fall time	t <sub>f</sub>	—	100	—	ns	
Body-drain diode forward voltage	V <sub>DF</sub>		-0.95	-1.23	V	$I_F = -4.4 \text{ A}, V_{GS} = 0$
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	50	—	ns	$I_F = -4.4 \text{ A}, V_{GS} = 0$
						di <sub>F</sub> /dt = −20 A/µs

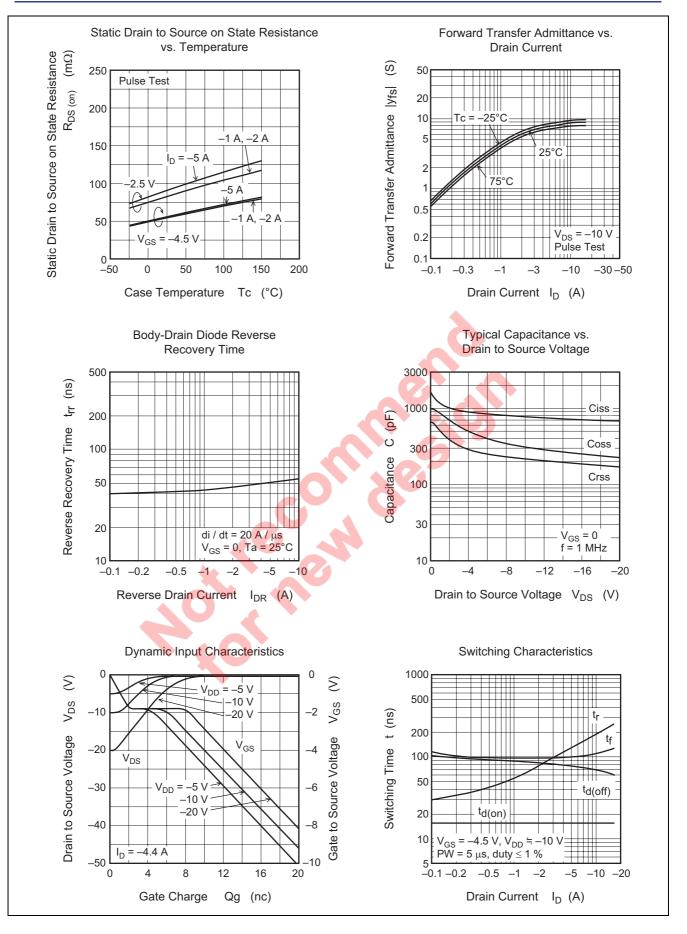
Note: 4. Pulse test



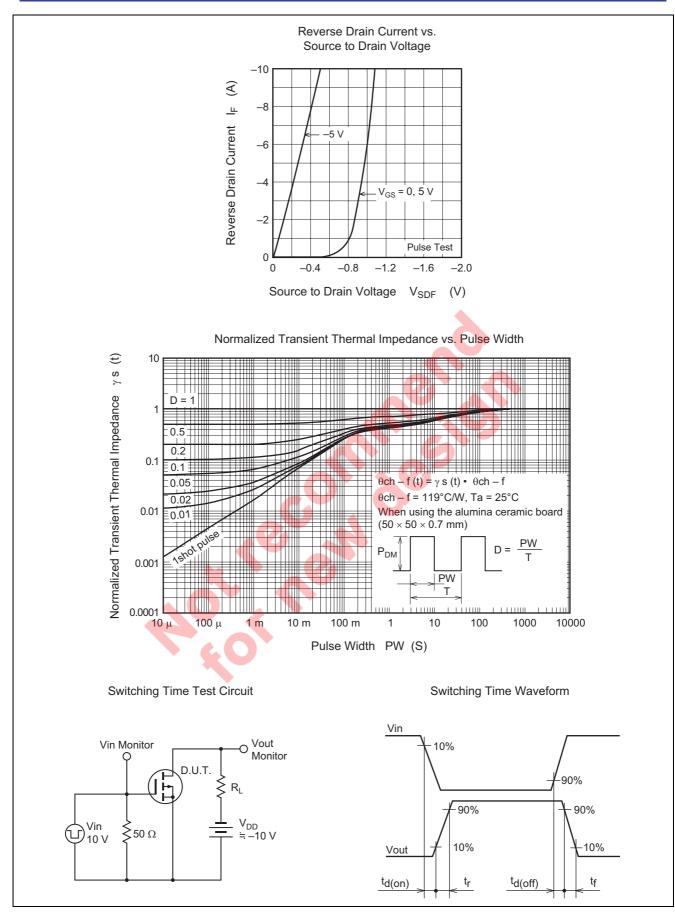
### **Main Characteristics**





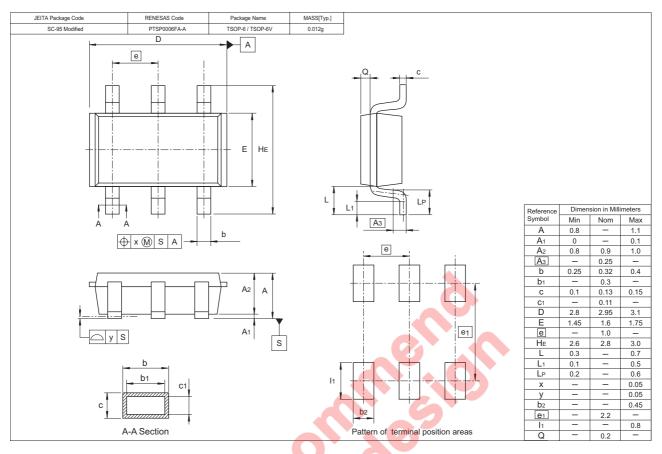








## **Package Dimensions**



### **Ordering Information**

Part Name	Quantity	Shipping Container
HAT1043M-EL-E	3000 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.



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