

HAT1072H

Silicon P Channel Power MOS FET
Power Switching

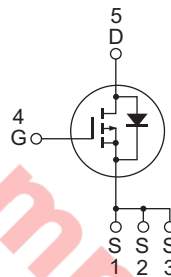
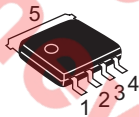
REJ03G1155-0700
(Previous: ADE-208-1534E)
Rev.7.00
Sep 07, 2005

Features

- Capable of -4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance
 $R_{DS(on)} = 3.6\text{ m}\Omega$ typ (at $V_{GS} = -10\text{ V}$)

Outline

RENESAS Package code: PTZZ0005DA-A
(Package name: LFPAK)



1, 2, 3 Source
4 Gate
5 Drain

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Drain to source voltage	V _{DSS}	-30	V
Gate to source voltage	V _{GSS}	-20 / +10	V
Drain current	I _D	-40	A
Drain peak current	I _{D (pulse)} ^{Note 1}	-160	A
Body-drain diode reverse drain current	I _{DR}	-40	A
Channel dissipation	P _{ch} ^{Note 2}	30	W
Channel temperature	T _{ch}	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1%

2. Tc = 25°C

Electrical Characteristics

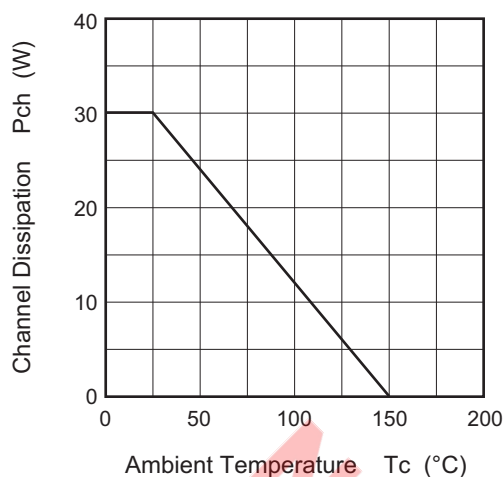
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR) DSS}	-30	—	—	V	I _D = -10 mA, V _{GS} = 0
Gate to source leak current	I _{GSS}	—	—	±0.1	μA	V _{GS} = -20, +10 V, V _{DS} = 0
Zero gate voltage drain current	I _{DSS}	—	—	-1	μA	V _{DS} = -30 V, V _{GS} = 0
Gate to source cutoff voltage	V _{GS (off)}	-0.5	—	-2.0	V	V _{DS} = -10 V, I _D = -1 mA
Static drain to source on state resistance	R _{DS (on)}	—	3.6	4.5	mΩ	I _D = -20 A, V _{GS} = -10 V ^{Note 3}
	R _{DS (on)}	—	5.3	7.7	mΩ	I _D = -20 A, V _{GS} = -4.5 V ^{Note 3}
Forward transfer admittance	y _{fs}	36	60	—	S	I _D = -20 A, V _{DS} = -10 V ^{Note 3}
Input capacitance	C _{iss}	—	9500	—	pF	V _{DS} = -10 V
Output capacitance	C _{oss}	—	1300	—	pF	V _{GS} = 0
Reverse transfer capacitance	C _{rss}	—	700	—	pF	f = 1 MHz
Total gate charge	Q _g	—	155	—	nC	V _{DD} = -10 V
Gate to source charge	Q _{gs}	—	28	—	nC	V _{GS} = -10 V
Gate to drain charge	Q _{gd}	—	26	—	nC	I _D = -40 A
Turn-on delay time	t _{d (on)}	—	28	—	ns	V _{GS} = -10 V, I _D = -20 A,
Rise time	t _r	—	60	—	ns	V _{DD} ≅ -10 V
Turn-off delay time	t _{d (off)}	—	305	—	ns	R _L = 0.5 Ω
Fall time	t _f	—	140	—	ns	R _g = 4.7 Ω
Body-drain diode forward voltage	V _{DF}	—	0.87	1.14	V	I _F = -40 A, V _{GS} = 0 ^{Note 3}
Body-drain diode reverse recovery time	t _{rr}	—	110	—	ns	I _F = -40 A, V _{GS} = 0 di _F /dt = 100 A/μs

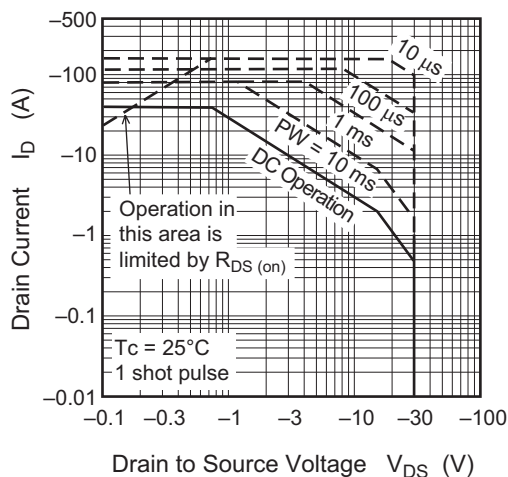
Note: 3. Pulse test

Main Characteristics

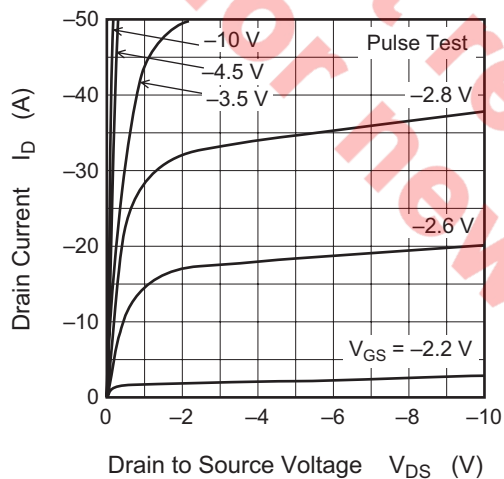
Power vs. Temperature Derating



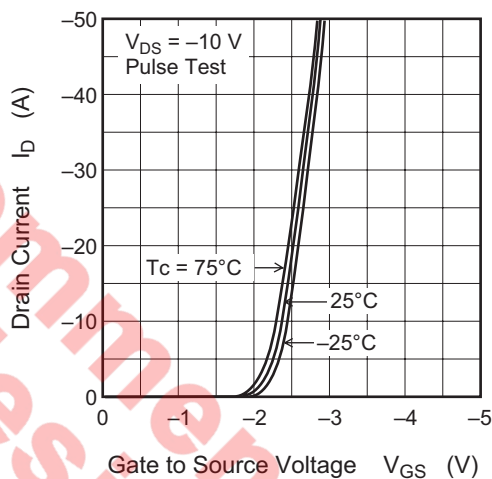
Maximum Safe Operation Area



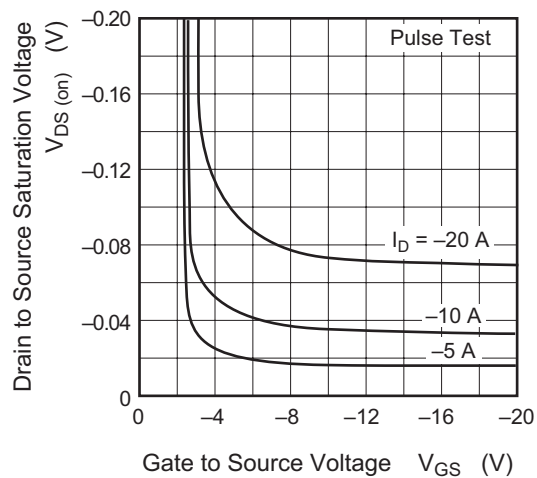
Typical Output Characteristics



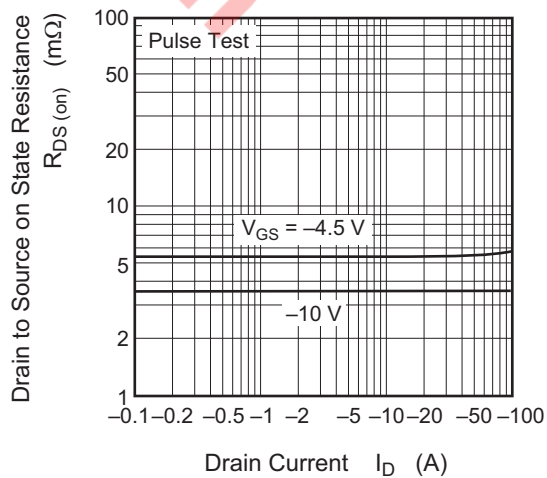
Typical Transfer Characteristics



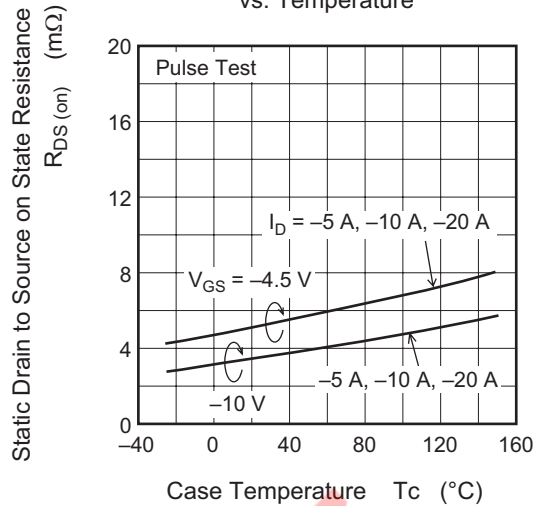
Drain to Source Saturation Voltage vs. Gate to Source Voltage



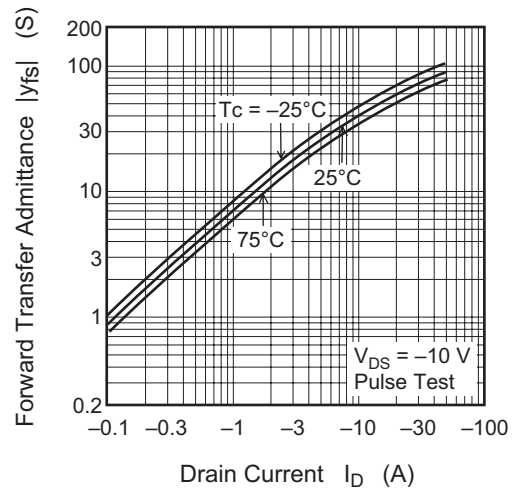
Static Drain to Source on State Resistance vs. Drain Current



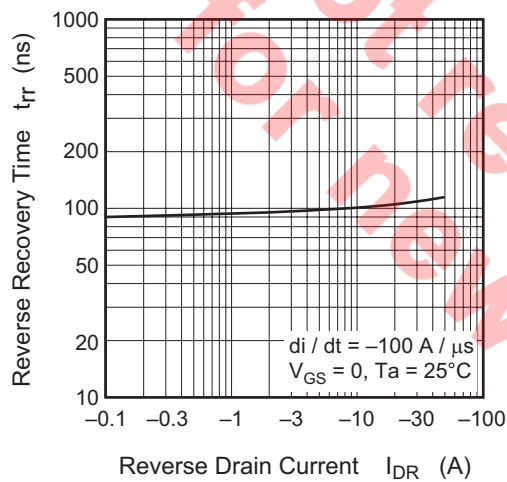
Static Drain to Source on State Resistance vs. Temperature



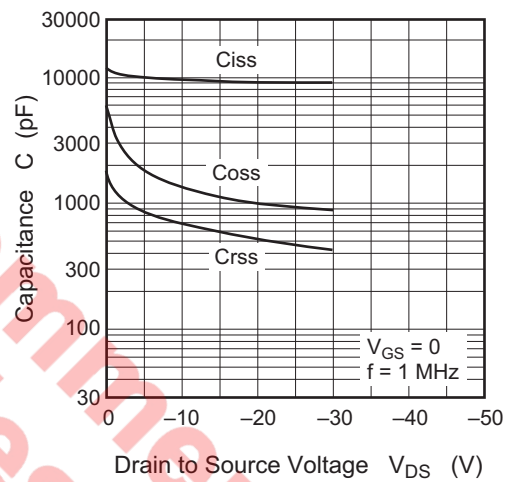
Forward Transfer Admittance vs. Drain Current



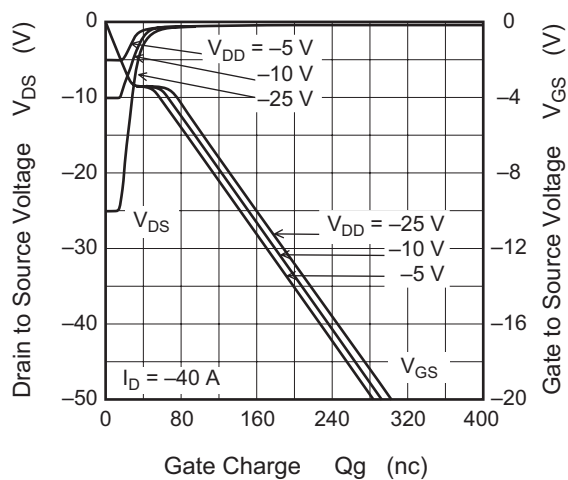
Body-Drain Diode Reverse Recovery Time



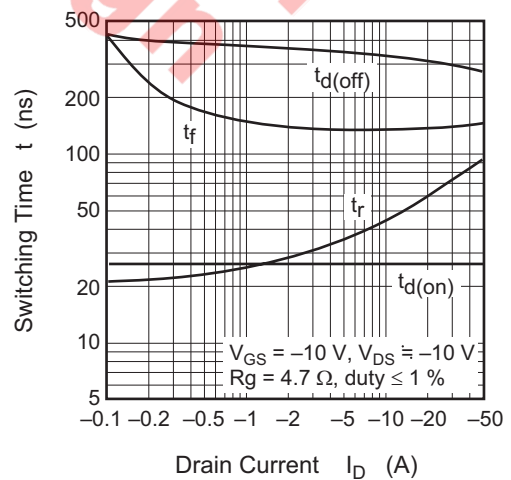
Typical Capacitance vs. Drain to Source Voltage

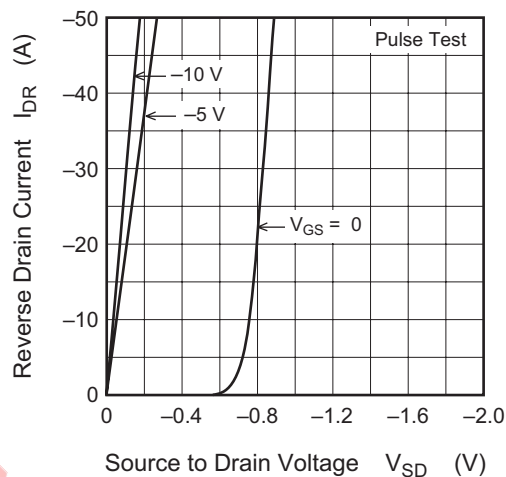


Dynamic Input Characteristics

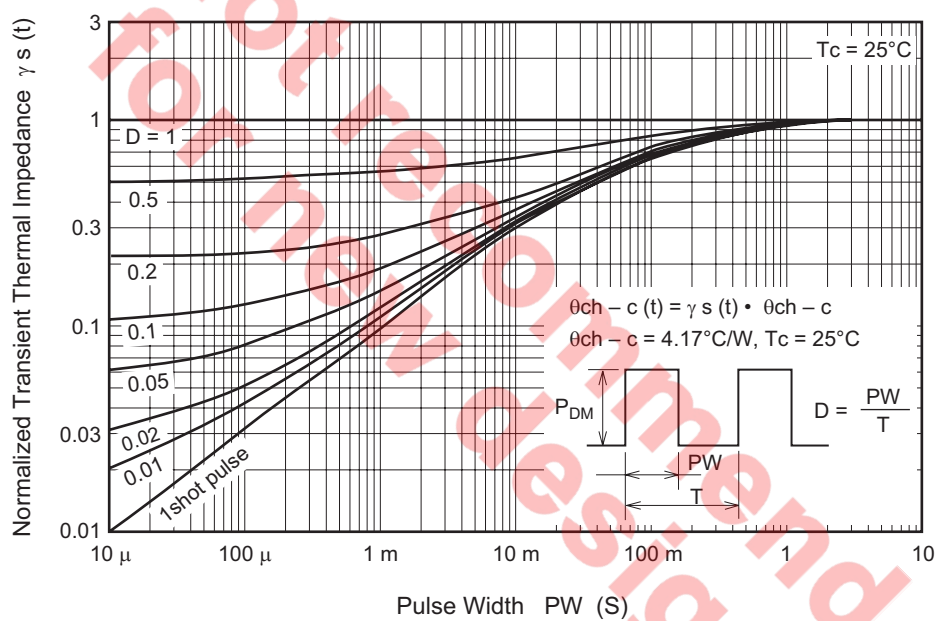


Switching Characteristics

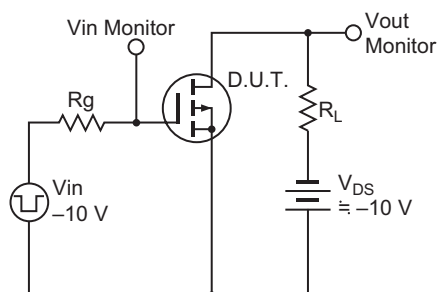


Reverse Drain Current vs.
Source to Drain Voltage

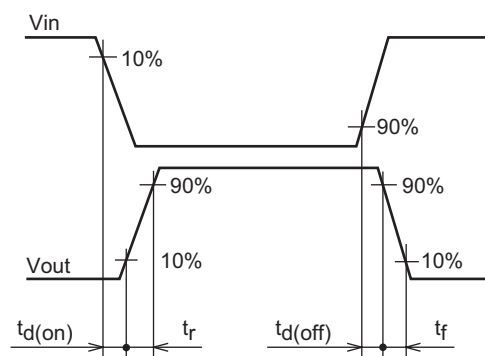
Normalized Transient Thermal Impedance vs. Pulse Width



Switching Time Test Circuit



Switching Time Waveform



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