

HAT2049T

Silicon N Channel Power MOS FET
High Speed Power Switching

HITACHI

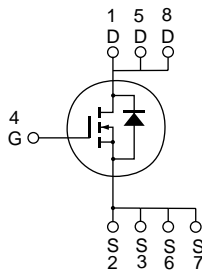
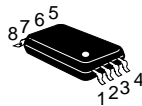
ADE-208-723 (Z)
1st. Edition
February 1999

Features

- Low on-resistance
- Capable of 2.5 V gate drive
- Low drive current
- High density mounting

Outline

TSSOP-8



1, 5, 8 Drain
2, 3, 6, 7 Source
4 Gate

Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	30	V
Gate to source voltage	V_{GSS}	± 12	V
Drain current	I_D	8	A
Drain peak current	$I_{D(pulse)}$ ^{Note1}	64	A
Body-drain diode reverse drain current	I_{DR}	8	A
Channel dissipation	Pch ^{Note2}	1.3	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	- 55 to + 150	°C

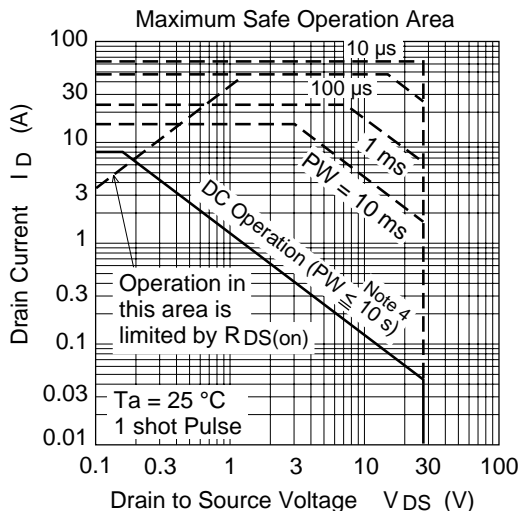
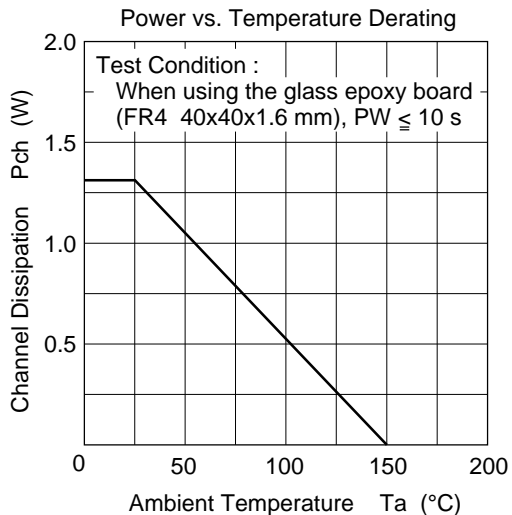
Note: 1. $PW \leq 10\mu s$, duty cycle $\leq 1\%$
 2. When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), $PW \leq 10s$

Electrical Characteristics (Ta = 25°C)

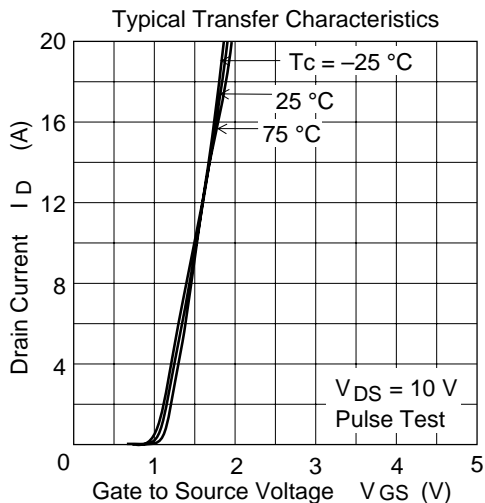
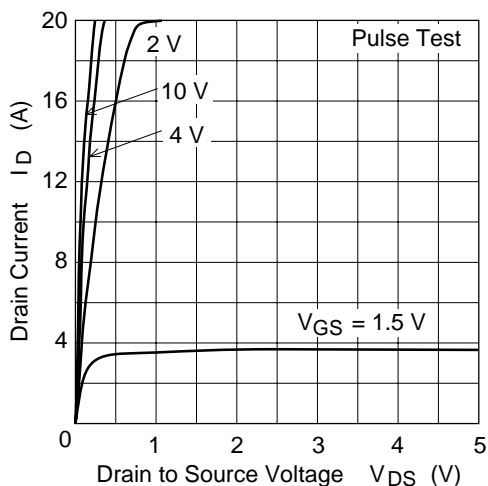
Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	—	—	V	$I_D = 10\text{ mA}$, $V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	± 12	—	—	V	$I_G = \pm 100\ \mu A$, $V_{DS} = 0$
Gate to source leak current	I_{GSS}	—	—	± 0.1	μA	$V_{GS} = \pm 12\text{ V}$, $V_{DS} = 0$
Zero gate voltage drain current	I_{DSS}	—	—	1	μA	$V_{DS} = 30\text{ V}$, $V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	0.4	—	1.4	V	$V_{DS} = 10\text{ V}$, $I_D = 1\text{ mA}$
Static drain to source on state resistance	$R_{DS(on)}$	—	0.013	0.017	Ω	$I_D = 4\text{ A}$, $V_{GS} = 4\text{ V}$ ^{Note3}
	$R_{DS(on)}$	—	0.017	0.025	Ω	$I_D = 4\text{ A}$, $V_{GS} = 2.5\text{ V}$ ^{Note3}
Forward transfer admittance	$ y_{fs} $	13	20	—	S	$I_D = 4\text{ A}$, $V_{DS} = 10\text{ V}$ ^{Note3}
Input capacitance	Ciss	—	1430	—	pF	$V_{DS} = 10\text{ V}$
Output capacitance	Coss	—	410	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	—	265	—	pF	f = 1MHz
Turn-on delay time	$t_{d(on)}$	—	23	—	ns	$V_{GS} = 4\text{ V}$, $I_D = 4\text{ A}$
Rise time	t_r	—	165	—	ns	$V_{DD} \cong 10\text{ V}$
Turn-off delay time	$t_{d(off)}$	—	215	—	ns	
Fall time	t_f	—	185	—	ns	
Body-drain diode forward voltage	V_{DF}	—	0.83	1.08	V	IF = 8 A, $V_{GS} = 0$ ^{Note3}
Body-drain diode reverse recovery time	t_{rr}	—	30	—	ns	IF = 8 A, $V_{GS} = 0$ diF/ dt = 20 A/ μs

Note: 3. Pulse test

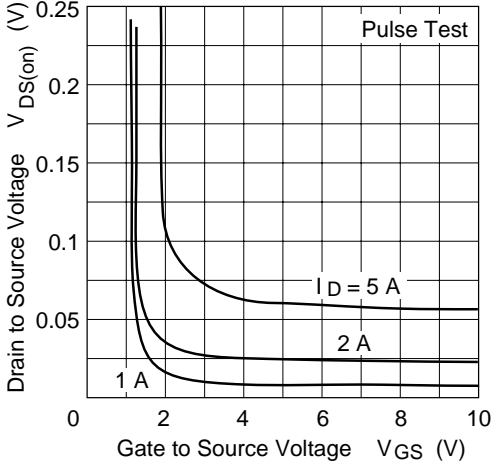
Main Characteristics



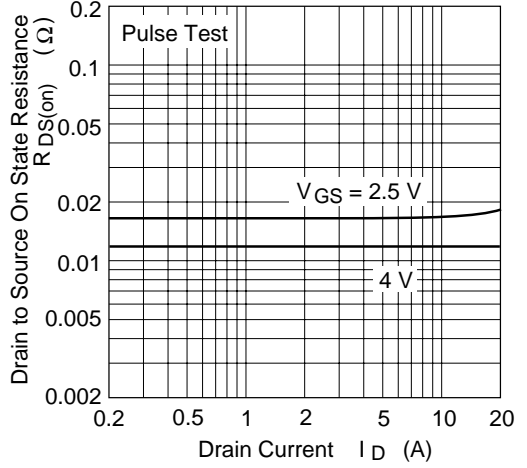
Typical Output Characteristics



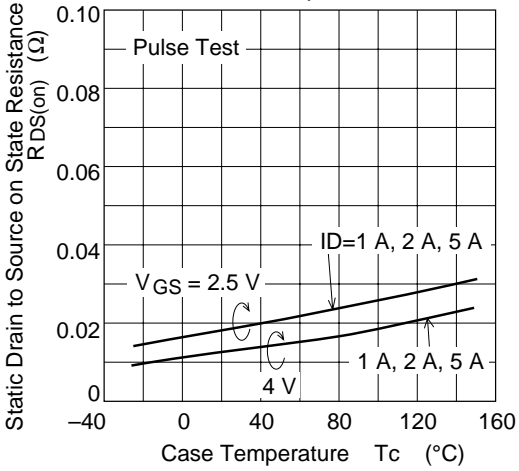
Drain to Source Saturation Voltage vs. Gate to Source Voltage



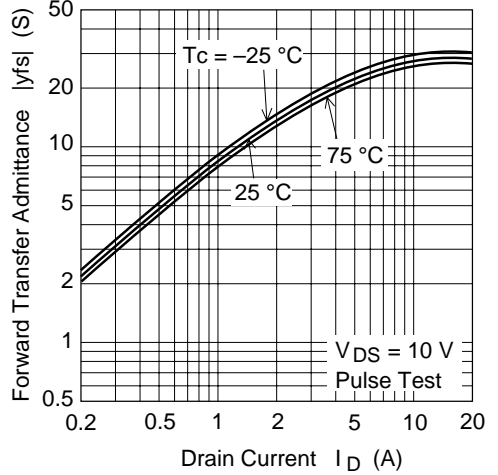
Static Drain to Source on State Resistance vs. Drain Current



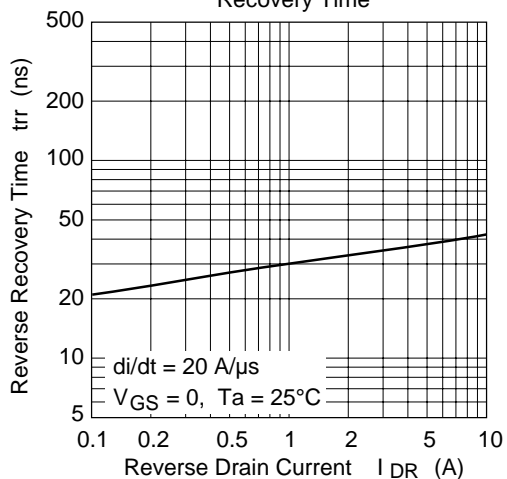
Static Drain to Source on State Resistance vs. Temperature



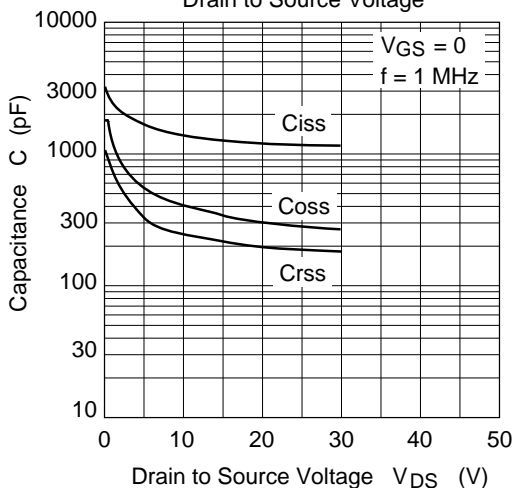
Forward Transfer Admittance vs. Drain Current



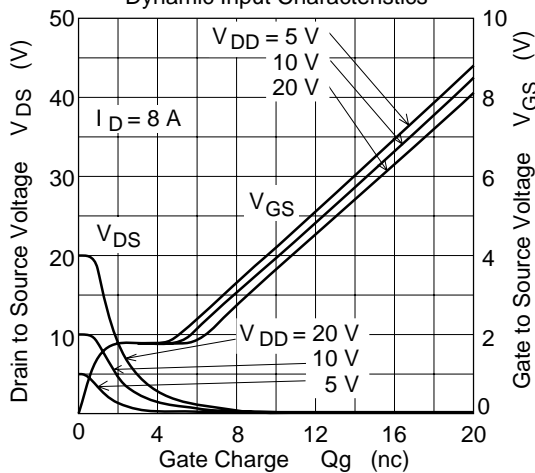
Body-Drain Diode Reverse Recovery Time



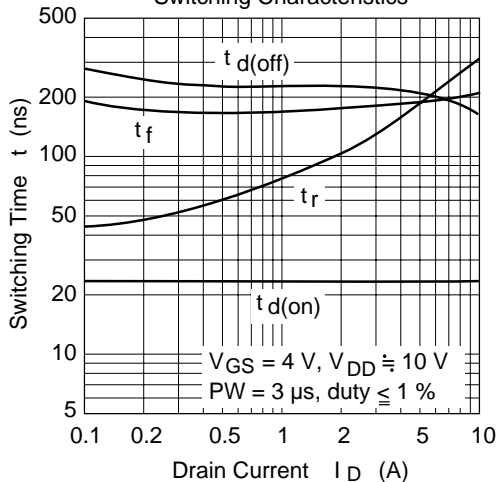
Typical Capacitance vs. Drain to Source Voltage

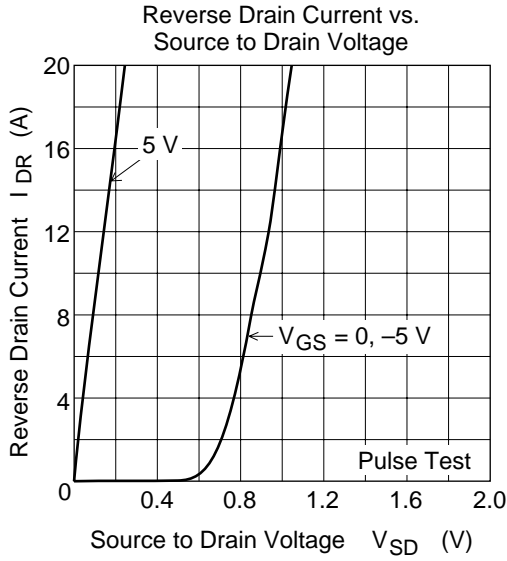


Dynamic Input Characteristics

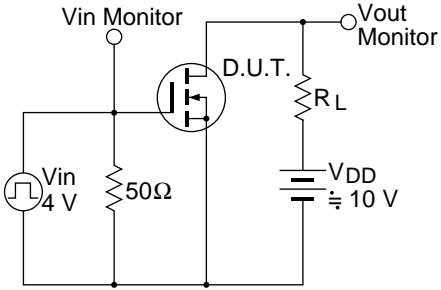


Switching Characteristics

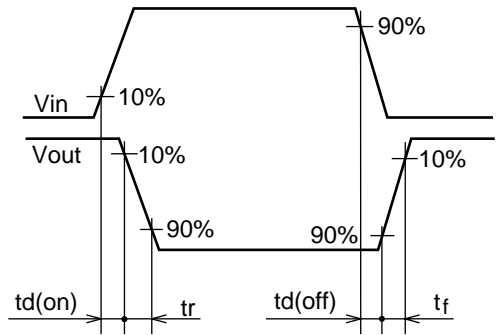




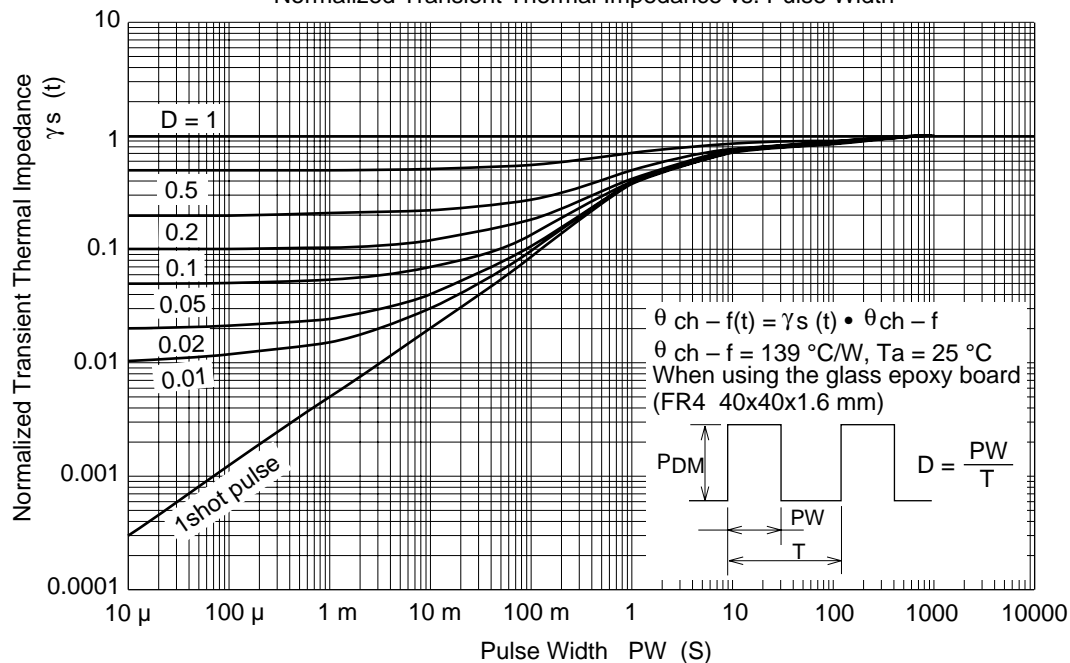
Switching Time Test Circuit



Switching Time Waveform

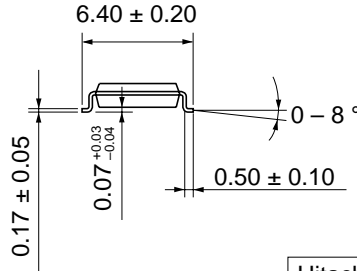
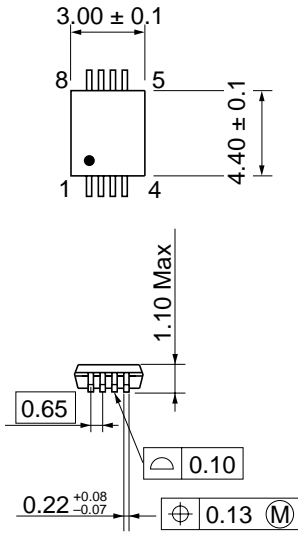


Normalized Transient Thermal Impedance vs. Pulse Width



Package Dimensions

Unit: mm



Hitachi code	TTP-8D
EIAJ	—
JEDEC	—

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