

# HAT2282C

## Silicon N Channel MOS FET Power Switching

REJ03G1329-0100

Rev.1.00

Jan 26, 2006

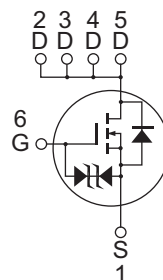
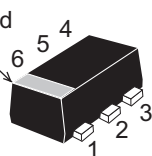
### Features

- Low on-resistance  
 $R_{DS(on)} = 173 \text{ m}\Omega$  typ.(at  $V_{GS} = 4.5 \text{ V}$ )
- Low drive current
- High density mounting
- 2.5 V gate drive device

### Outline

RENESAS Package code: PWSF0006JA-A  
(Package name: CMFPAK-6)

Indexband



1. Source
2. Drain
3. Drain
4. Drain
5. Drain
6. Gate

### Absolute Maximum Ratings

( $T_a = 25^\circ\text{C}$ )

Item	Symbol	Ratings	Unit
Drain to Source voltage	$V_{DSS}$	60	V
Gate to Source voltage	$V_{GSS}$	$\pm 12$	V
Drain current	$I_D$	1.5	A
Drain peak current	$I_{D(pulse)}$ <sup>Note1</sup>	6	A
Body - Drain diode reverse Drain current	$I_{DR}$	1.5	A
Channel dissipation	$P_{ch}$ <sup>Note2</sup>	830	mW
Channel temperature	$T_{ch}$	150	$^\circ\text{C}$
Storage temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$

Notes: 1.  $PW \leq 10 \mu\text{s}$ , duty cycle  $\leq 1\%$

2. When using the glass epoxy board (FR4  $40 \times 40 \times 1.6\text{mm}$ )

## Electrical Characteristics

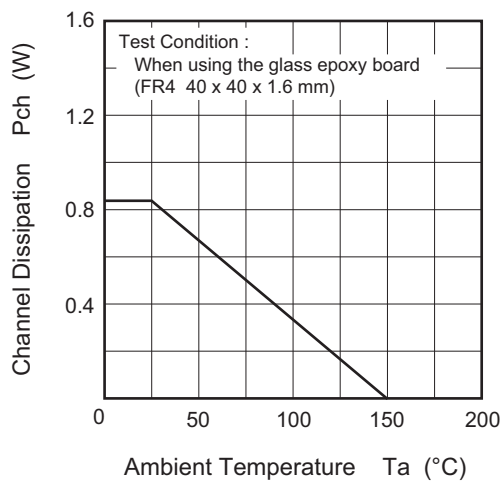
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to Source breakdown voltage	$V_{(BR)DSS}$	60	—	—	V	$I_D = 10 \text{ mA}$ , $V_{GS} = 0$
Gate to Source breakdown voltage	$V_{(BR)GSS}$	$\pm 12$				$I_G = \pm 100 \text{ }\mu\text{A}$ , $V_{DS} = 0$
Gate to Source leak current	$I_{GSS}$	—	—	$\pm 10$	$\mu\text{A}$	$V_{GS} = \pm 10 \text{ V}$ , $V_{DS} = 0$
Drain to Source leak current	$I_{DSS}$	—	—	1	$\mu\text{A}$	$V_{DS} = 60 \text{ V}$ , $V_{GS} = 0$
Gate to Source cutoff voltage	$V_{GS(off)}$	0.4	—	1.4	V	$V_{DS} = 10 \text{ V}$ , $I_D = 1 \text{ mA}$
Drain to Source on state resistance	$R_{DS(on)}$	—	173	225	$\text{m}\Omega$	$I_D = 0.8 \text{ A}$ , $V_{GS} = 4.5 \text{ V}$ <sup>Note3</sup>
	$R_{DS(on)}$	—	207	290	$\text{m}\Omega$	$I_D = 0.8 \text{ A}$ , $V_{GS} = 2.5 \text{ V}$ <sup>Note3</sup>
Forward transfer admittance	$ y_{fs} $	2.3	3.5	—	S	$I_D = 0.8 \text{ A}$ , $V_{DS} = 10 \text{ V}$ <sup>Note3</sup>
Input capacitance	$C_{iss}$	—	200	—	pF	$V_{DS} = 10 \text{ V}$ , $V_{GS} = 0$ , $f = 1 \text{ MHz}$
Output capacitance	$C_{oss}$	—	25	—	pF	
Reverse transfer capacitance	$C_{rss}$	—	15	—	pF	
Turn - on delay time	$t_{d(on)}$	—	10	—	ns	$I_D = 0.8 \text{ A}$ $V_{GS} = 4.5 \text{ V}$ , $V_{DD} = 10 \text{ V}$ $R_L = 1.25 \text{ }\Omega$ , $R_g = 4.7 \text{ }\Omega$
Rise time	$t_r$	—	26	—	ns	
Turn - off delay time	$t_{d(off)}$	—	30	—	ns	
Fall time	$t_f$	—	4	—	ns	
Total Gate charge	$Q_g$	—	2.4	—	nC	$V_{DD} = 10 \text{ V}$ , $V_{GS} = 4.5 \text{ V}$ $I_D = 1.5 \text{ A}$
Gate to Source charge	$Q_{gs}$	—	0.4	—	nC	
Gate to Drain charge	$Q_{gd}$	—	0.6	—	nC	
Body - Drain diode forward voltage	$V_{DF}$	—	0.8	1.1	V	$I_F = 1.5 \text{ A}$ , $V_{GS} = 0$ <sup>Note3</sup>

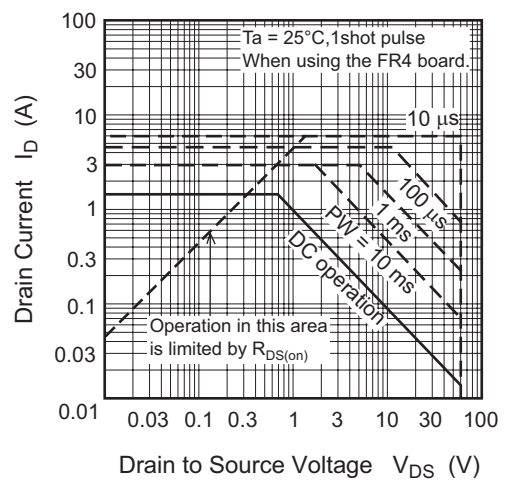
Notes: 3. Pulse test

## Main Characteristics

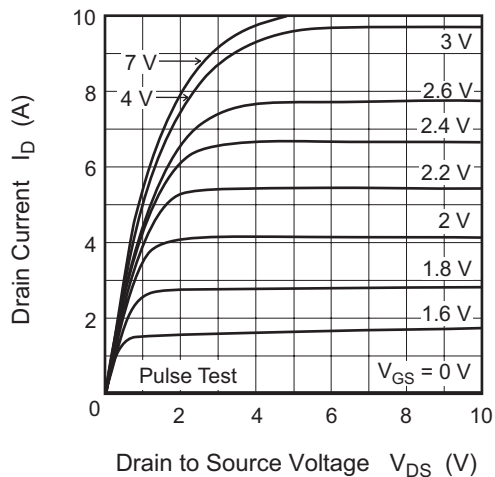
Power vs. Temperature Derating



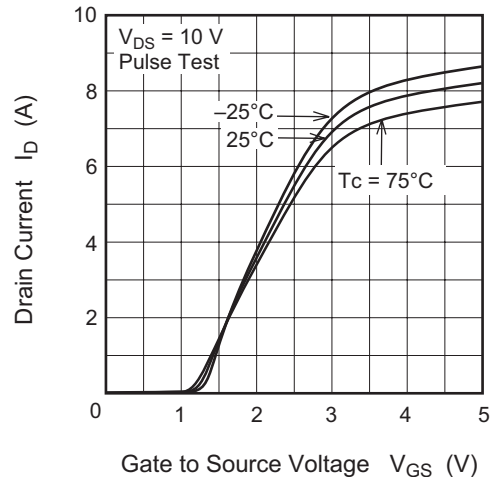
Maximum Safe Operation Area



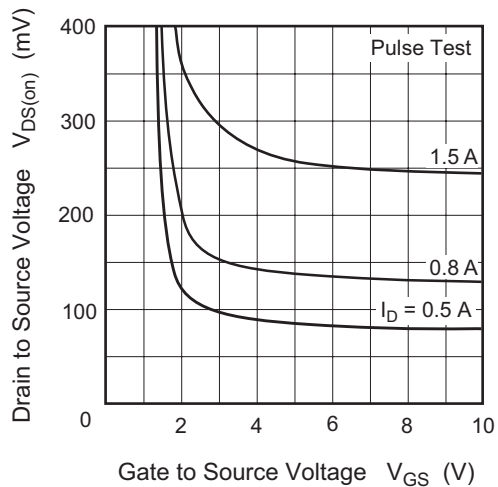
Typical Output Characteristics



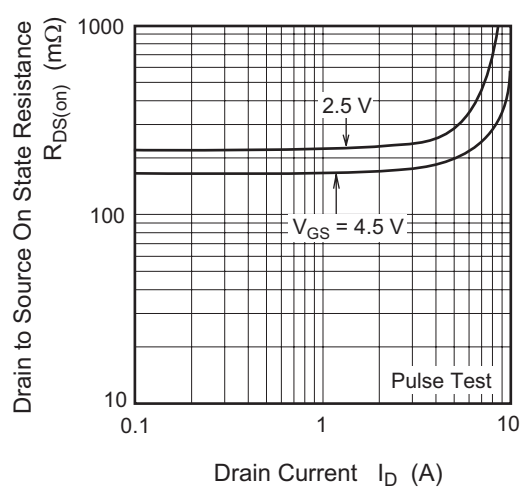
Typical Transfer Characteristics

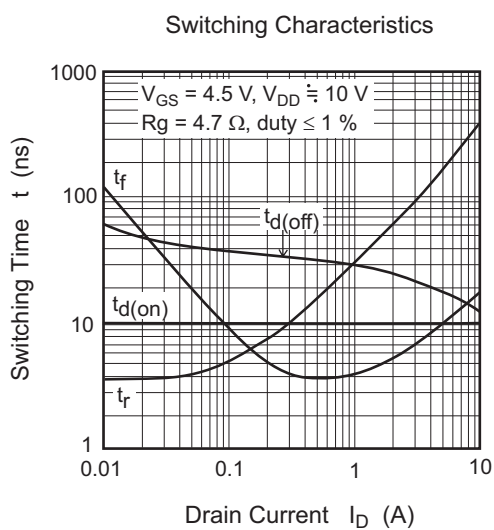
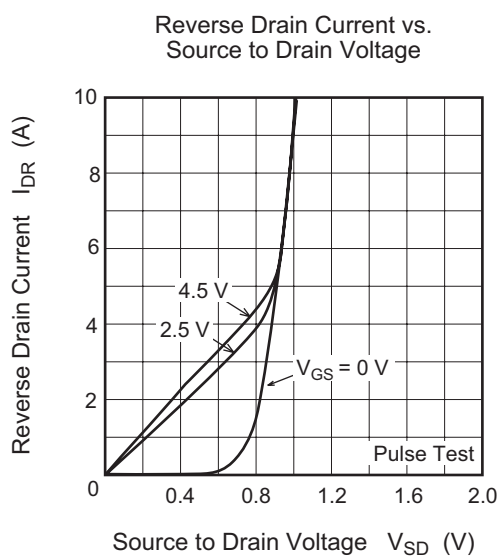
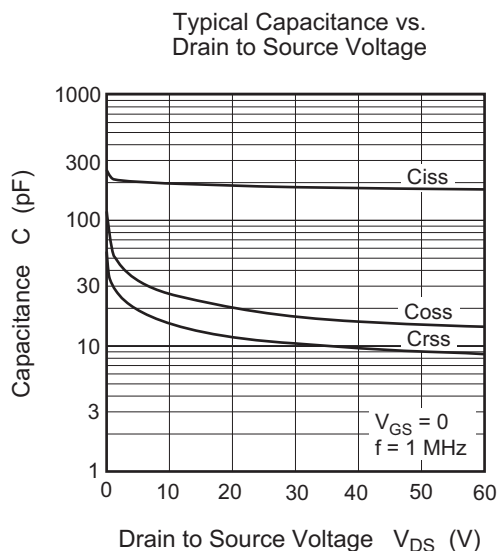
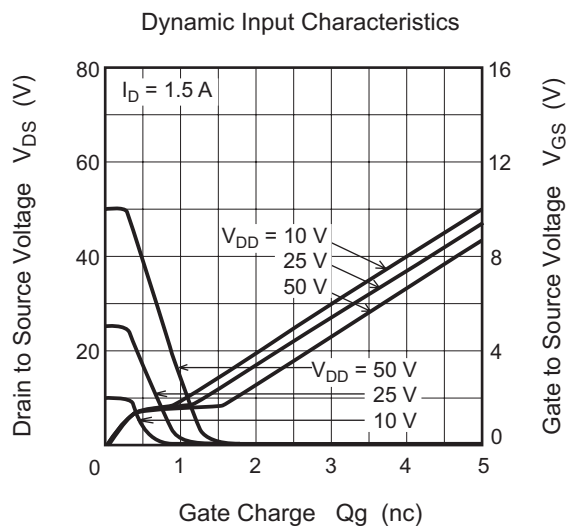
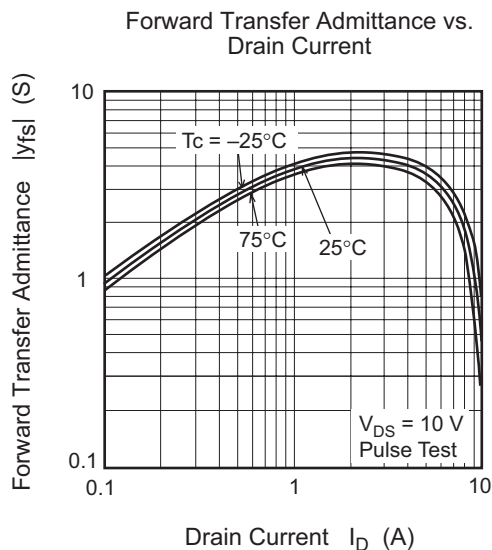
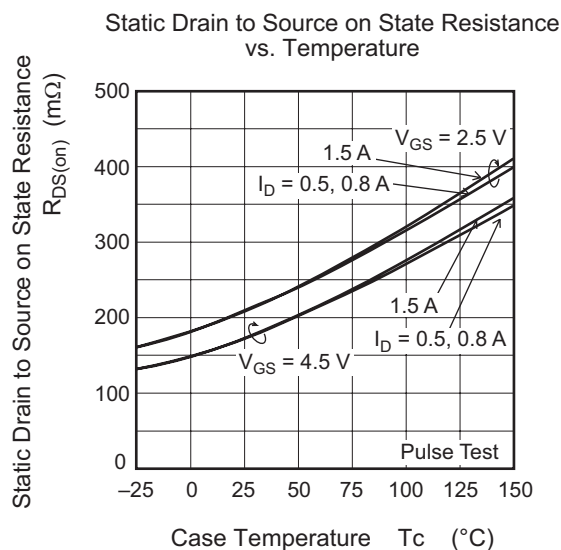


Drain to Source Saturation Voltage vs. Gate to Source Voltage

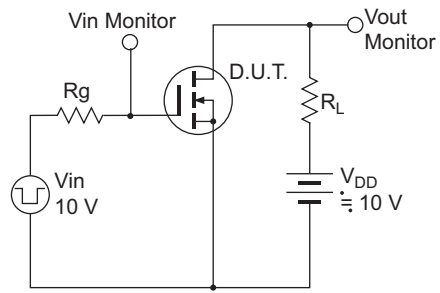


Static Drain to Source on State Resistance vs. Drain Current

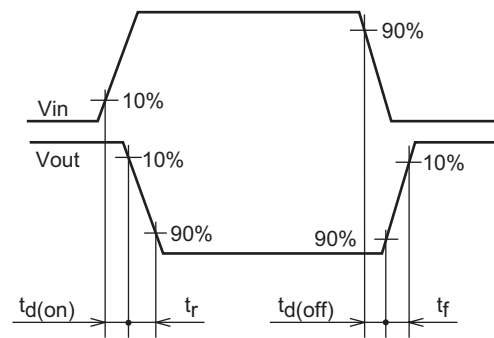




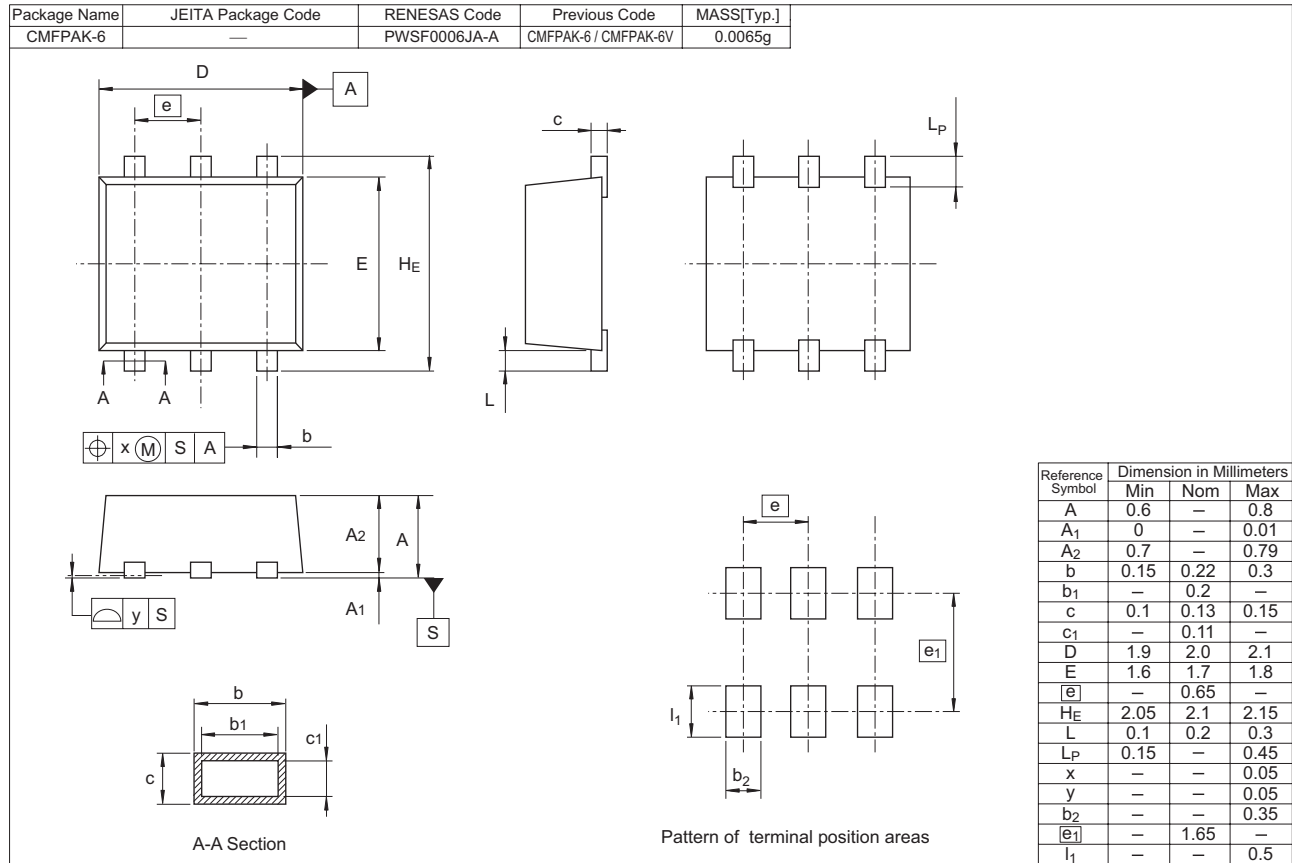
Switching Time Test Circuit



Waveform



## Package Dimensions



## Ordering Information

Part Name	Quantity	Shipping Container
HAT2282C-EL-E	3000 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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