

Over voltage and over current protection IC

Description

HCP4802 is an Over-Voltage-Protection (OVP) IC. The device will switch off internal MOSFET to disconnect VIN to OUT to protect load when any of input voltage, input current over the threshold. The Over temperature protection (OTP) function monitors chip temperature to protect the device.

Application

- PND
- Tablet
- HD Player
- OTT
- Digital Cameras
- Digital Videos

Features

- High voltage technology
- Maximum input voltage :30V
- Output power ON time :8ms(Typ)
- OVP threshold voltage :6.0V
- OVP response time :<1us
- Output auto discharge
- Small Package :DFN8L (0203-0.75-0.50)

Ordering information



XX YY: Date code

XXXXX.1: Wafer batch

Fig.1 Top view

Table 1

Package	DFN8L (0203-0.75-0.50)
MOQ	3000 pcs

Typical Application

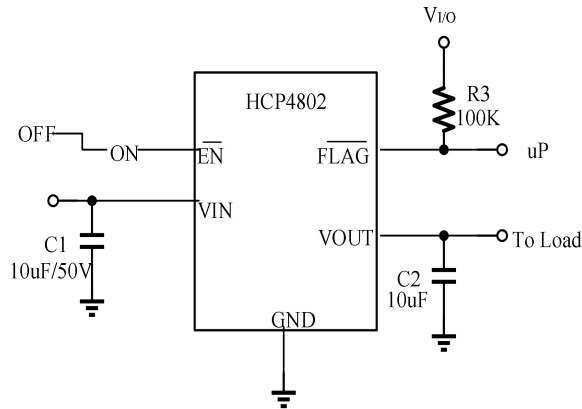


Fig.2

Pin Configuration and Function

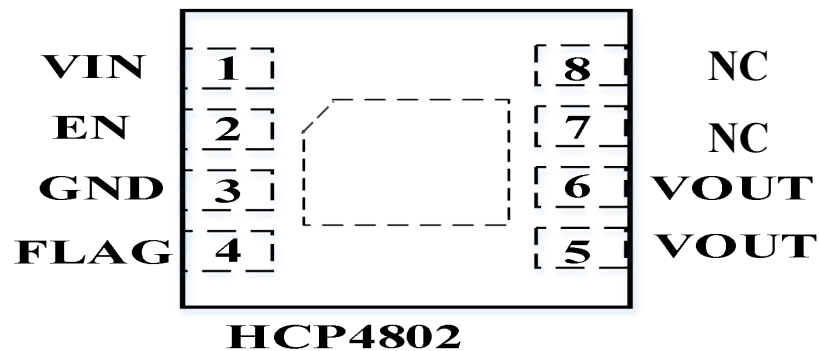


Fig.3 Pin configuration (Top view)

Table 2

NO.	Symbol	Type	Description
1	VIN	INPUT	Input pin. A 10uF low ESR ceramic capacitor or larger must be connected as close as to this pin. It is recommended to use 50V capacitor or according to application.
2	EN	INPUT	Enable pin. Active Low.
3	GND	GROUND	Ground
4	FLAG	OUTPUT	Flag Pin. Open-Drain, Active low if any OVP, OTP occur.
5/6	VOUT	OUTPUT	Output pin. Connect to load.
7/8	NC		
Bottom pad		INPUT	Must be connect to VIN on PCB board.

Functional Block Diagram

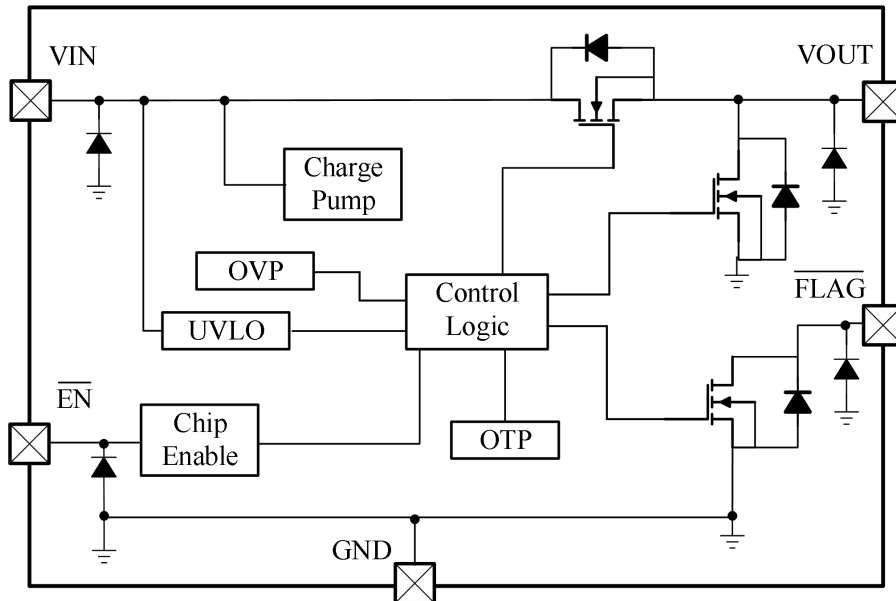


Fig.4

Absolute Maximum Ratings

Table 3

Parameter	Symbol	Value	Unit
Input voltage (VIN pin)	V _{IN}	-0.3 ~ 30	V
Output voltage (VOUT pin)	V _{OUT}	-0.3 ~ 30	V
Junction temperature	T _J	150	°C
Storage temperature	T _{stg}	-55 ~ 150	°C
ESD Ratings	HBM	±3000	V
	MM	±200	V

Note: These are stress ratings only. Stresses exceeding the range specified under “Absolute Maximum Ratings” may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

Recommend Operating Conditions

Table 4(T_a=25°C, unless otherwise noted)

Parameter	Symbol	Value	Unit
Input voltage	V _{IN}	3.5 ~ 30	V
Output current	I _{OUT}	3.5	A
Ambient operating temperature	T _{opr}	-40 ~ 85	°C

Electrical Characteristics

Table 5

Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
DC characteristics and Power-ON-Reset						
Input quiescent current	I _Q	V _{in} =5V, I _{out} =0A		170	260	uA
IN-to-OUT ON resistance	R _{ON}	V _{in} =5V, I _{out} =0.5A		32@4.5V		mΩ
Output auto discharge resistance	R _{DISCHARGE}			500		Ω
Under voltage lock out threshold	UVLO	V _{in} increasing from 0~3.8V		3.4		V
Under voltage lock out hysteresis	V _{HYS-UVLO}	V _{in} decreasing from 3.8~0V		300		mV
Output power-on time	T _{ON}	V _{in} =0 -> 5V to output ON	6	8	10	ms
EN Threshold Voltage	V _{ENL}				0.4	V
	V _{ENH}		1.2			V
EN to GND current	I _{EN}				2	uA
Input Over-Voltage-Protection (OVP)						
OVP threshold Voltage	V _{OVP(th)}		5.8	6.0	6.2	V
OVP active time	T _{OVP}	V _{IN} = 5 -> 10V			1	us
OVP recovery time	T _{ON(OVP)}	V _{IN} =10 -> 5V to output ON	6	8	10	ms
Over-Temperature-Protection (OTP)						
OTP threshold				155		°C
OTP hysteresis				40		°C
Power Switch Body Diode						
Forward peak surge current	I _{FSM}	Pulse Width=10ms			15	A
		Pulse Width=20us			50	A

Typical characteristic (Ta=25°C, unless otherwise noted)

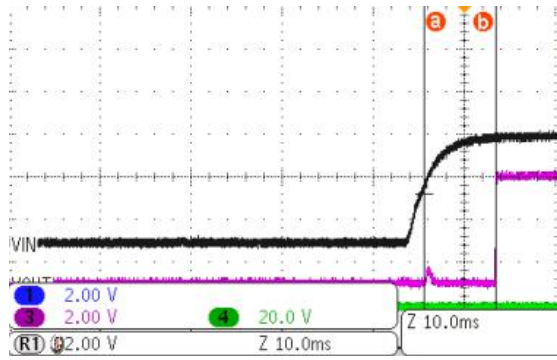


Fig.5 Power ON time

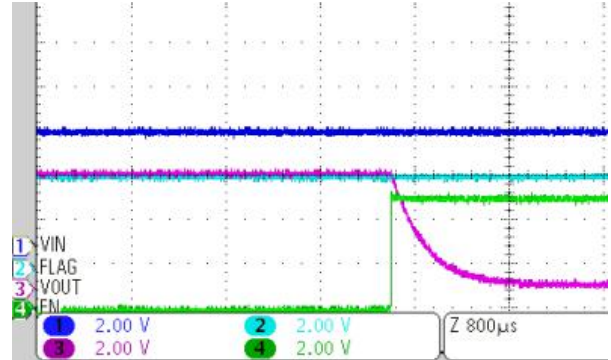


Fig.7 EN_able

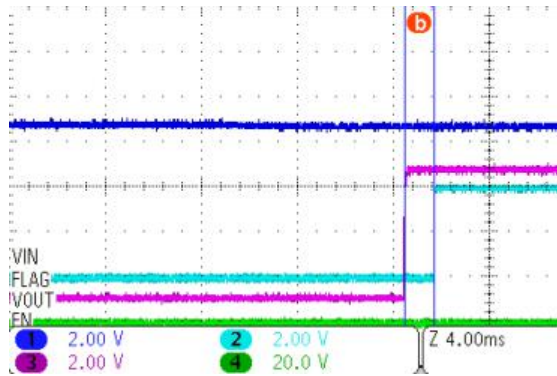


Fig.6 FLAG delay time

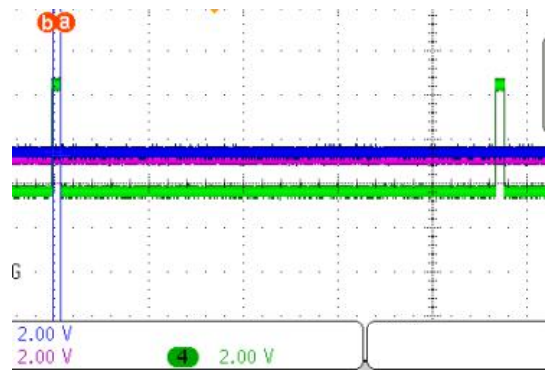
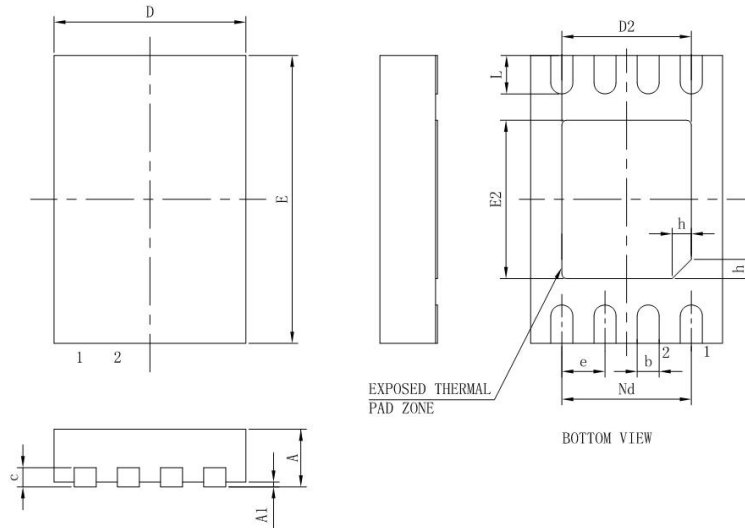


Fig.8 OCP_restart_detection

Package Outline

DFN8L (0203-0.75-0.50)


Fig. 9 Package outline
Table 6 (Unit: mm)

Symbol	Min	Type	Max
A	0.70	0.75	0.80
A1	-	0.02	0.05
b	0.20	0.25	0.30
c	0.18	0.20	0.25
D	1.90	2.00	2.10
D2	1.40	1.50	1.60
E	2.90	3.00	3.10
E2	1.50	1.60	1.70
e	0.50BSC		
Nd	1.50BSC		
L	0.30	0.40	0.50
h	0.20	0.25	0.30