

HD29413

Quadruple Differential Line Receivers With 3 State Outputs

REJ03D0306-0200Z (Previous ADE-205-582 (Z)) Rev.2.00 Jul.16.2004

Description

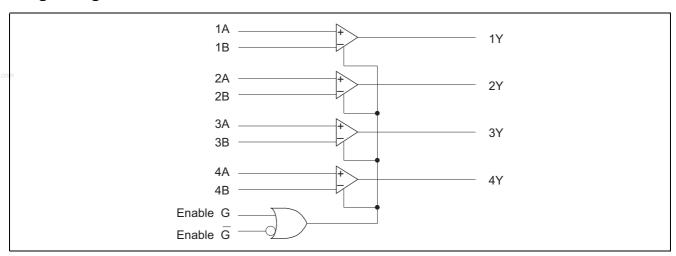
The HD29413 features quadruple differential line receivers designed to meet the spec of EIA RS-422AandRS-423A. The device operates from a single 5 V power supply. The enable function is common to all four receivers and offer a choice of active high or active low inputs. (Complementary output enable input.) Faile safe circuit guarantees the outputs always at the high level when the inputs are open.

Features

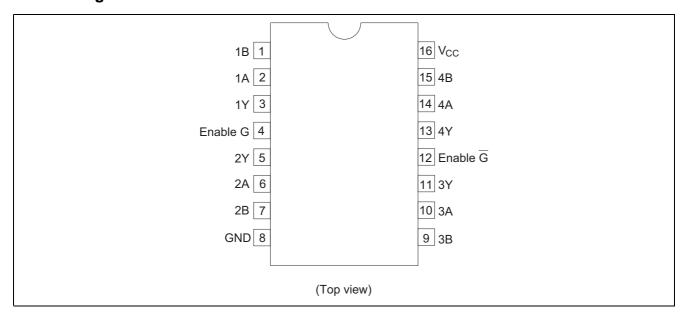
• Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD29413P	DILP-16 pin	DP-16E, -16FV	Р	_

Logic Diaglam



Pin Arrangement



Function Table

Differential Input	Enable	Output	
$V_{IA} - V_{IB}$	G	G	Υ
+	Н	x	Н
	X	L	Н
_	Н	x	L
_	X	L	L
X	L	Н	Z

H : High levelL : Low levelX : IrrelevantZ : High impedance

Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply Voltage	V _{CC} *1	+7	V
In Phase Input Voltage	V _{IC} * ²	-25 to +25	V
Differential Input Voltage	V _{ID} *3	0 to +25	V
Enable Input Voltage	V _{IN}	+7	V
Output Sink Current	Io	+50	mA
Operating Temperature	Topr	0 to +70	°C
Storage Temperature	Tstg	-65 to +150	°C

Notes: 1. All voltage values except for differential input voltage are with respect to ground terminal.

- 2. $V_{IC} = 1/2 (V_{IA} + V_{IB}) |V_{ID}| = |V_{IA} V_{IB}|$
- 3. Differential input voltage is measured at the noninverting input with respect to the corresponding inverting input
- 4. The absolute maximum ratings are values which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Min	Тур	Max	Unit
Supply Voltage	V _{cc}	4.75	5.0	5.25	V
In Phase Input Voltage	V _{IC}	- 7	_	+7	V
Differential Input Voltage	V_{ID}	+0.3	_	+6.0	V
Output Current	I _{OH}	_	_	-440	μΑ
	I _{OL}	_	_	8	mA
Operating Temperature	Topr	0	_	70	°C

Electrical Characteristics (Ta = 0 to + 70°C)

Item	Symbol	Min	Typ*1	Max	Unit	Conditions				
Differential Input High	V_{TH}		_	0.3	٧	$V_{CC} = 5 \text{ V} \pm 5 \text{ %}, V_{OH} \ge 2.7 \text{ V}, I_{OH} = -440 \mu\text{A}$				
Threshold Voltage						$V_{IC} = -7 \text{ to } +7 \text{ V}$,			
Differential Input Low	V_{TL}	_	_	-0.3	٧		$V_{OL} \le 0.4 \text{ V, I}_{OL} =$	4 mA		
Threshold Voltage										
Enable Input Voltage	V_{IH}	2.0	_	_	V					
	V_{IL}	_	_	8.0	V					
Enable Input Clamp	V_{IK}	_	_	-1.5	V	$V_{CC} = 4.75 \text{ V}, I_{IN} = -18 \text{ mA}$				
Voltage										
Output Voltage	V_{OH}	2.7	_	_	V	$V_{CC} = 4.75 \text{ V}$	$V_{ID} = 0.3 \text{ to } 6 \text{ V}$	I _{OH} = -440 μA		
	V_{OL}	_	_	0.4	V	$V_{IL}(\overline{G}) = 0.8 \text{ V}$	$V_{ID} = -0.3 \text{ to } -6 \text{ V}$	I _{OL} = 4mA		
		_	—	0.45	V	$V_{IH}(G) = 2 V$		I _{OL} = 8 mA		
Off State (High	I _{OZ}	_	_	20	μΑ	$V_{CC} = 5.25 \text{ V}$		$V_0 = 2.4 \text{ V}$		
impedance) Output		_	_	-20	μΑ	$V_{IL}(G) = 0.8 V,$	$V_{IH}(\overline{G}) = 2 V$	$V_0 = 0.4 \text{ V}$		
Current										
Line Input Current	I _{IN}	_	_	2.2	mΑ	$V_{CC} = 5.25 \text{ V or}$	$V_{CC} = 0 V$	V _I = -10 V		
		0	_	1.0	mΑ			V ₁ = 3 V		
		0	_	-1.0	mΑ			V ₁ = -3 V		
		_	_	-2.2	mΑ			V _I = -10 V		
Enable Input Current	I _{I(EN)}	_	_	100	μΑ	$V_{CC} = 5.25 \text{ V}$		V _I = 5.5 V		
	I _{IH}	_	_	20	μΑ			V _I = 2.7 V		
	I _{IL}	_	_	-0.36	mΑ]		V _I = 0.4 V		
Short Circuit Output	I _{OS} *2	-15	_	-85	mΑ	$V_{CC} = 5.25 \text{ V}, \text{ V}_{C}$	_o = 0 V			
Current										
Supply Current	I _{cc}			70	mΑ	$V_{CC} = 5.25 \text{ V}, \text{ V}_{I}$	= 0 V (All Output D	Disable)		

Notes: 1. All typical values are at $V_{CC} = 5 \text{ V}$, $Ta = 25^{\circ}\text{C}$, $V_{IC} = 0$

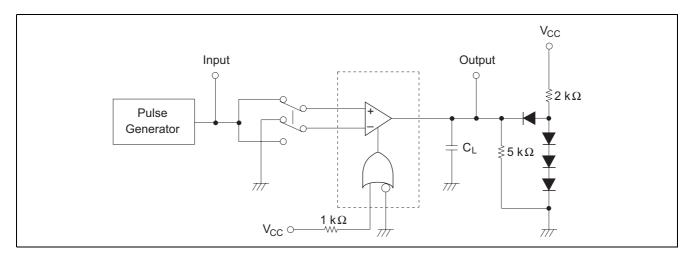
Switching Characteristics ($V_{CC} = 5 \text{ V}$, $Ta = 25^{\circ}\text{C}$)

Item	Symbol	Min	Тур	Max	Unit	Conditions
Propagation Delay Time	t_{PLH} , t_{PHL}	_	17	25	ns	C _L = 15 pF
Output Enable Time	t_{ZH}, t_{ZL}	_	15	22	ns	
Output Disable Time	t_{HZ}	_	15	22	ns	C _L = 5 pF
	t_{LZ}	_	20	30	ns	

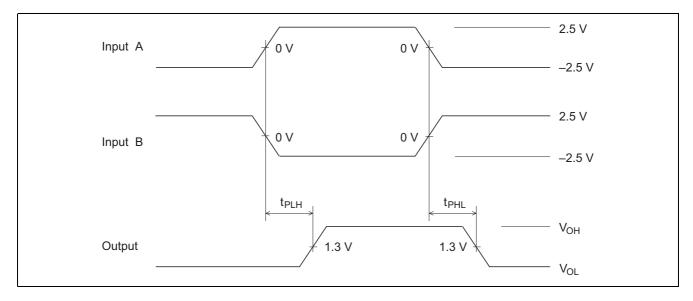
^{2.} Not more than one output should be shorted at a time.

$1. \ t_{PLH}, t_{PHL}$

Test Circuit

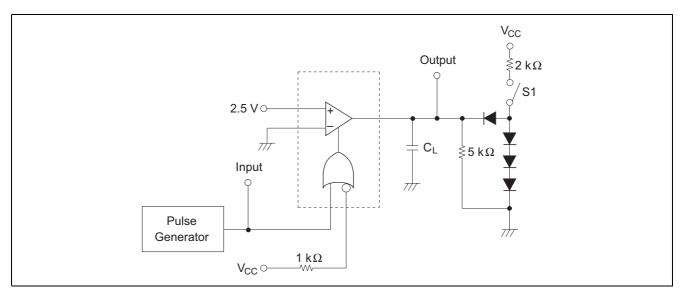


Waveforms

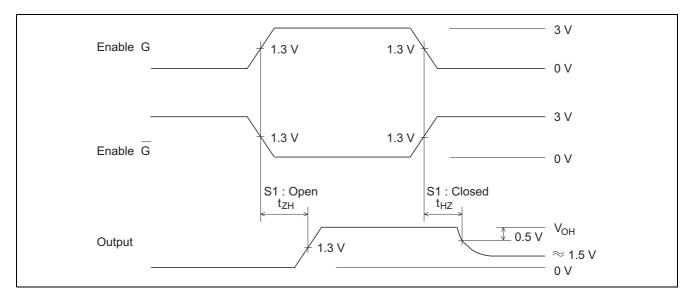


$2. \quad t_{HZ}, \, t_{ZH}$

Test Circuit

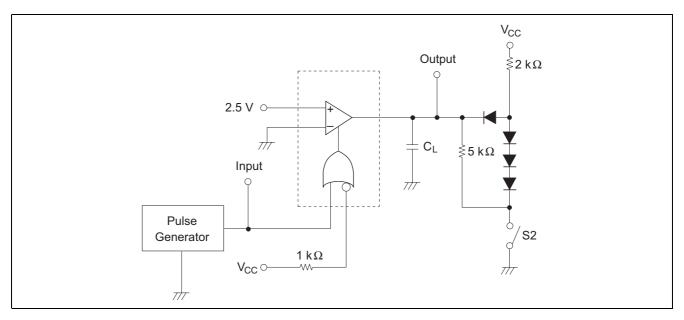


Waveforms

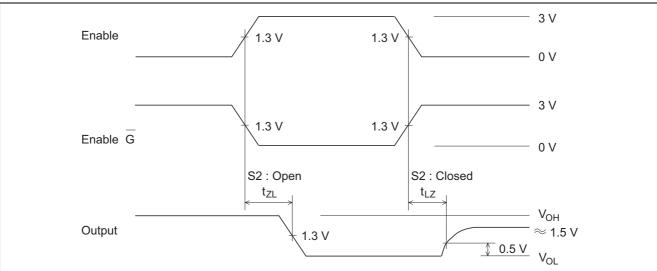


3. t_{LZ} , t_{ZL}

Test Circuit



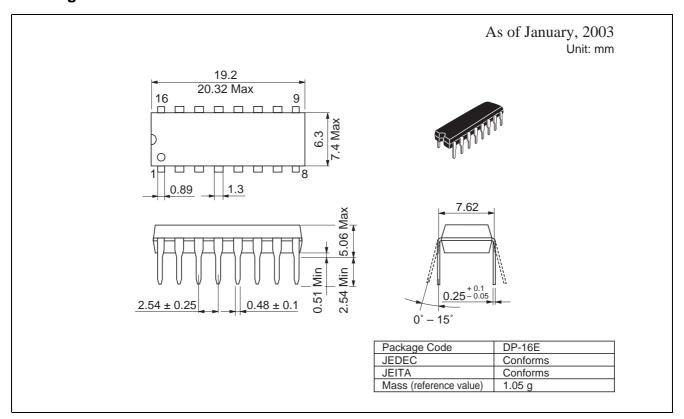
Waveforms

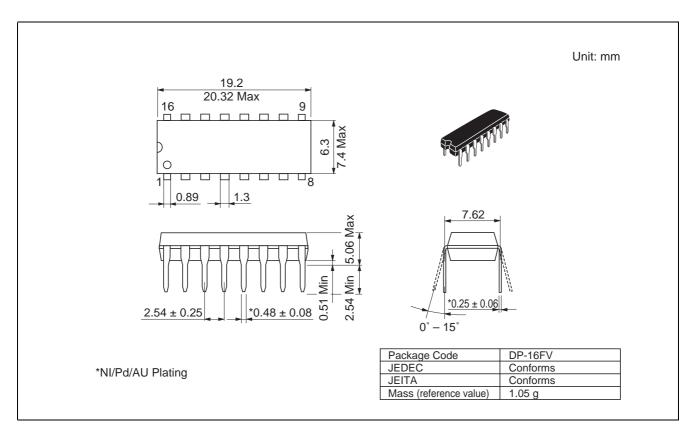


Notes:

- 1. The pulse generator has the following characteristics: PRR = 1 MHz duty cycle 50%, $t_r \le 15$ ns, $t_i \le 6$ ns, Zout = 50 Ω .
- 2. C_L include probe and jig capacitance.
- 3. All diodes are 1S2074(H)
- 4. To test G input, ground G input and apply an inverted input waveform.

Package Dimensions





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