

HD74HC157/HD74HC158

Quad. 2-to-1-line Data Selectors / Multiplexers
(with noninverted outputs)

Quad. 2-to-1-line Data Selectors / Multiplexers
(with inverted outputs)

REJ03D0578-0200
(Previous ADE-205-454)
Rev.2.00
Oct 11, 2005

Description

These devices each consist of four 2-input digital multiplexers with common select and strobe inputs. On the HD74HC157, when the strobe input is at logical “L” the four outputs assume the values as selected from the inputs. When the strobe input is at a logical “H” the outputs assume logical “L”. The HD74HC158 operates in the same manner, except that its outputs are inverted. Select decoding is done internally resulting in a single select input only. If enabled, the select input determines whether the A or B inputs get routed to their corresponding Y outputs.

Features

- High Speed Operation: t_{pd} (Data to Output) = 12 ns typ ($C_L = 50$ pF)
- High Output Current: Fanout of 10 LSTTL Loads
- Wide Operating Voltage: $V_{CC} = 2$ to 6 V
- Low Input Current: 1 μ A max
- Low Quiescent Supply Current: I_{CC} (static) = 4 μ A max ($T_a = 25^\circ\text{C}$)
- Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74HC157P HD74HC158P	DILP-16 pin	PRDP0016AE-B (DP-16FV)	P	—
HD74HC157FPEL HD74HC158FPEL	SOP-16 pin (JEITA)	PRSP0016DH-B (FP-16DAV)	FP	EL (2,000 pcs/reel)
HD74HC158RPEL	SOP-16 pin (JEDEC)	PRSP0016DG-A (FP-16DNV)	RP	EL (2,500 pcs/reel)
HD74HC157TELL	TSSOP-16 pin	PTSP0016JB-A (TTP-16DAV)	T	ELL (2,000 pcs/reel)

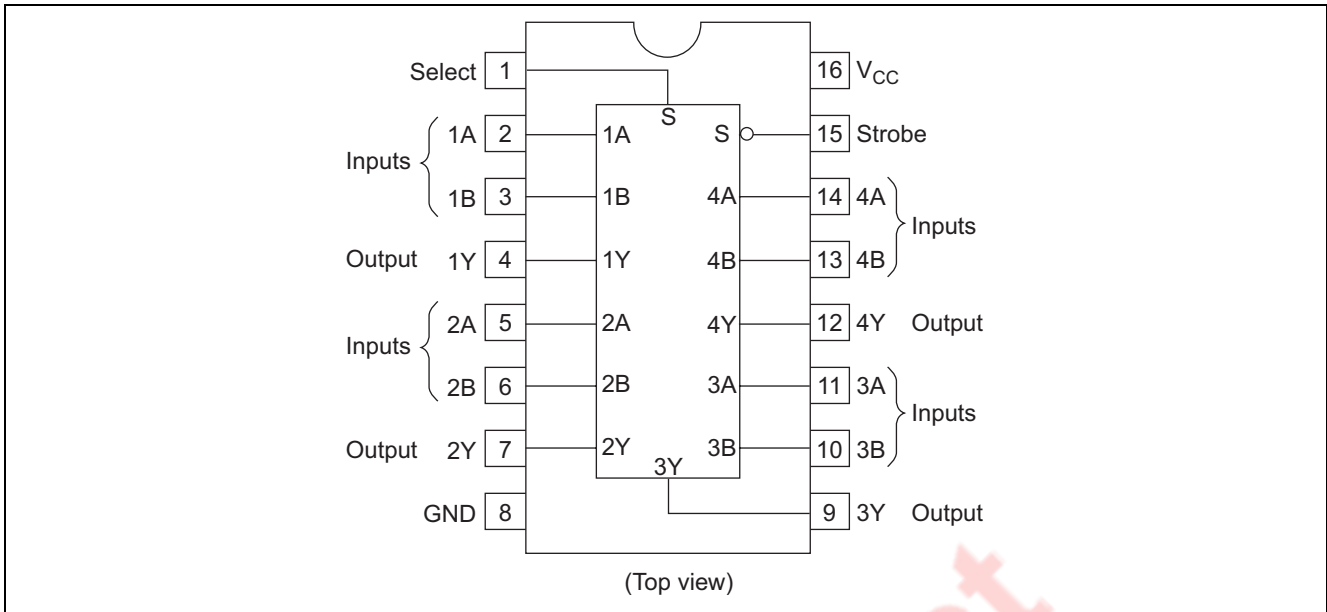
Note: Please consult the sales office for the above package availability.

Function Table

Inputs				Output Y	
Strobe	Select	A	B	HC157	HC158
H	X	X	X	L	H
L	L	L	X	L	H
L	L	H	X	H	L
L	H	X	L	L	H
L	H	X	H	H	L

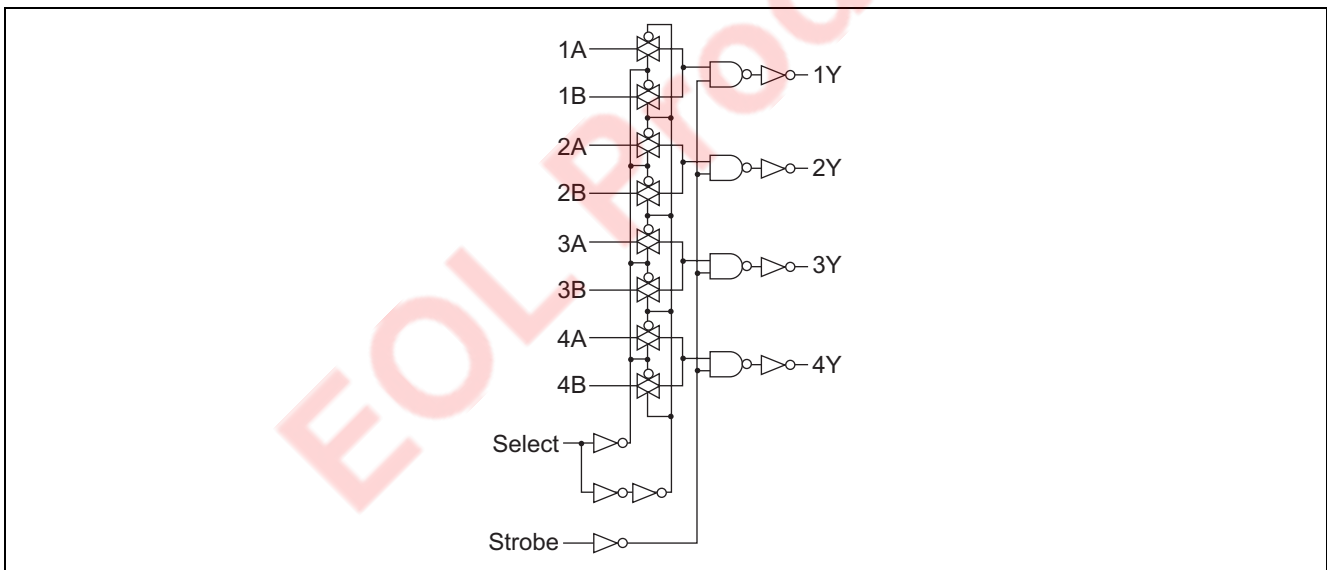
H: High level
L: Low level
X: Irrelevant

Pin Arrangement

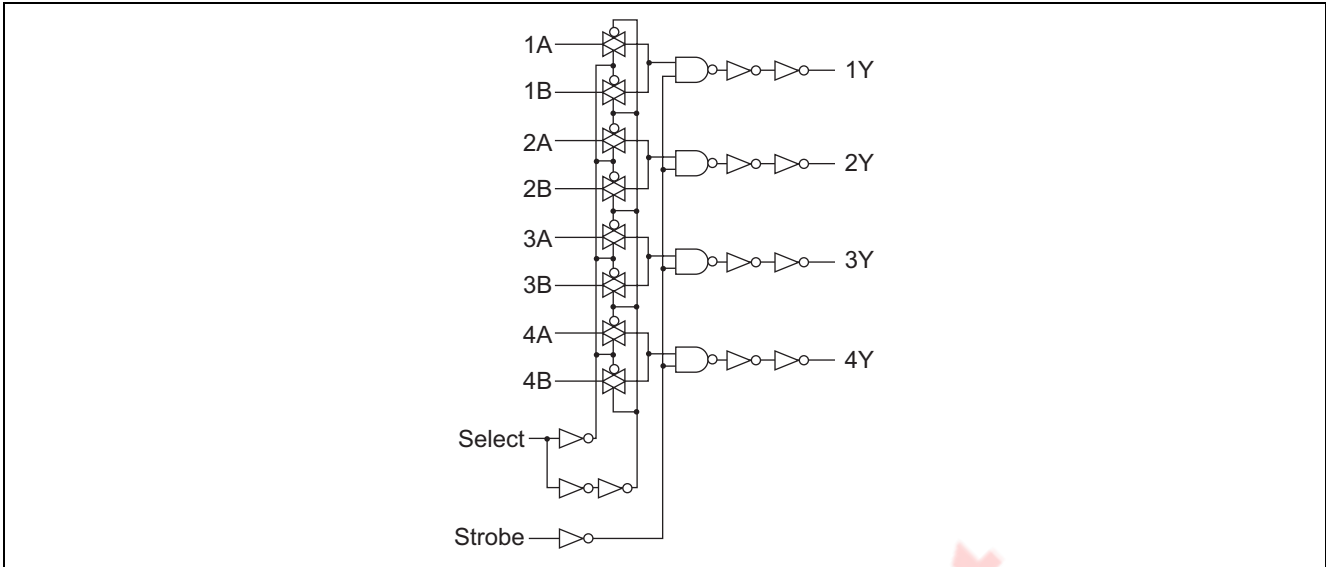


Logic Diagram

HD74HC157



HD74HC158



Absolute Maximum Ratings

Item	Symbol	Rating	Unit
Supply voltage range	V_{CC}	-0.5 to +7.0	V
Input voltage	V_{IN}	-0.5 to $V_{CC} + 0.5$	V
Output voltage	V_{OUT}	-0.5 to $V_{CC} + 0.5$	V
Output current	I_{OUT}	± 25	mA
DC current drain per V_{CC} , GND	I_{CC} , I_{GND}	± 50	mA
DC input diode current	I_{IK}	± 20	mA
DC output diode current	I_{OK}	± 20	mA
Power dissipation per package	P_T	500	mW
Storage temperature	T_{stg}	-65 to +150	$^{\circ}C$

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V_{CC}	2 to 6	V	
Input / Output voltage	V_{IN} , V_{OUT}	0 to V_{CC}	V	
Operating temperature	T_a	-40 to 85	$^{\circ}C$	
Input rise / fall time ^{*1}	t_r , t_f	0 to 1000	ns	$V_{CC} = 2.0 V$
		0 to 500		$V_{CC} = 4.5 V$
		0 to 400		$V_{CC} = 6.0 V$

Note: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

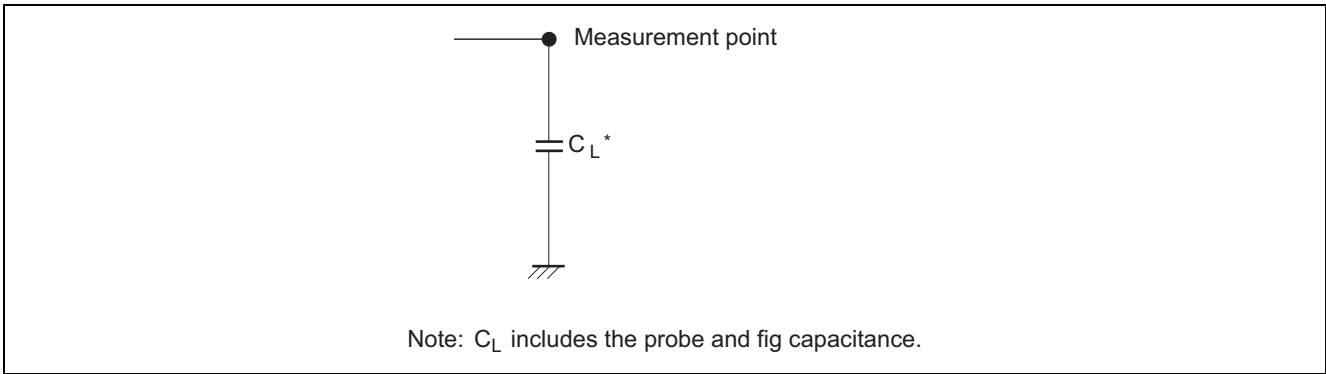
Electrical Characteristics

Item	Symbol	V _{CC} (V)	Ta = 25°C			Ta = -40 to +85°C		Unit	Test Conditions	
			Min	Typ	Max	Min	Max			
Input voltage	V _{IH}	2.0	1.5	—	—	1.5	—	V		
		4.5	3.15	—	—	3.15	—			
		6.0	4.2	—	—	4.2	—			
	V _{IL}	2.0	—	—	0.5	—	0.5	V		
		4.5	—	—	1.35	—	1.35			
		6.0	—	—	1.8	—	1.8			
Output voltage	V _{OH}	2.0	1.9	2.0	—	1.9	—	V	V _{in} = V _{IH} or V _{IL}	I _{OH} = -20 μA
		4.5	4.4	4.5	—	4.4	—			I _{OH} = -4 mA
		6.0	5.9	6.0	—	5.9	—			I _{OH} = -5.2 mA
		4.5	4.18	—	—	4.13	—			
		6.0	5.68	—	—	5.63	—			
	V _{OL}	2.0	—	0.0	0.1	—	0.1	V	V _{in} = V _{IH} or V _{IL}	I _{OL} = 20 μA
		4.5	—	0.0	0.1	—	0.1			
		6.0	—	0.0	0.1	—	0.1			
		4.5	—	—	0.26	—	0.33			I _{OL} = 4 mA
		6.0	—	—	0.26	—	0.33			I _{OL} = 5.2 mA
Input current	I _{in}	6.0	—	—	±0.1	—	±1.0	μA	V _{in} = V _{CC} or GND	
Quiescent supply current	I _{CC}	6.0	—	—	4.0	—	40	μA	V _{in} = V _{CC} or GND, I _{out} = 0 μA	

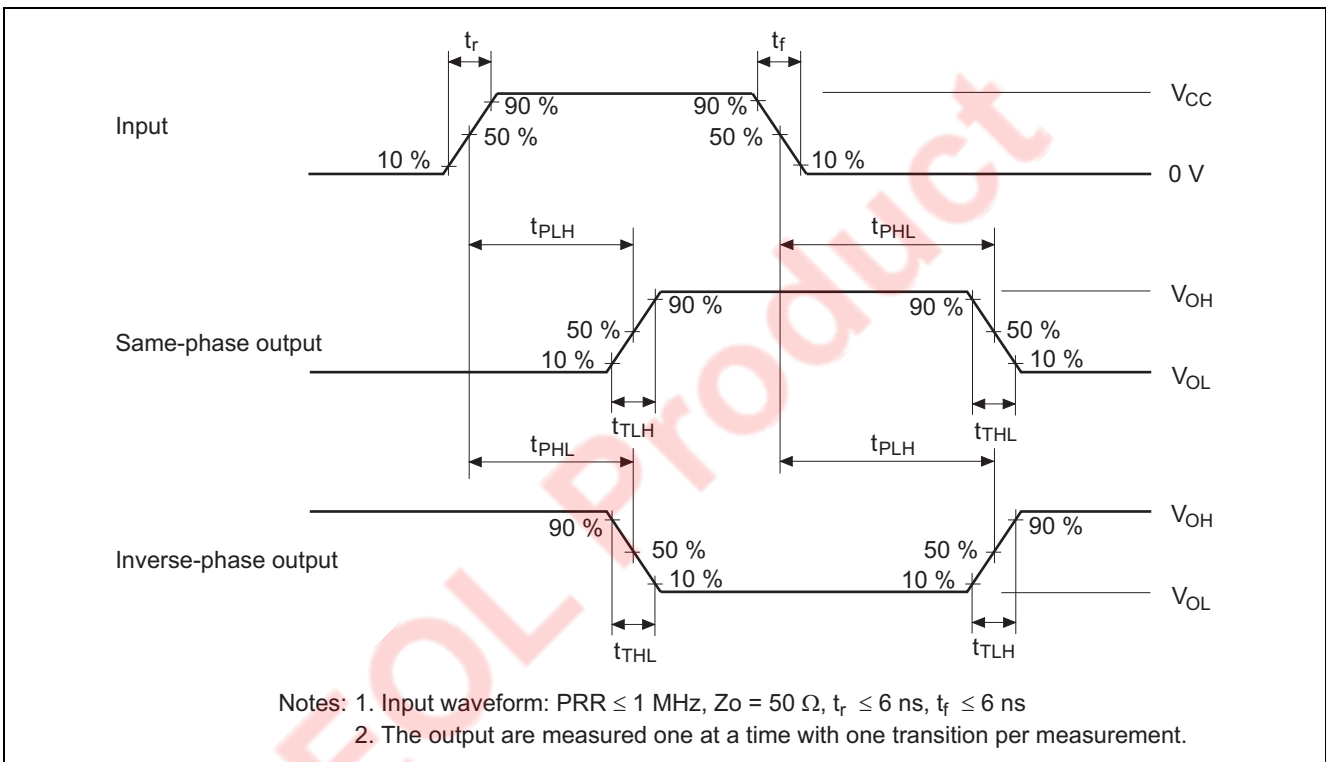
Switching Characteristics (C_L = 50 pF, Input t_r = t_f = 6 ns)

Item	Symbol	V _{CC} (V)	Ta = 25°C			Ta = -40 to +85°C		Unit	Test Conditions	
			Min	Typ	Max	Min	Max			
Propagation delay time	t _{PLH} , t _{PHL}	2.0	—	—	125	—	155	ns	Data to output	HD74HC157 only
		4.5	—	12	25	—	31			
		6.0	—	—	21	—	26			
	t _{PLH} , t _{PHL}	2.0	—	—	110	—	140	ns		HD74HC158 only
		4.5	—	12	22	—	28			
		6.0	—	—	19	—	24			
	t _{PHL}	2.0	—	—	125	—	155	ns	Select to output	
		4.5	—	13	25	—	31			
		6.0	—	—	21	—	26			
	t _{PLH}	2.0	—	—	160	—	200	ns		
		4.5	—	17	32	—	40			
		6.0	—	—	27	—	34			
	t _{PHL}	2.0	—	—	160	—	200	ns	Strobe to output	
		4.5	—	12	32	—	40			
		6.0	—	—	27	—	34			
t _{PLH}	2.0	—	—	160	—	200	ns			
	4.5	—	12	32	—	40				
	6.0	—	—	27	—	34				
Output rise/fall time	t _{TLH} , t _{THL}	2.0	—	—	75	—	95	ns		
		4.5	—	5	15	—	19			
		6.0	—	—	13	—	16			
Input capacitance	C _{in}	—	—	5	10	—	10	pF		

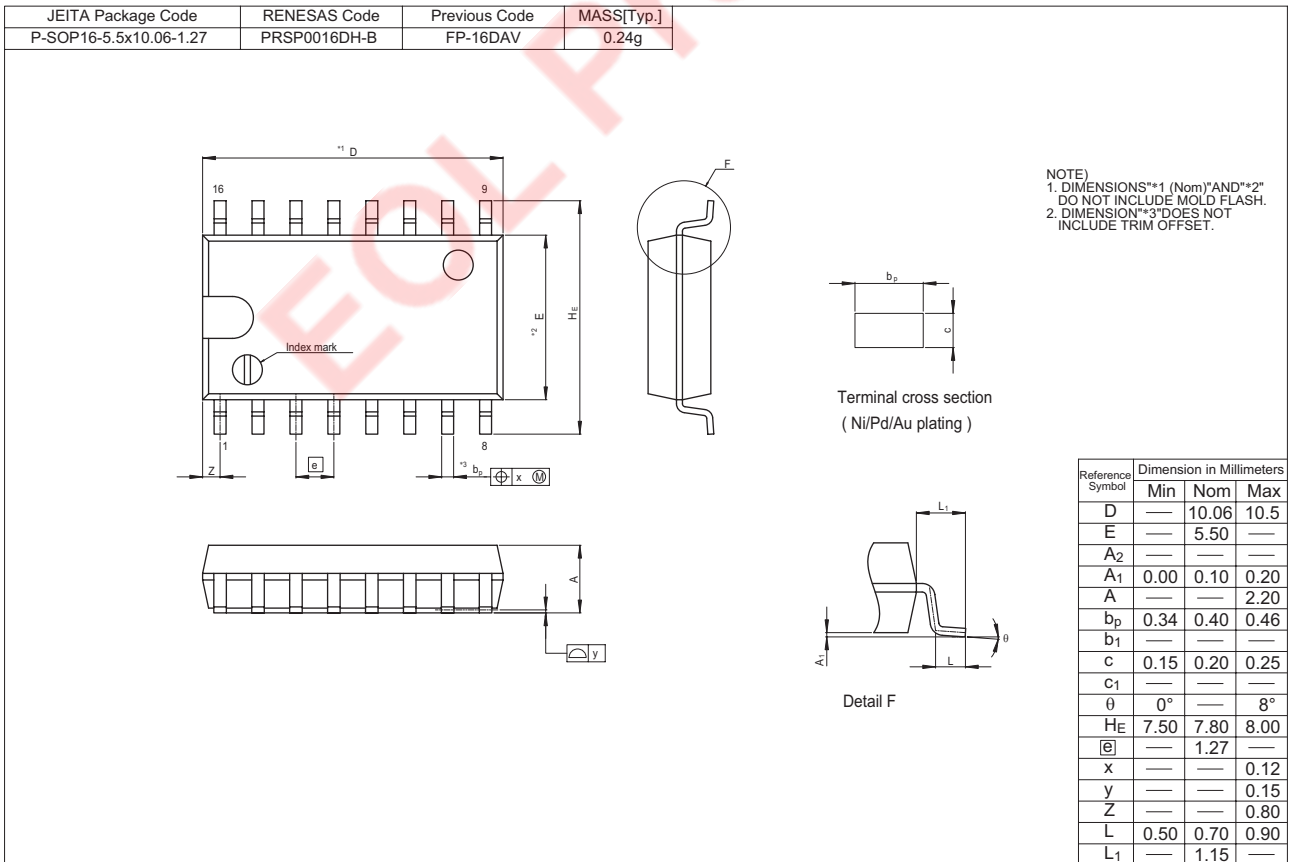
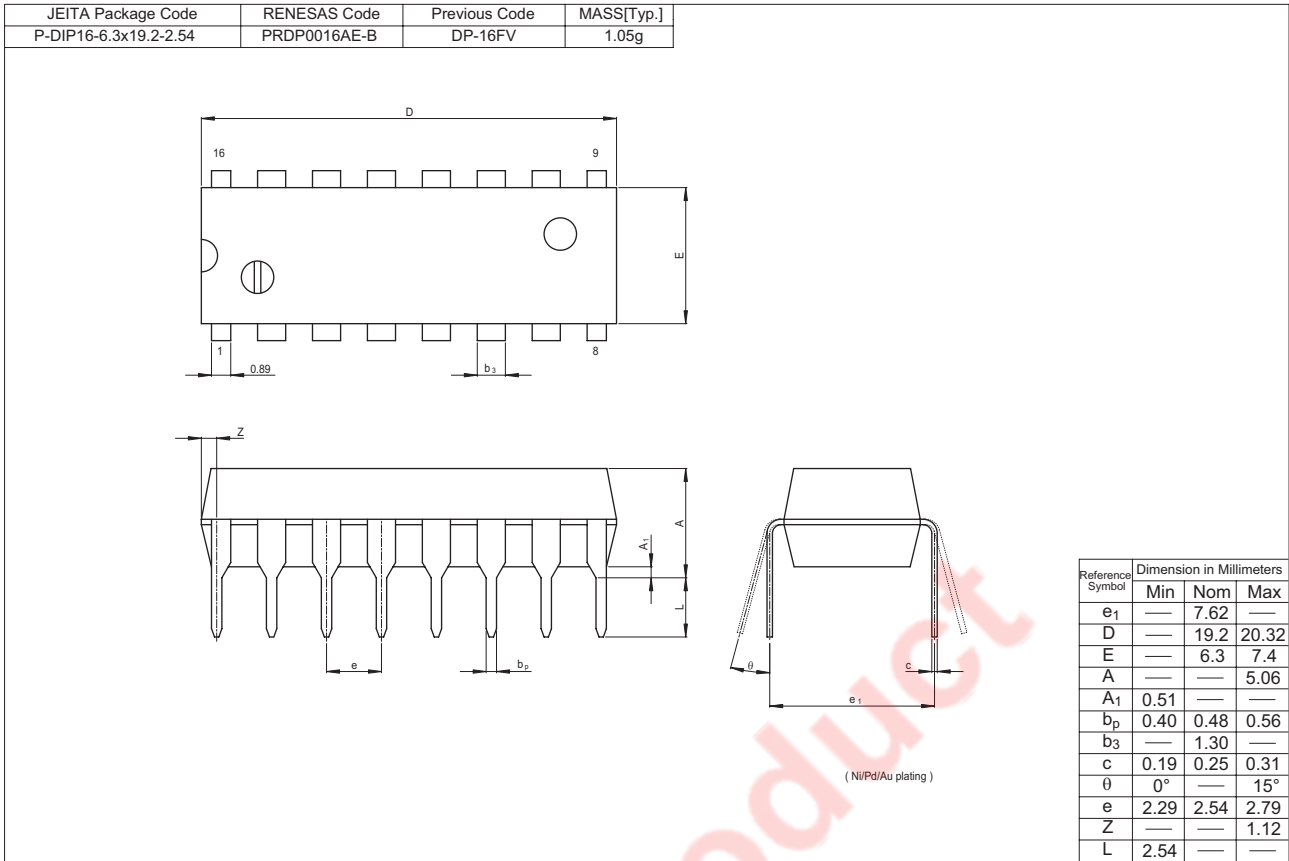
Test Circuit



Waveforms

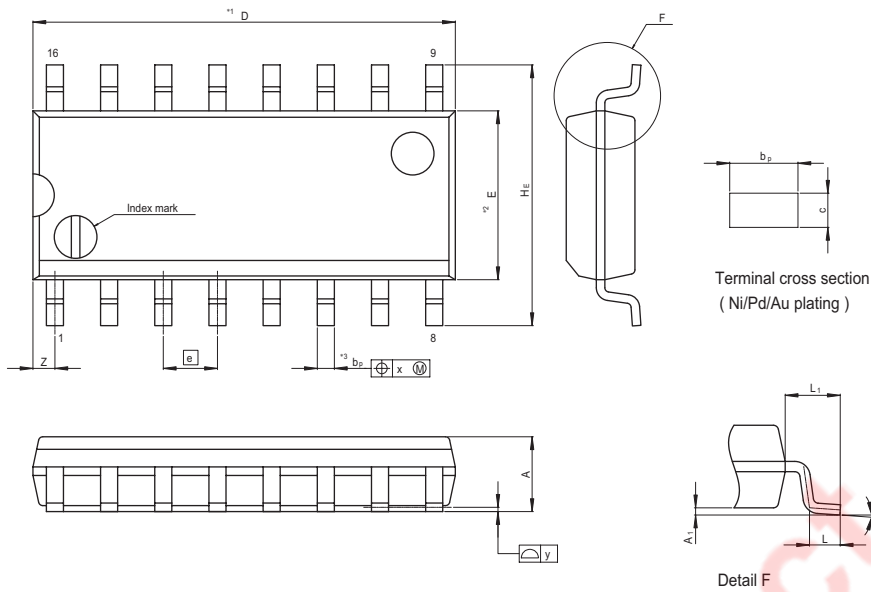


Package Dimensions



HD74HC157, HD74HC158

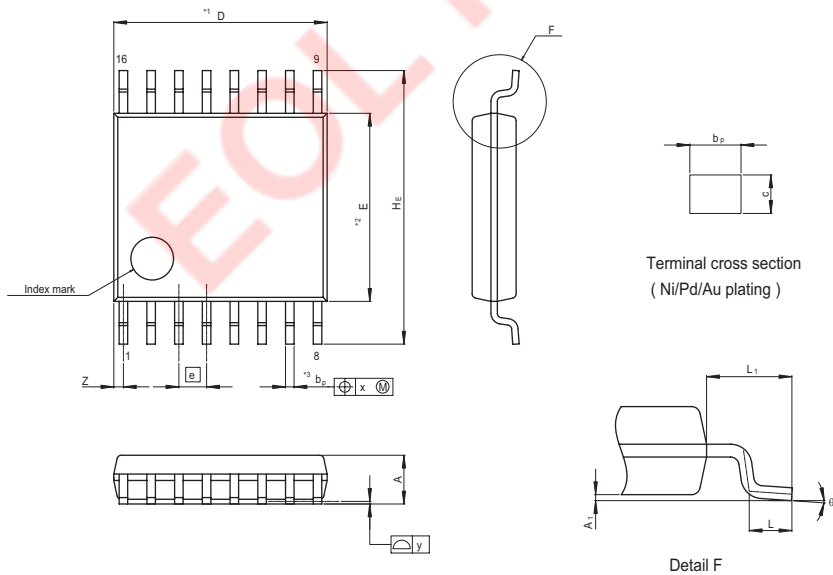
JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
P-SOP16-3.95x9.9-1.27	PRSP0016DG-A	FP-16DNV	0.15g



NOTE)
 1. DIMENSIONS*1 (Nom)*AND*2*
 DO NOT INCLUDE MOLD FLASH.
 2. DIMENSION*3*DOES NOT
 INCLUDE TRIM OFFSET.

Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
D	—	9.90	10.30
E	—	3.95	—
A ₂	—	—	—
A ₁	0.10	0.14	0.25
A	—	—	1.75
b _p	0.34	0.40	0.46
b ₁	—	—	—
c	0.15	0.20	0.25
c ₁	—	—	—
θ	0°	—	8°
H _E	5.80	6.10	6.20
Ⓜ	—	1.27	—
x	—	—	0.25
y	—	—	0.15
Z	—	—	0.635
L	0.40	0.60	1.27
L ₁	—	1.08	—

JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
P-TSSOP16-4.4x5-0.65	PTSP0016JB-A	TTP-16DAV	0.05g



NOTE)
 1. DIMENSIONS*1 (Nom)*AND*2*
 DO NOT INCLUDE MOLD FLASH.
 2. DIMENSION*3*DOES NOT
 INCLUDE TRIM OFFSET.

Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
D	—	5.0	5.3
E	—	4.40	—
A ₂	—	—	—
A ₁	0.03	0.07	0.10
A	—	—	1.10
b _p	0.15	0.20	0.25
b ₁	—	—	—
c	0.10	0.15	0.20
c ₁	—	—	—
θ	0°	—	8°
H _E	6.20	6.40	6.60
Ⓜ	—	0.65	—
x	—	—	0.13
y	—	—	0.10
Z	—	—	0.65
L	0.4	0.5	0.6
L ₁	—	1.0	—

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