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# HD74HC4052/HD74HC4053

Dual 4-channel Analog Multiplexers/Demultiplexers/  
Triple 2-channel Analog Multiplexers/Demultiplexers

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## Description

HD74HC4052: This device connects together the outputs of 4 switches in two sets, thus achieving a pair of 4 channel multiplexers. The binary code placed on the A, and B select lines determine which switch in each 4 channel section is “on”, connecting one of the four inputs in each section to its common output. This enables the implementation of a 4 channel differential multiplexer.

HD74HC4053: This device contains 6 switches whose outputs are connected together in pairs, thus implementing a triple 2 channel multiplexer, or the equivalent of 3 single-pole-double throw configuration. Each of the A, B, or C select lines independently controls one pair of switches, selecting one of the two switches to be “on”.

## Features

- High Speed Operation
- Wide Operating Voltage
- Low Quiescent Supply Current

# HD74HC4052/HD74HC4053

## Function Table

### Control Inputs

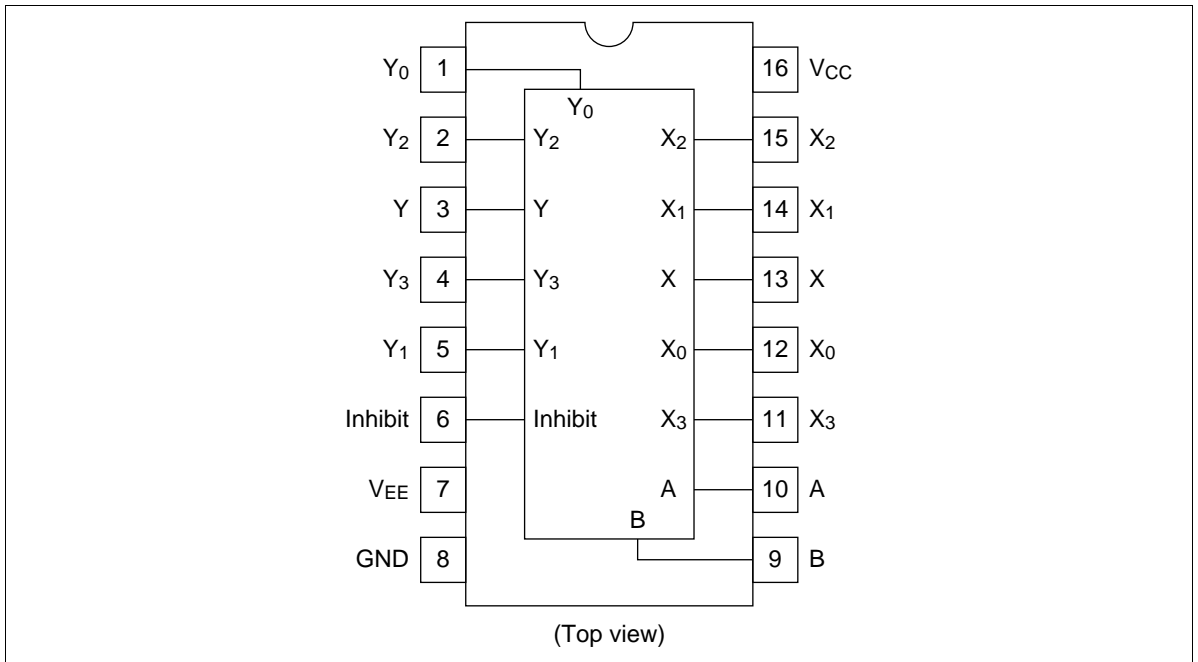
| Inhibit | Select          |   |   | ON Switch      |                |                |                |                |
|---------|-----------------|---|---|----------------|----------------|----------------|----------------|----------------|
|         | C* <sup>1</sup> | B | A | HD74HC4052     |                | HD74HC4053     |                |                |
| L       | L               | L | L | Y <sub>0</sub> | X <sub>0</sub> | Z <sub>0</sub> | Y <sub>0</sub> | X <sub>0</sub> |
| L       | L               | L | H | Y <sub>1</sub> | X <sub>1</sub> | Z <sub>0</sub> | Y <sub>0</sub> | X <sub>1</sub> |
| L       | L               | H | L | Y <sub>2</sub> | X <sub>2</sub> | Z <sub>0</sub> | Y <sub>1</sub> | X <sub>0</sub> |
| L       | L               | H | H | Y <sub>3</sub> | X <sub>3</sub> | Z <sub>0</sub> | Y <sub>1</sub> | X <sub>1</sub> |
| L       | H               | L | L |                |                | Z <sub>1</sub> | Y <sub>0</sub> | X <sub>0</sub> |
| L       | H               | L | H |                |                | Z <sub>1</sub> | Y <sub>0</sub> | X <sub>1</sub> |
| L       | H               | H | L |                |                | Z <sub>1</sub> | Y <sub>1</sub> | X <sub>0</sub> |
| L       | H               | H | H |                |                | Z <sub>1</sub> | Y <sub>1</sub> | X <sub>1</sub> |
| H       | X               | X | X | —              |                | —              |                |                |

Note: 1. Not applicable for HD74HC4052

X = Don't Care

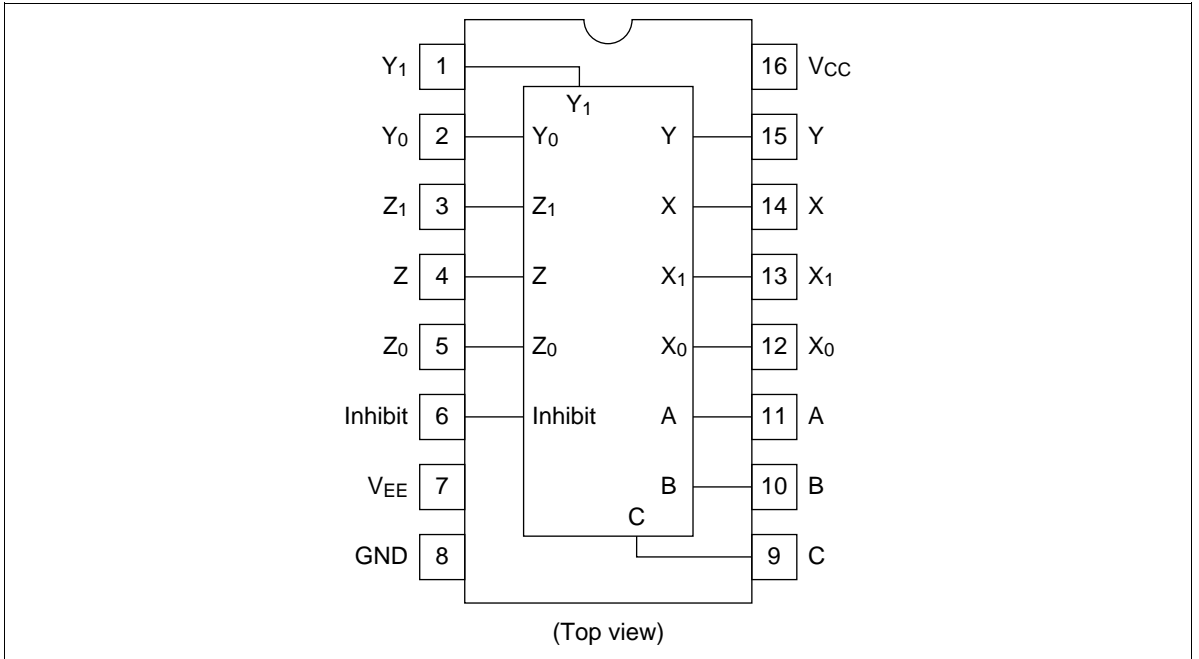
## Pin Arrangement

### HD74HC4052



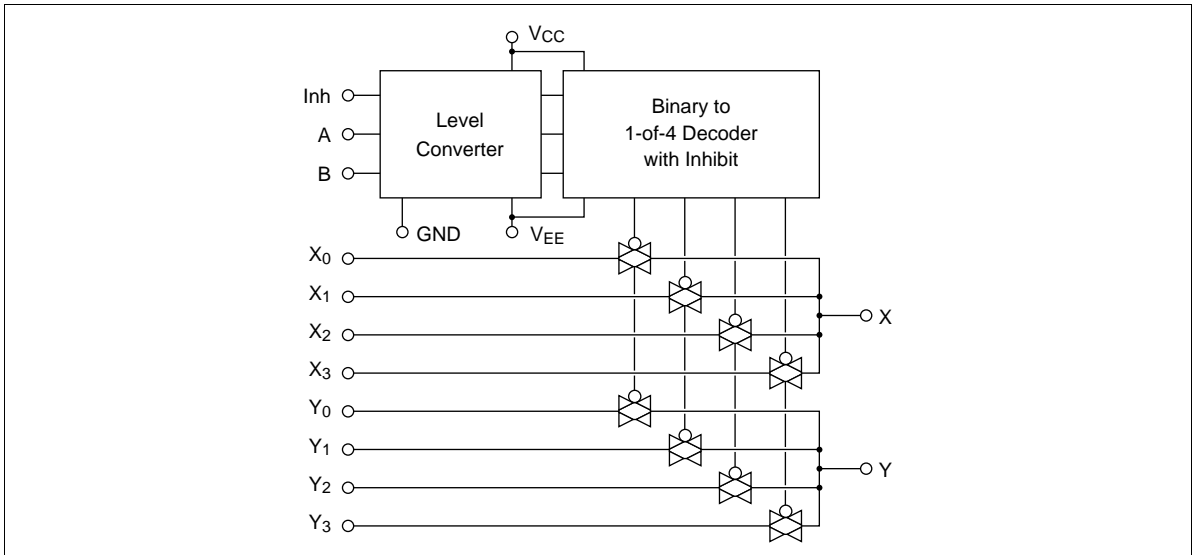
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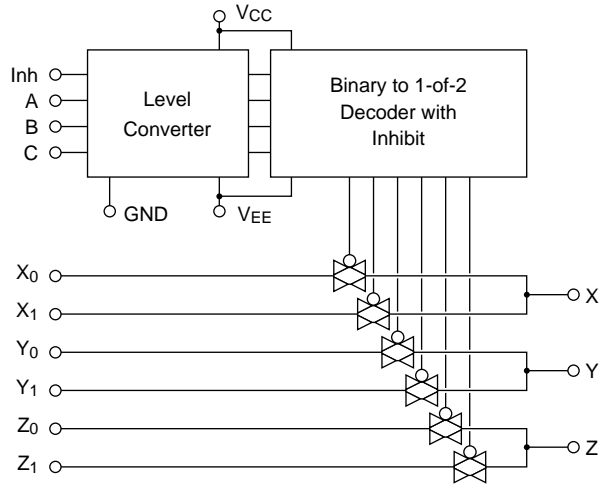


Block Diagram

HD74HC4052



## HD74HC4053



### Absolute Maximum Ratings

| Item                         | Symbol            | Rating                           | Unit |
|------------------------------|-------------------|----------------------------------|------|
| Supply voltage               | $V_{CC}$          | -0.5 to +7.0                     | V    |
|                              | $V_{CC} - V_{EE}$ | -0.5 to +7.0                     | V    |
| Control input voltage        | $V_{IN}$          | GND - 0.5 to $V_{CC} + 0.5$      | V    |
| Switch I/O voltage           | $V_{I/O}$         | $V_{EE} - 0.5$ to $V_{CC} + 0.5$ | V    |
| Supply current               | ( $V_{CC}$ )      | $I_{CC}$                         | +50  |
|                              | (GND)             | $I_{GND}$                        | -50  |
| Switch I/O current (per pin) | $I_{I/O}$         | ±25                              | mA   |
| Control input diode current  | $I_{IK}$          | ±20                              | mA   |
| Switch I/O diode current     | $I_{IOK}$         | ±20                              | mA   |
| Power dissipation            | $P_T$             | 500                              | mW   |
| Storage temperature range    | Tstg              | -65 to +150                      | °C   |

**Recommended Operating Range**

| Item                  |                         | Symbol            | Min      | Typ | Max      | Unit |
|-----------------------|-------------------------|-------------------|----------|-----|----------|------|
| Supply voltage        |                         | $V_{CC} - V_{EE}$ | 2        | —   | 6        | V    |
|                       |                         | $GND - V_{EE}$    | -4       | —   | 0        | V    |
| Control input voltage |                         | $V_{IN}$          | 0        | —   | $V_{CC}$ | V    |
| Switch I/O voltage    |                         | $V_{I/O}$         | $V_{EE}$ | —   | $V_{CC}$ | V    |
| Operating temperature |                         | $T_{opr}$         | -40      | —   | +85      | °C   |
| Input rise/fall time  | $V_{CC} = 2.0\text{ V}$ | $t_r, t_f$        | 0        | —   | 1000     | ns   |
|                       | $V_{CC} = 4.5\text{ V}$ |                   | 0        | —   | 500      | ns   |
|                       | $V_{CC} = 6.0\text{ V}$ |                   | 0        | —   | 400      | ns   |

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## DC Characteristics ( $V_{SS} = V_{EE} = \text{GND}$ )

| Item                                            | Symbol          | $V_{CC}$ (V) | $T_a = 25^\circ\text{C}$ |      |           | $T_a = -40$ to $+85^\circ\text{C}$ |           | Unit          | Test Conditions               |
|-------------------------------------------------|-----------------|--------------|--------------------------|------|-----------|------------------------------------|-----------|---------------|-------------------------------|
|                                                 |                 |              | Min                      | Typ  | Max       | Min                                | Max       |               |                               |
| Control input voltage                           | $V_{IH}$        | 2.0          | 1.5                      | —    | —         | 1.5                                | —         | V             |                               |
|                                                 |                 | 4.5          | 3.15                     | —    | —         | 3.15                               | —         |               |                               |
|                                                 |                 | 6.0          | 4.2                      | —    | —         | 4.2                                | —         |               |                               |
|                                                 | $V_{IL}$        | 2.0          | —                        | —    | 0.5       | —                                  | 0.5       | V             |                               |
|                                                 |                 | 4.5          | —                        | —    | 1.35      | —                                  | 1.35      |               |                               |
|                                                 |                 | 6.0          | —                        | —    | 1.8       | —                                  | 1.8       |               |                               |
| ON resistance                                   | $R_{ON}$        | 2.0          | —                        | 2000 | 5000      | —                                  | 6250      | $\Omega$      | $V_{INH} = V_{IL}$            |
|                                                 |                 | 4.5          | —                        | 120  | 180       | —                                  | 225       |               | $V_{IO} = V_{CC}$ to $V_{EE}$ |
|                                                 |                 | 6.0          | —                        | 100  | 170       | —                                  | 210       |               | $I_{IO} \leq 2 \text{ mA}$    |
|                                                 |                 | 2.0          | —                        | 200  | 800       | —                                  | 1000      | $\Omega$      | $V_{INH} = V_{IL}$            |
|                                                 |                 | 4.5          | —                        | 80   | 150       | —                                  | 190       |               | $V_{IO} = V_{CC}$ to $V_{EE}$ |
|                                                 |                 | 6.0          | —                        | 70   | 140       | —                                  | 175       |               | $V_{IO} \leq 2 \text{ mA}$    |
| $\Delta$ ON resistance between any two channels | $\Delta R_{ON}$ | 2.0          | —                        | 50   | —         | —                                  | —         | $\Omega$      | $V_{INH} = V_{IL}$            |
|                                                 |                 | 4.5          | —                        | 13   | 40        | —                                  | 50        |               | $V_{IO} = V_{CC}$ to $V_{EE}$ |
|                                                 |                 | 6.0          | —                        | 10   | 20        | —                                  | 25        |               | $I_{IO} \leq 2 \text{ mA}$    |
| OFF channel leakage current (switch off)        | $I_{S(OFF)}$    | 6.0          | —                        | —    | $\pm 0.1$ | —                                  | $\pm 1.0$ | $\mu\text{A}$ | $V_{INH} = V_{IL}$            |
| OFF channel leakage current (switch on)         | $I_{S(ON)}$     | 6.0          | —                        | —    | $\pm 0.1$ | —                                  | $\pm 1.0$ | $\mu\text{A}$ | $V_{INH} = V_{IL}$            |
| Control input current                           | $I_{in}$        | 6.0          | —                        | —    | $\pm 0.1$ | —                                  | $\pm 1.0$ | $\mu\text{A}$ | $V_{in} = V_{CC}$ or $V_{SS}$ |
| Quiescent supply current                        | $I_{CC}$        | 6.0          | —                        | —    | 4.0       | —                                  | 40        | $\mu\text{A}$ | $V_{in} = V_{CC}$ or $V_{SS}$ |

**AC Characteristics** ( $C_L = 50$  pF, Input  $t_r = t_f = 6$  ns,  $V_{SS} = V_{EE} = \text{GND}$ )

| Item                            | Symbol       | $V_{CC}$ (V) | $T_a = 25^\circ\text{C}$ |     |     | $T_a = -40$ to $+85^\circ\text{C}$ |     | Unit | Test Conditions                                            |
|---------------------------------|--------------|--------------|--------------------------|-----|-----|------------------------------------|-----|------|------------------------------------------------------------|
|                                 |              |              | Min                      | Typ | Max | Min                                | Max |      |                                                            |
| Propagation delay time          | $t_{PLH}$    | 2.0          | —                        | 25  | 60  | —                                  | 75  | ns   | $R_L = 10$ k $\Omega$<br>Switch input to<br>switch output  |
|                                 |              | 4.5          | —                        | 6   | 12  | —                                  | 15  |      |                                                            |
|                                 |              | 6.0          | —                        | 5   | 10  | —                                  | 13  |      |                                                            |
|                                 | $t_{PHL}$    | 2.0          | —                        | 25  | 60  | —                                  | 75  | ns   |                                                            |
|                                 |              | 4.5          | —                        | 6   | 12  | —                                  | 15  |      |                                                            |
|                                 |              | 6.0          | —                        | 5   | 10  | —                                  | 13  |      |                                                            |
| Propagation delay time          | $t_{PLH}$    | 2.0          | —                        | 50  | 153 | —                                  | 191 | ns   | $R_L = 10$ k $\Omega$<br>Control input to<br>switch output |
|                                 |              | 4.5          | —                        | 16  | 30  | —                                  | 38  |      |                                                            |
|                                 |              | 6.0          | —                        | 14  | 26  | —                                  | 33  |      |                                                            |
|                                 | $t_{PHL}$    | 2.0          | —                        | 50  | 153 | —                                  | 191 | ns   |                                                            |
|                                 |              | 4.5          | —                        | 16  | 30  | —                                  | 38  |      |                                                            |
|                                 |              | 6.0          | —                        | 14  | 26  | —                                  | 33  |      |                                                            |
| Output enable time              | $t_{ZH}$     | 2.0          | —                        | 50  | 153 | —                                  | 191 | ns   | $R_L = 1$ k $\Omega$                                       |
|                                 |              | 4.5          | —                        | 14  | 30  | —                                  | 38  |      |                                                            |
|                                 |              | 6.0          | —                        | 12  | 26  | —                                  | 33  |      |                                                            |
|                                 | $t_{ZL}$     | 2.0          | —                        | 50  | 153 | —                                  | 191 | ns   |                                                            |
|                                 |              | 4.5          | —                        | 14  | 30  | —                                  | 38  |      |                                                            |
|                                 |              | 6.0          | —                        | 12  | 26  | —                                  | 33  |      |                                                            |
| Output disable time             | $t_{HZ}$     | 2.0          | —                        | 40  | 153 | —                                  | 191 | ns   | $R_L = 1$ k $\Omega$                                       |
|                                 |              | 4.5          | —                        | 17  | 30  | —                                  | 38  |      |                                                            |
|                                 |              | 6.0          | —                        | 14  | 26  | —                                  | 33  |      |                                                            |
|                                 | $t_{LZ}$     | 2.0          | —                        | 40  | 153 | —                                  | 191 | ns   |                                                            |
|                                 |              | 4.5          | —                        | 17  | 30  | —                                  | 38  |      |                                                            |
|                                 |              | 6.0          | —                        | 14  | 26  | —                                  | 33  |      |                                                            |
| Control input capacitance       | $C_{in}$     | —            | —                        | 5   | 10  | —                                  | 10  | pF   |                                                            |
| Switch input capacitance        | $C_{in}$     | 5.0          | —                        | 5   | —   | —                                  | —   | pF   |                                                            |
| Output capacitance (Common pin) | $C_{out}$    | 5.0          | —                        | 12  | —   | —                                  | —   | pF   | HD74HC4052                                                 |
|                                 |              | 5.0          | —                        | 6   | —   | —                                  | —   |      | HD74HC4053                                                 |
| Feed through capacitance        | $C_{in-out}$ | 5.0          | —                        | 0.6 | —   | —                                  | —   | pF   | HD74HC4052                                                 |
|                                 |              | 5.0          | —                        | 0.5 | —   | —                                  | —   |      | HD74HC4053                                                 |

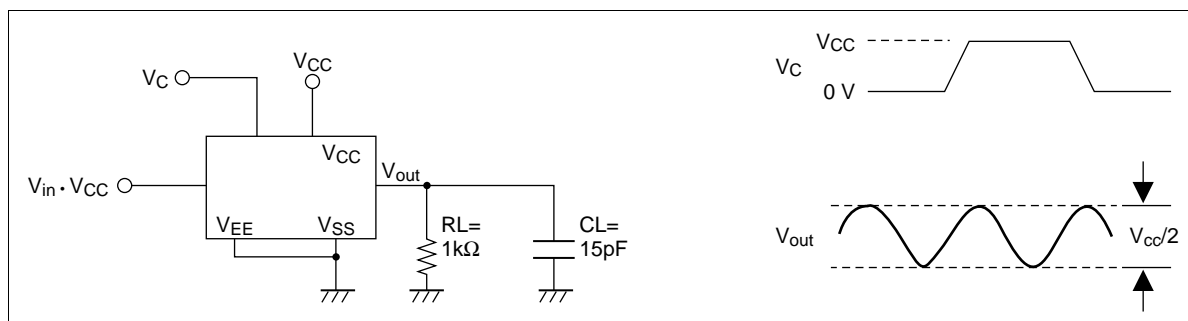
# HD74HC4052/HD74HC4053

**AC Characteristics** ( $C_L = 50$  pF, Input  $t_r = t_f = 6$  ns,  $V_{SS} = V_{EE} = \text{GND}$ ) (cont)

| Item                                              | Symbol   | $V_{CC}$ (V) | $T_a = 25^\circ\text{C}$ |      |     | $T_a = -40$ to $+85^\circ\text{C}$ |     | Unit | Test Conditions                                                                                  |
|---------------------------------------------------|----------|--------------|--------------------------|------|-----|------------------------------------|-----|------|--------------------------------------------------------------------------------------------------|
|                                                   |          |              | Min                      | Typ  | Max | Min                                | Max |      |                                                                                                  |
| Power dissipation capacitance                     | $C_{PD}$ | 5.0          | —                        | 32.0 | —   | —                                  | —   | pF   | HD74HC4052                                                                                       |
|                                                   |          | 5.0          | —                        | 17.0 | —   | —                                  | —   |      | HD74HC4053                                                                                       |
| Sine wave distortion                              |          | 4.5          | —                        | 0.1  | —   | —                                  | —   | %    | $f_{in} = 1$ kHz, $V_{in} = 4 V_{P-P}$<br>$R_L = 10$ k $\Omega$ , $C_L = 50$ pF                  |
| Frequency response channel "ON" (Sine wave input) |          | 4.5          | —                        | 95   | —   | —                                  | —   | MHz  | $f_{in} = 1$ MHz,<br>$20 \log_{10} V_{OS}/V_{IS} = -3$ dB<br>$R_L = 50$ $\Omega$ , $C_L = 10$ pF |
| Feed through attenuation                          |          | 4.5          | —                        | -50  | —   | —                                  | —   | dB   | $R_L = 600$ $\Omega$ , $C_L = 50$ pF,<br>$f_{in} = 1$ MHz                                        |
| Cross talk between control input and switch I/O   |          | 2.0          | —                        | 25   | —   | —                                  | —   | mV   | $R_L = 600$ $\Omega$ , $C_L = 15$ pF,<br>$f_{in} = 1$ MHz                                        |
|                                                   |          | 4.5          | —                        | 50   | —   | —                                  | —   |      |                                                                                                  |
|                                                   |          | 6.0          | —                        | 75   | —   | —                                  | —   |      |                                                                                                  |
| Cross talk between any two switches               |          | 4.5          | —                        | 50   | —   | —                                  | —   | dB   | $R_L = 600$ $\Omega$ , $C_L = 50$ pF,<br>$f_{in} = 1$ MHz                                        |
| Maximum control frequency                         |          | 2.0          | —                        | 20   | —   | —                                  | —   | MHz  | $R_L = 1$ k $\Omega$ , $C_L = 15$ pF<br>$V_{out} = 1/2 (V_{CC})$                                 |
|                                                   |          | 4.5          | —                        | 30   | —   | —                                  | —   |      |                                                                                                  |
|                                                   |          | 6.0          | —                        | 30   | —   | —                                  | —   |      |                                                                                                  |

## AC Characteristics Test Circuit

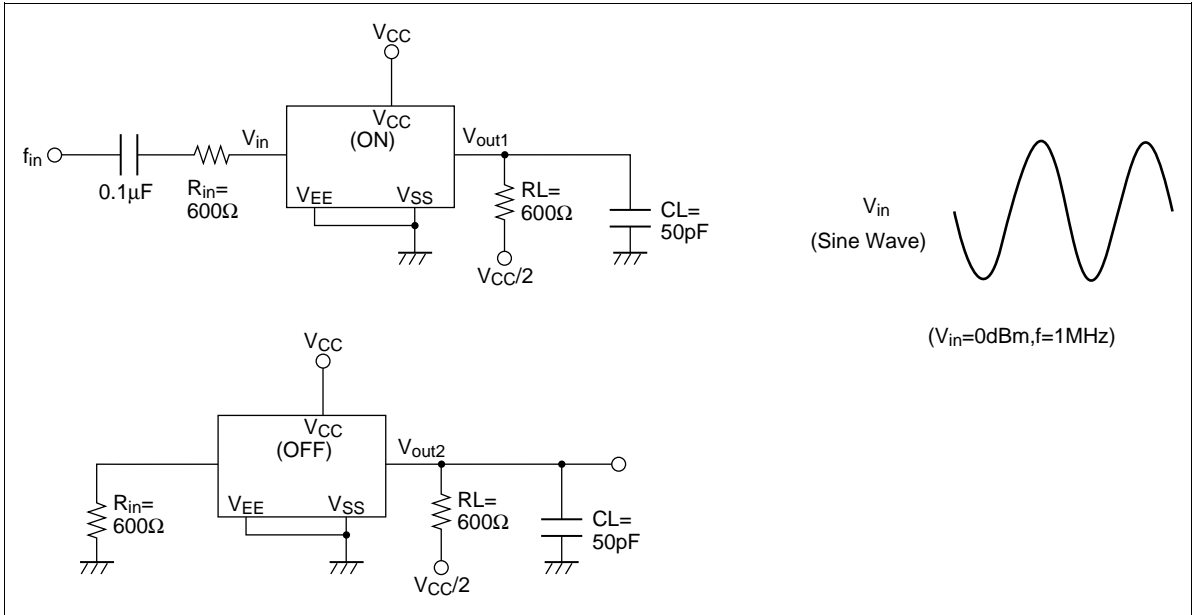
### Maximum Control Frequency



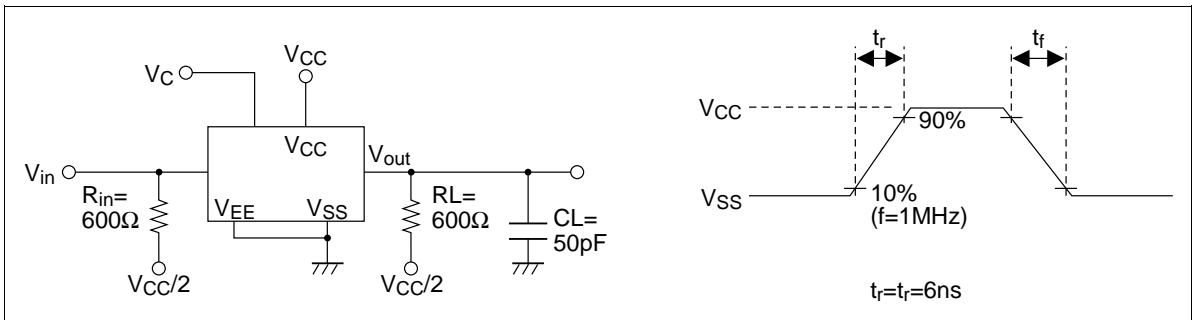
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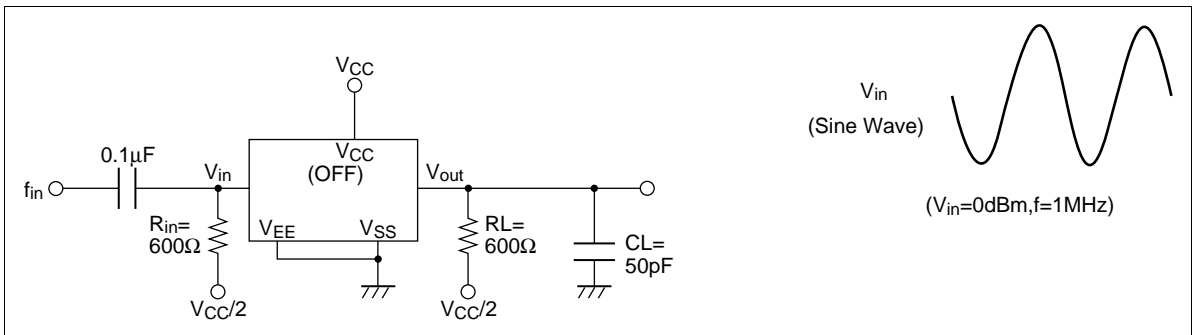
**Cross talk (Between Any Two Switches)**



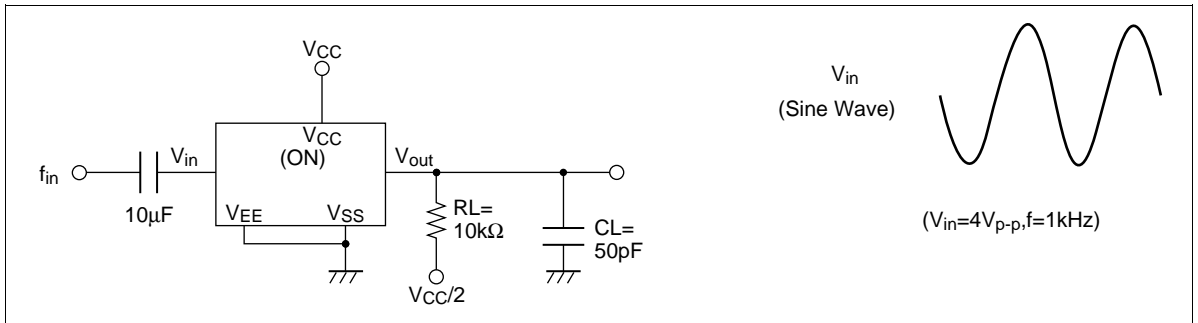
**Cross talk (Control Input to Switch Output)**



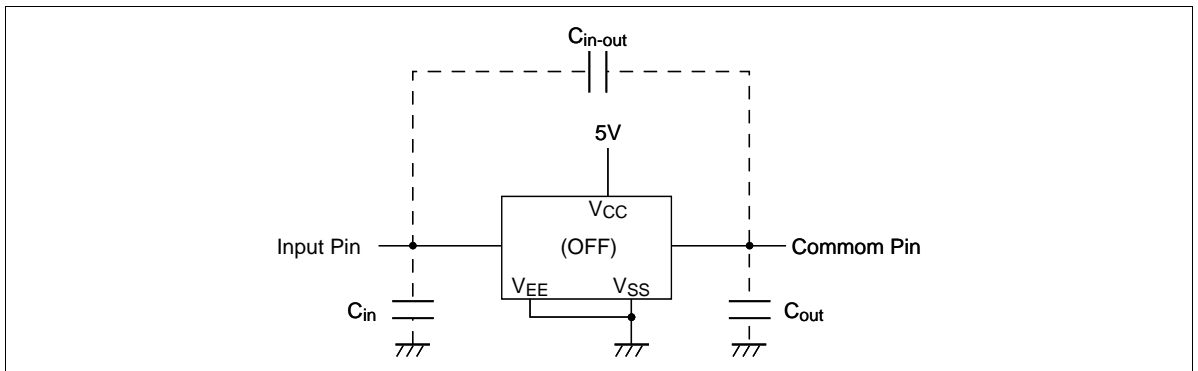
**Feed through Attenuation**



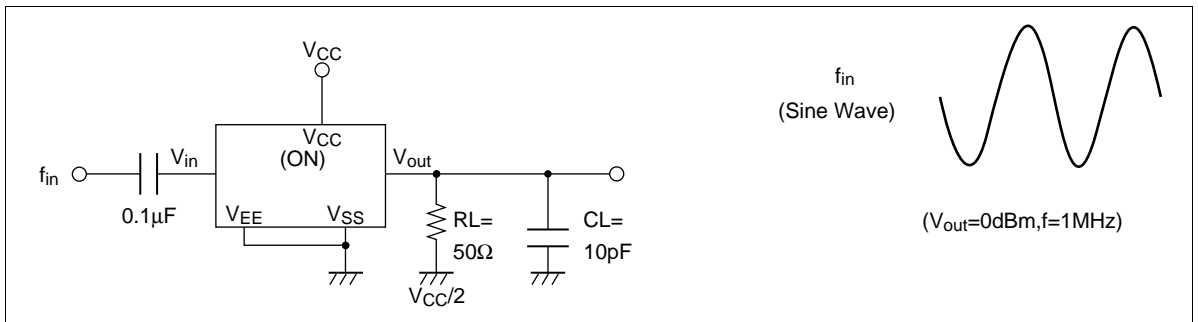
## Sine Wave Distortion



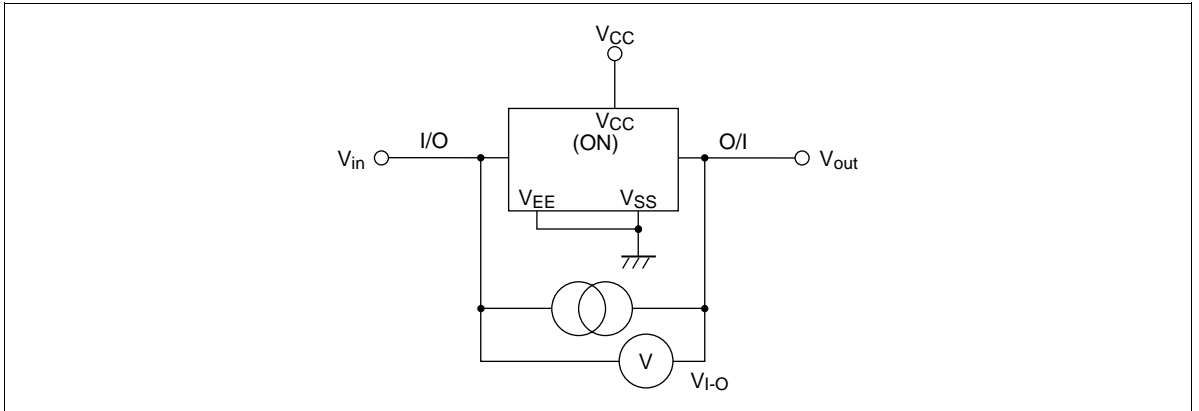
## C<sub>in</sub>, C<sub>out</sub>, C<sub>in-out</sub> (Input, Output and Feed through Capacitance)



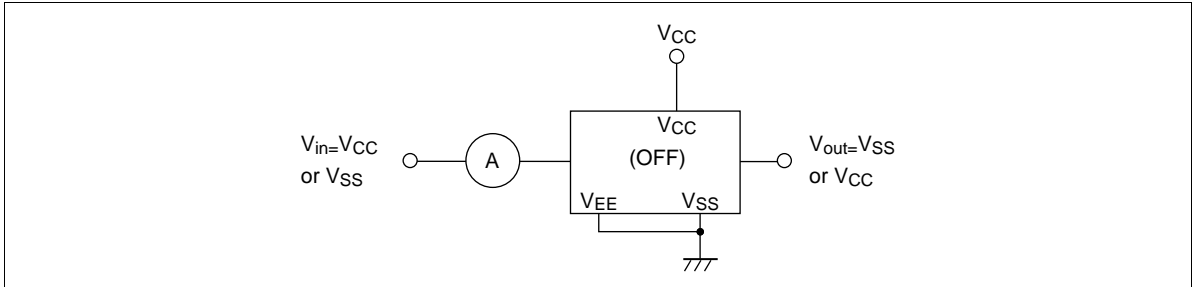
## Frequency Response Channel ON



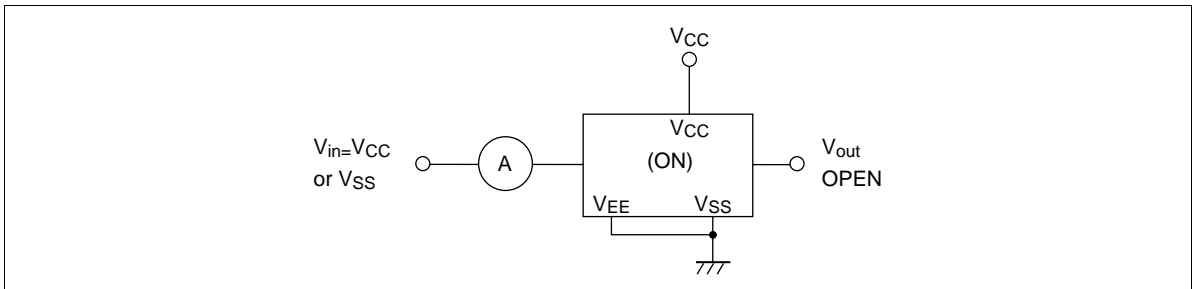
**R<sub>ON</sub>: ON Resistance**



**I<sub>s</sub> (OFF): OFF Channel Leakage Current (Switch OFF)**

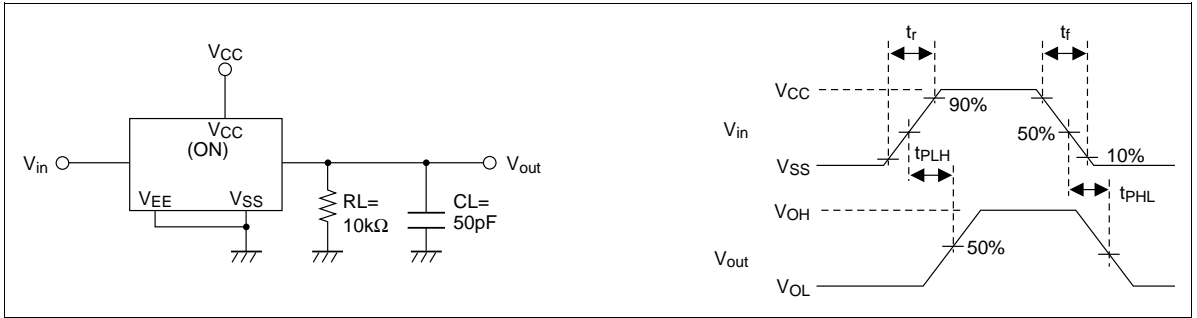


**I<sub>s</sub> (ON): ON Channel Leakage Current (Switch ON)**

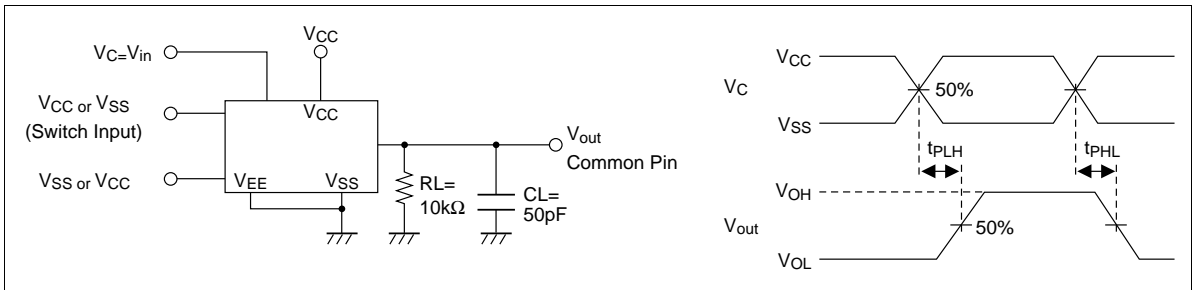


# HD74HC4052/HD74HC4053

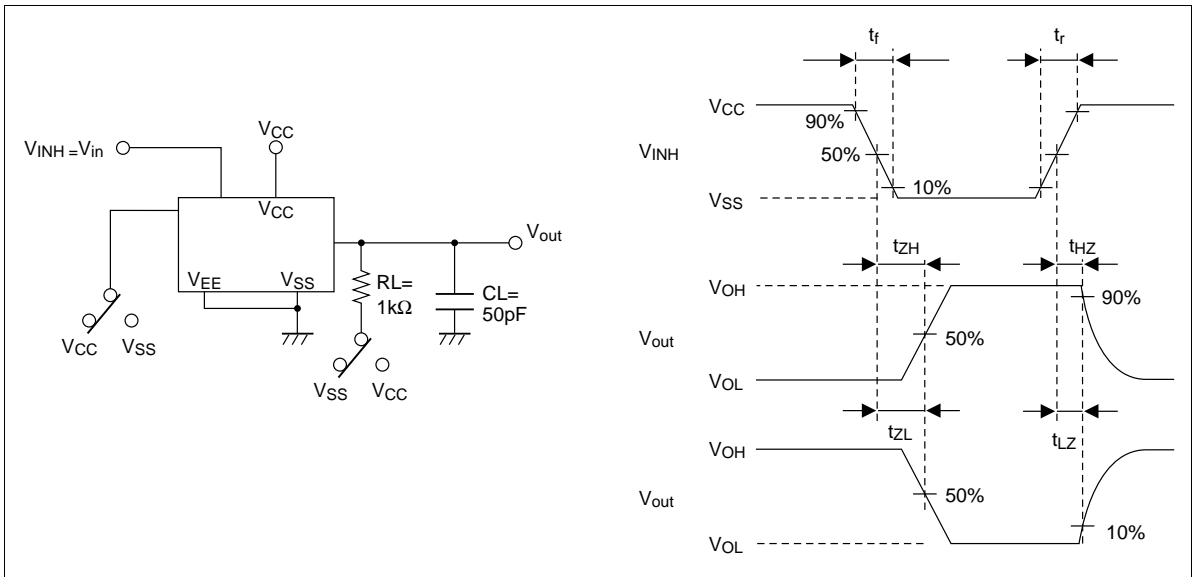
$t_{PLH}$ ,  $t_{PHL}$ : Propagation Delay Time (Switch Input to Switch Output)



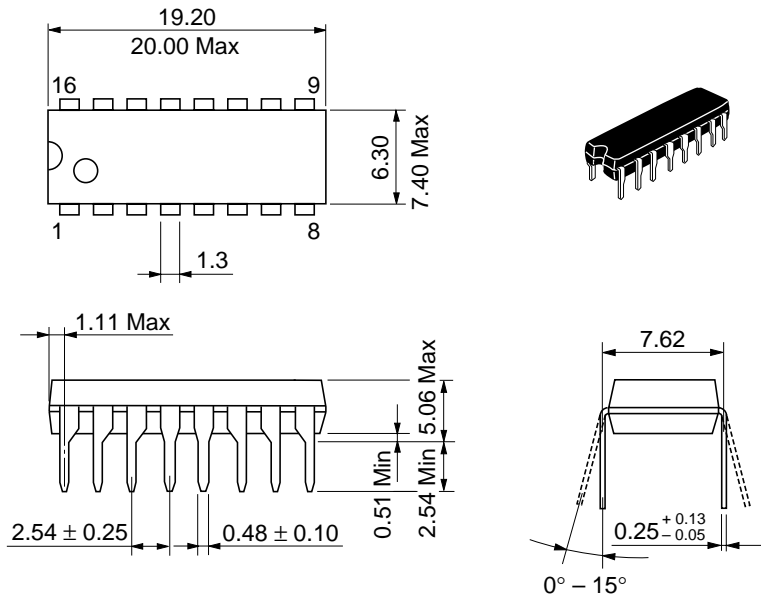
$t_{PLH}$ ,  $t_{PHL}$ : Propagation Delay Time (Control Input to Switch Output)



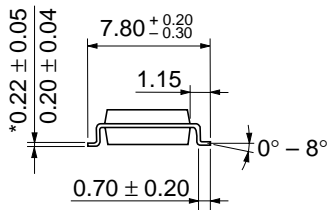
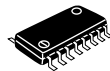
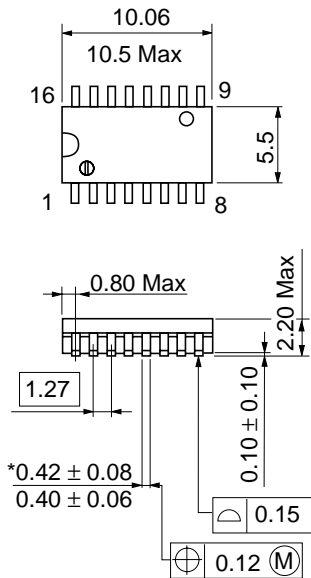
$t_{ZH}$ ,  $t_{ZL}/t_{HZ}$ ,  $t_{LZ}$ : Output Enable and Disable Time



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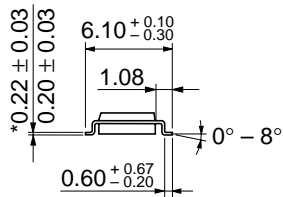
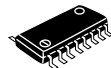
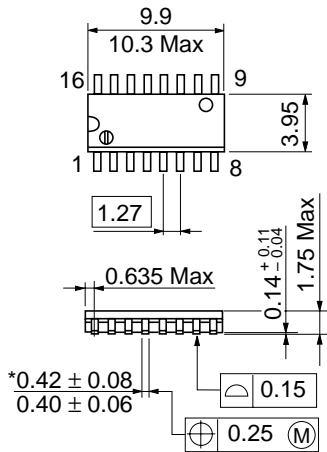


|                          |          |
|--------------------------|----------|
| Hitachi Code             | DP-16    |
| JEDEC                    | Conforms |
| EIAJ                     | Conforms |
| Weight (reference value) | 1.07 g   |



\*Dimension including the plating thickness  
Base material dimension

|                          |          |
|--------------------------|----------|
| Hitachi Code             | FP-16DA  |
| JEDEC                    | —        |
| EIAJ                     | Conforms |
| Weight (reference value) | 0.24 g   |



\*Dimension including the plating thickness  
 Base material dimension

|                          |          |
|--------------------------|----------|
| Hitachi Code             | FP-16DN  |
| JEDEC                    | Conforms |
| EIAJ                     | Conforms |
| Weight (reference value) | 0.15 g   |

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