

HD74LV1G02A

2-input NOR Gate

REJ03D0063-0700 Rev.7.00 Mar 21, 2008

Description

The HD74LV1G02A has two-input NOR gate in a 5 pin package. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- Electrical characteristics equivalent to the HD74LV02A

Supply voltage range: 1.65 to 5.5 V

Operating temperature range : -40 to +85°C

• All inputs V_{IH} (Max.) = 5.5 V (@ V_{CC} = 0 V to 5.5 V)

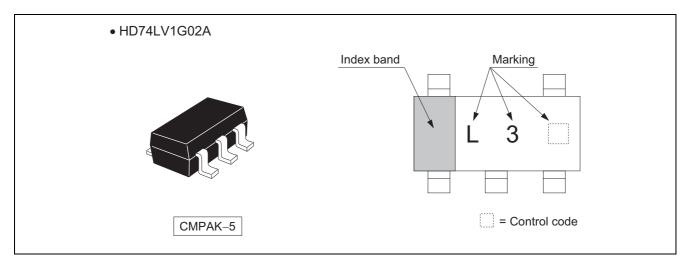
All outputs V_0 (Max.) = 5.5 V (@ $V_{CC} = 0 V$)

- Output current ± 6 mA (@V_{CC} = 3.0 V to 3.6 V), ± 12 mA (@V_{CC} = 4.5 V to 5.5 V)
- All the logical input has hysteresis voltage for the slow transition.
- Ordering Information

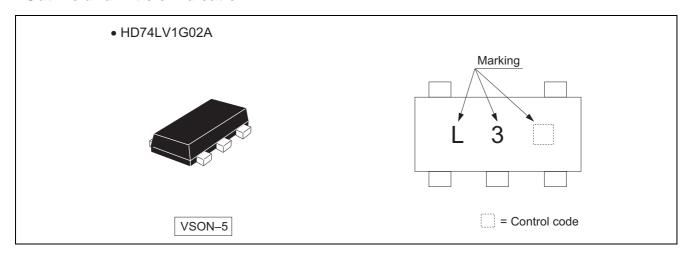
Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LV1G02ACME	CMPAK-5 pin	PTSP0005ZC-A (CMPAK-5V)	СМ	E (3000 pcs/reel)
HD74LV1G02AVSE	VSON-5 pin	PUSN0005KA-A (TNP-5DV)	VS	E (3000 pcs/reel)

Note: Please consult the sales office for the above package availability.

Outline and Article Indication



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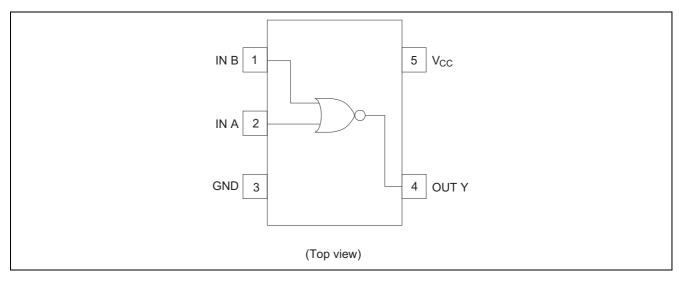


Function Table

Inp	Inputs					
Α	В	Output Y				
L	L	Н				
L	Н	L				
Н	L	L				
Н	Н	L				

H : High level L : Low level

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V _{cc}	-0.5 to 7.0	V	
Input voltage range *1	VI	-0.5 to 7.0	V	
Output voltage range *1, 2	V	-0.5 to $V_{CC} + 0.5$	V	Output : H or L
Output voltage range	Vo	-0.5 to 7.0	7 v	V _{CC} : OFF
Input clamp current	I _{IK}	-20	mA	V ₁ < 0
Output clamp current	I _{OK}	±50	mA	$V_O < 0$ or $V_O > V_{CC}$
Continuous output current	I _O	±25	mA	$V_O = 0$ to V_{CC}
Continuous current through V _{CC} or GND	I _{CC} or I _{GND}	±50	mA	
Maximum power dissipation at Ta = 25°C (in still air) *3	P _T	200	mW	
Storage temperature	Tstg	-65 to 150	°C	

Notes:

- The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.
- 1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
- 2. This value is limited to 5.5 V maximum.
- 3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V_{CC}	1.65	5.5	V	
Input voltage range	VI	0	5.5	V	
Output voltage range	Vo	0	V _{CC}	V	
		_	1		V _{CC} = 1.65 to 1.95 V
	ı	_	2		V _{CC} = 2.3 to 2.7 V
	loL	_	6	mA	V _{CC} = 3.0 to 3.6 V
Output ourrant		_	12		V _{CC} = 4.5 to 5.5 V
Output current	Іон	_	-1		V _{CC} = 1.65 to 1.95 V
		_	-2		V _{CC} = 2.3 to 2.7 V
		_	-6		V _{CC} = 3.0 to 3.6 V
		_	-12		V _{CC} = 4.5 to 5.5 V
		0	300		V _{CC} = 1.65 to 1.95 V
Input transition rise or fall rate	A+ / A>.	0	200	ns / V	V _{CC} = 2.3 to 2.7 V
Input transition rise or fall rate	Δt / Δv	0	100	TIS / V	V _{CC} = 3.0 to 3.6 V
		0	20		$V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$
Operating free-air temperature	Ta	-40	85	°C	

Note: Unused or floating inputs must be held high or low.

Electrical Characteristic

• $Ta = -40 \text{ to } 85^{\circ}\text{C}$

Item	Symbol	V _{CC} (V) *	Min	Тур	Max	Unit	Test condition
		1.65 to 1.95	V _{CC} ×0.75	_	_		
	V _{IH}	2.3 to 2.7	V _{CC} ×0.7	_	_		
	VIH	3.0 to 3.6	V _{CC} ×0.7	_	_		
Input voltage		4.5 to 5.5	V _{CC} ×0.7	_	_	V	
input voltage		1.65 to 1.95	_	_	V _{CC} ×0.25	v	
	V _{IL}	2.3 to 2.7	_	_	V _{CC} ×0.3		
	V IL	3.0 to 3.6	_	_	V _{CC} ×0.3		
		4.5 to 5.5	_	_	V _{CC} ×0.3		
		1.8	_	0.25	_		
Hysteresis voltage	V _H	2.5	_	0.30	_	V	$V_T^+ - V_T^-$
l lysteresis voltage		3.3	_	0.35	_]	VT - VT
		5.0	_	0.45	_		
		Min to Max	V _{CC} -0.1	_	_		$I_{OH} = -50 \ \mu A$
	V _{он}	1.65	1.4	_	_		$I_{OH} = -1 \text{ mA}$
		2.3	2.0	_	_		$I_{OH} = -2 \text{ mA}$
		3.0	2.48	_	_		$I_{OH} = -6 \text{ mA}$
Output voltage		4.5	3.8	_	_	V	I _{OH} = −12 mA
Output voltage		Min to Max	_	_	0.1	v	$I_{OL} = 50 \mu\text{A}$
		1.65	_	_	0.3		I _{OL} = 1 mA
	V_{OL}	2.3	_	_	0.4		$I_{OL} = 2 \text{ mA}$
		3.0	_	_	0.44		$I_{OL} = 6 \text{ mA}$
		4.5	_	_	0.55		I _{OL} = 12 mA
Input current	I _{IN}	0 to 5.5	_	_	±1	μΑ	$V_{IN} = 5.5 \text{ V or GND}$
Quiescent	Icc	5.5	_		10	μА	$V_{IN} = V_{CC}$ or GND,
supply current	ICC	5.5	_		10	μΑ	$I_{O} = 0$
Output leakage	I _{OFF}	0	_	_	5	μΑ	V_{IN} or $V_O = 0$ to 5.5 V
current		2.2		2.5			
Input capacitance	C _{IN}	3.3		2.5	_	pF	$V_{IN} = V_{CC}$ or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

• $V_{CC} = 1.8 \pm 0.15 \text{ V}$

Item	Symbol	Ta = 25°C			Ta = -40	to 85°C	Unit	Test	FROM	ТО
item	Syllibol	Min	Тур	Max	Min	Max	Onit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	14.2	23.3	1.0	26.0		$C_L = 15 pF$	A or B	
delay time	t _{PHL}		20.5	33.5	1.0	36.5	ns	$C_L = 50 pF$	AUID	ľ

$\bullet \quad V_{CC} = 2.5 \pm 0.2 \ V$

Item	Symbol	Ta = 25°C			Ta = -40	Ta = -40 to 85°C		Test	FROM	ТО
item	Syllibol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	8.3	12.4	1.0	15.0	nc	C _L = 15 pF	A or B	V
delay time	t _{PHL}	_	11.0	16.1	1.0	19.0	ns	$C_L = 50 pF$	AUID	ı

$\bullet \quad V_{CC} = 3.3 \pm 0.3 \ V$

Item	Symbol	Ta = 25°C			Ta = -40	to 85°C	Unit	Test	FROM	ТО
item	Syllibol	Min	Тур	Max	Min	Max	Onit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	5.6	7.9	1.0	9.5	nc	$C_L = 15 pF$	A or B	V
delay time	t _{PHL}	_	7.6	11.4	1.0	13.0	ns	$C_L = 50 \text{ pF}$	AUID	'

$\bullet \quad V_{CC} = 5.0 \pm 0.5 \ V$

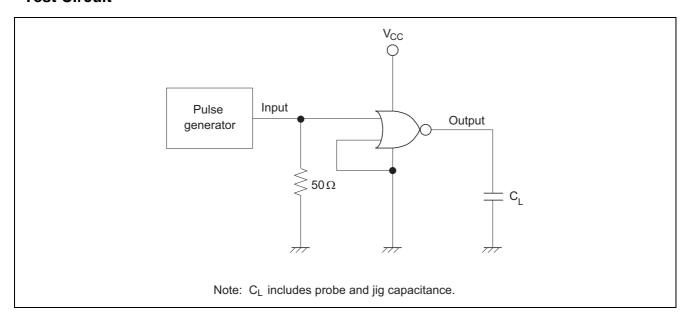
Item	Symbol	Ta = 25°C			Ta = -40	Ta = -40 to 85°C		Test	FROM	ТО
iteiii	Syllibol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	3.9	5.5	1.0	6.5	nc	$C_L = 15 pF$	A or B	V
delay time	t _{PHL}	_	5.3	7.5	1.0	8.5	ns	$C_L = 50 pF$	AUID	ı

Operating Characteristics

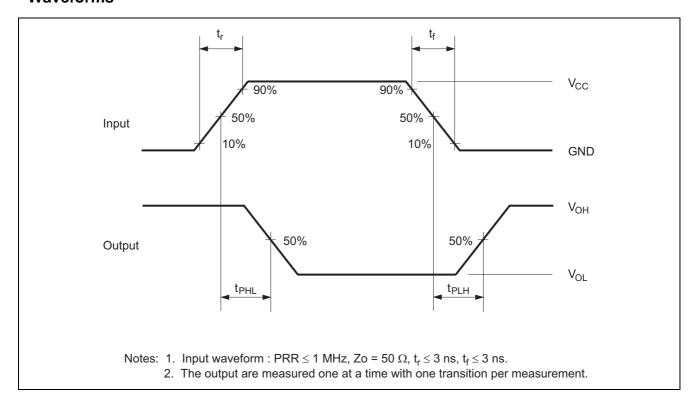
• $C_L = 50 \text{ pF}$

Item	Symbol	V _{cc} (V)	Ta = 25°C			Unit	Test Conditions	
item	Syllibol	VCC (V)	Min	Тур	Max	Offic	rest Conditions	
Power dissipation	C_PD	3.3	_	8.9	_	pF	f = 10 MHz	
capacitance	OPD	5.0	_	10.3	_		I = 10 MHZ	

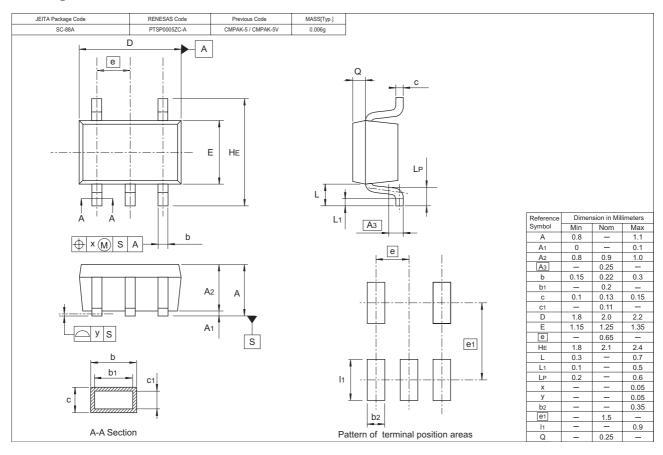
Test Circuit

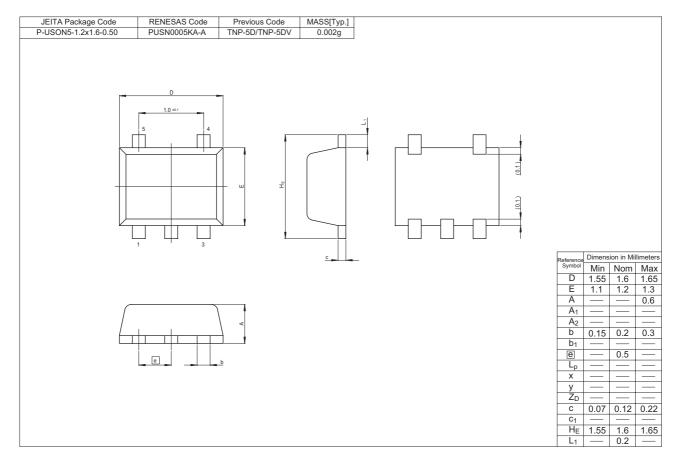


Waveforms



Package Dimensions





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