

# HD74LV1GT32A

## 2-input OR Gate / CMOS Logic Level Shifter

REJ03D0120-0900

Rev.9.00

Mar 21, 2008

### Description

The HD74LV1GT32A is high-speed CMOS two input OR gate using silicon gate CMOS process. With CMOS low power dissipation, it provides high-speed equivalent to LS-TTL series. The internal circuit of three stages construction with buffer provides wide noise margin and stable output. The input protection circuitry on this device allows over voltage tolerance on the input, allowing the device to be used as a logic-level translator from 3.0 V CMOS Logic to 5.0 V CMOS Logic or from 1.8 V CMOS logic to 3.0 V CMOS Logic while operating at the high-voltage power supply. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

### Features

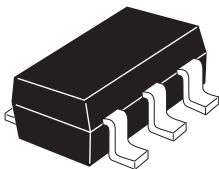
- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- TTL compatible input level.  
Supply voltage range : 3.0 to 5.5 V  
Operating temperature range : -40 to +85°C
- Logic-level translate function  
3.0 V CMOS logic → 5.0 V CMOS logic (@V<sub>CC</sub> = 5.0 V)  
1.8 V or 2.5 V CMOS logic → 3.3 V CMOS logic (@V<sub>CC</sub> = 3.3 V)
- All inputs V<sub>IH</sub> (Max.) = 5.5 V (@V<sub>CC</sub> = 0 V to 5.5 V)  
All outputs V<sub>O</sub> (Max.) = 5.5 V (@V<sub>CC</sub> = 0 V)
- Output current ±6 mA (@V<sub>CC</sub> = 3.0 V to 3.6 V), ±12 mA (@V<sub>CC</sub> = 4.5 V to 5.5 V)
- All the logical input has hysteresis voltage for the slow transition.
- Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LV1GT32ACME	COMPAK-5 pin	PTSP0005ZC-A (COMPAK-5V)	CM	E (3000 pcs/reel)
HD74LV1GT32AVSE	VSON-5 pin	PUSN0005KA-A (TNP-5DV)	VS	E (3000 pcs/reel)

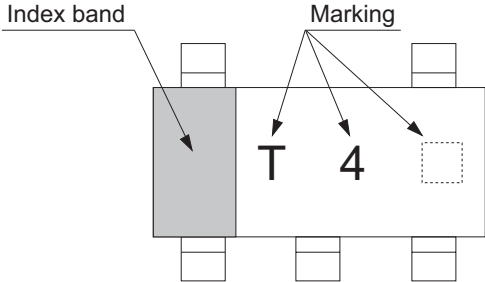
Note: Please consult the sales office for the above package availability.

Outline and Article Indication

• HD74LV1GT32A



CMLPAK-5



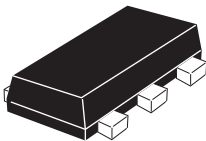
Index band

Marking

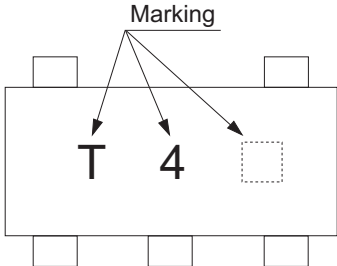
T 4 □

□ = Control code

• HD74LV1GT32A



VSON-5



Marking

T 4 □

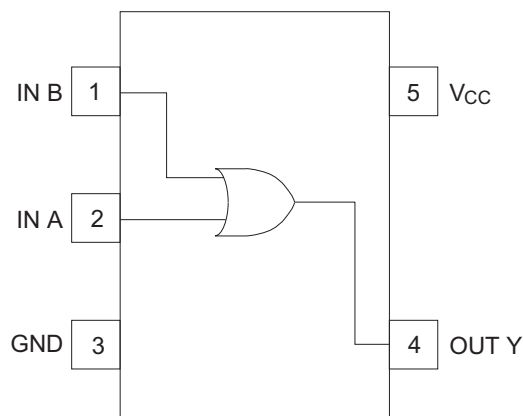
□ = Control code

Function Table

Inputs		Output Y
A	B	
L	L	L
H	L	H
L	H	H
H	H	H

H : High level  
L : Low level

## Pin Arrangement



(Top view)

## Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	$V_{CC}$	-0.5 to 7.0	V	
Input voltage range <sup>*1</sup>	$V_I$	-0.5 to 7.0	V	
Output voltage range <sup>*1, 2</sup>	$V_O$	-0.5 to $V_{CC} + 0.5$	V	Output : H or L
		-0.5 to 7.0		$V_{CC}$ : OFF
Input clamp current	$I_{IK}$	-20	mA	$V_I < 0$
Output clamp current	$I_{OK}$	$\pm 50$	mA	$V_O < 0$ or $V_O > V_{CC}$
Continuous output current	$I_O$	$\pm 25$	mA	$V_O = 0$ to $V_{CC}$
Continuous current through $V_{CC}$ or GND	$I_{CC}$ or $I_{GND}$	$\pm 50$	mA	
Maximum power dissipation at $T_a = 25^\circ\text{C}$ (in still air) <sup>*3</sup>	$P_T$	200	mW	
Storage temperature	$T_{stg}$	-65 to 150	$^\circ\text{C}$	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
2. This value is limited to 5.5 V maximum.
3. The maximum package power dissipation was calculated using a junction temperature of 150 $^\circ\text{C}$ .

## Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	$V_{CC}$	3.0	5.5	V	
Input voltage range	$V_I$	0	5.5	V	
Output voltage range	$V_O$	0	$V_{CC}$	V	
Output current	$I_{OL}$	—	6	mA	$V_{CC} = 3.0$ to $3.6$ V
		—	12		$V_{CC} = 4.5$ to $5.5$ V
	$I_{OH}$	—	-6		$V_{CC} = 3.0$ to $3.6$ V
		—	-12		$V_{CC} = 4.5$ to $5.5$ V
Input transition rise or fall rate	$\Delta t / \Delta v$	0	100	ns / V	$V_{CC} = 3.0$ to $3.6$ V
		0	20		$V_{CC} = 4.5$ to $5.5$ V
Operating free-air temperature	$T_a$	-40	85	$^\circ\text{C}$	

Note: Unused or floating inputs must be held high or low.

## Electrical Characteristic

- $T_a = -40$  to  $85^\circ\text{C}$

Item	Symbol	$V_{CC}$ (V) *	Min	Typ	Max	Unit	Test condition
Input voltage	$V_{IH}$	3.0 to 3.6	1.5	—	—	V	
		4.5 to 5.5	2.0	—	—		
	$V_{IL}$	3.0 to 3.6	—	—	0.6		
		4.5 to 5.5	—	—	0.8		
Hysteresis voltage	$V_H$	3.3	—	0.10	—	V	$V_{T^+} - V_{T^-}$
		5.0	—	0.15	—		
Output voltage	$V_{OH}$	Min to Max	$V_{CC}-0.1$	—	—	V	$I_{OH} = -50\ \mu\text{A}$
		3.0	2.48	—	—		$I_{OH} = -6\ \text{mA}$
		4.5	3.8	—	—		$I_{OH} = -12\ \text{mA}$
	$V_{OL}$	Min to Max	—	—	0.1		$I_{OL} = 50\ \mu\text{A}$
		3.0	—	—	0.44		$I_{OL} = 6\ \text{mA}$
		4.5	—	—	0.55		$I_{OL} = 12\ \text{mA}$
Input current	$I_{IN}$	0 to 5.5	—	—	$\pm 1$	$\mu\text{A}$	$V_{IN} = 5.5\ \text{V}$ or GND
Quiescent supply current	$I_{CC}$	5.5	—	—	10	$\mu\text{A}$	$V_{IN} = V_{CC}$ or GND, $I_O = 0$
	$\Delta I_{CC}$	5.5	—	—	1.5	mA	One input $V_{IN} = 3.4\ \text{V}$ , other input $V_{CC}$ or GND
Output leakage current	$I_{OFF}$	0	—	—	5	$\mu\text{A}$	$V_{IN}$ or $V_O = 0$ to $5.5\ \text{V}$
Input capacitance	$C_{IN}$	5.0	—	2.5	—	pF	$V_{IN} = V_{CC}$ or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

## Switching Characteristics

- $V_{CC} = 3.3 \pm 0.3\ \text{V}$

Item	Symbol	$T_a = 25^\circ\text{C}$			$T_a = -40$ to $85^\circ\text{C}$		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Typ	Max	Min	Max				
Propagation delay time	$t_{PLH}$	—	7.0	10.0	1.0	12.0	ns	$C_L = 15\ \text{pF}$	A or B	Y
	$t_{PHL}$	—	7.5	12.0	1.0	14.0		$C_L = 50\ \text{pF}$		

- $V_{CC} = 5.0 \pm 0.5\ \text{V}$

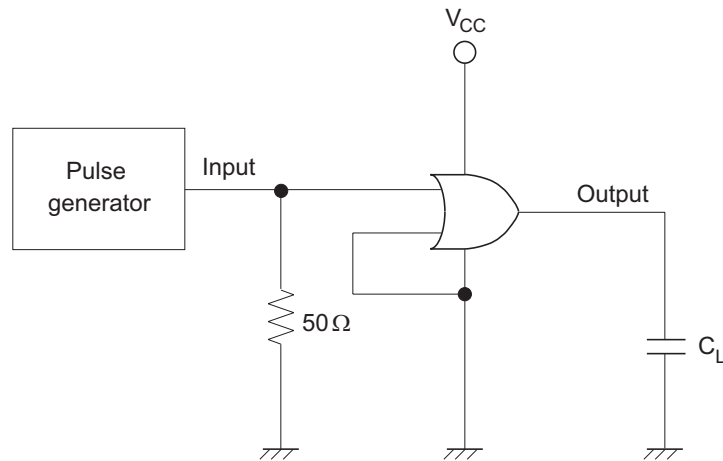
Item	Symbol	$T_a = 25^\circ\text{C}$			$T_a = -40$ to $85^\circ\text{C}$		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Typ	Max	Min	Max				
Propagation delay time	$t_{PLH}$	—	5.0	6.9	1.0	8.0	ns	$C_L = 15\ \text{pF}$	A or B	Y
	$t_{PHL}$	—	5.5	7.9	1.0	9.0		$C_L = 50\ \text{pF}$		

## Operating Characteristics

- $C_L = 50\ \text{pF}$

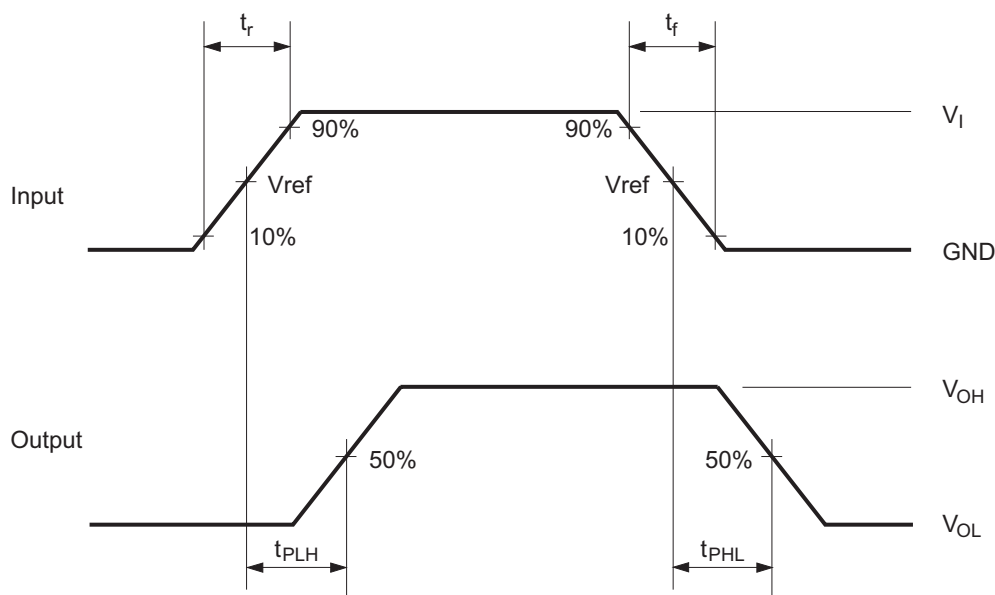
Item	Symbol	$V_{CC}$ (V)	$T_a = 25^\circ\text{C}$			Unit	Test Conditions
			Min	Typ	Max		
Power dissipation capacitance	$C_{PD}$	5.0	—	11.5	—	pF	$f = 10\ \text{MHz}$

# Test Circuit



Note:  $C_L$  includes probe and jig capacitance.

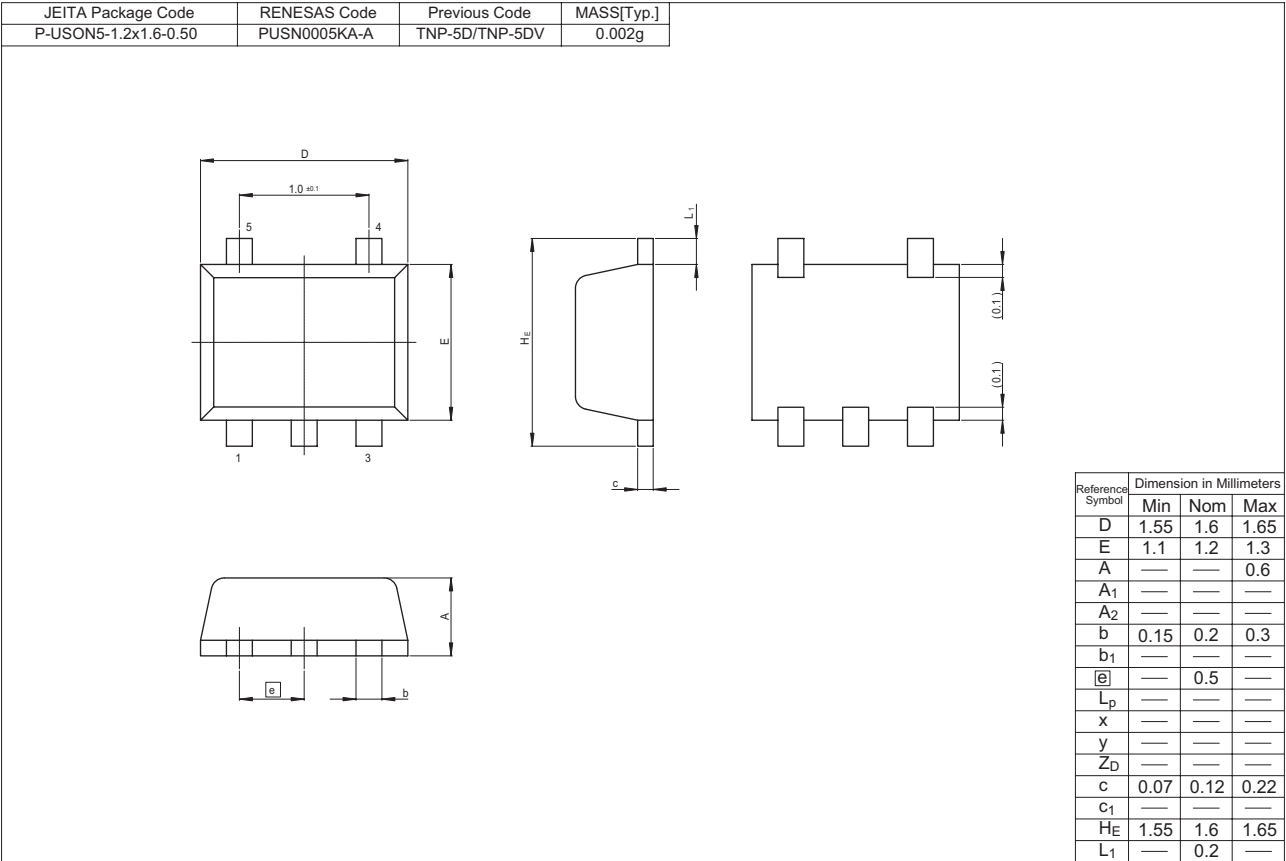
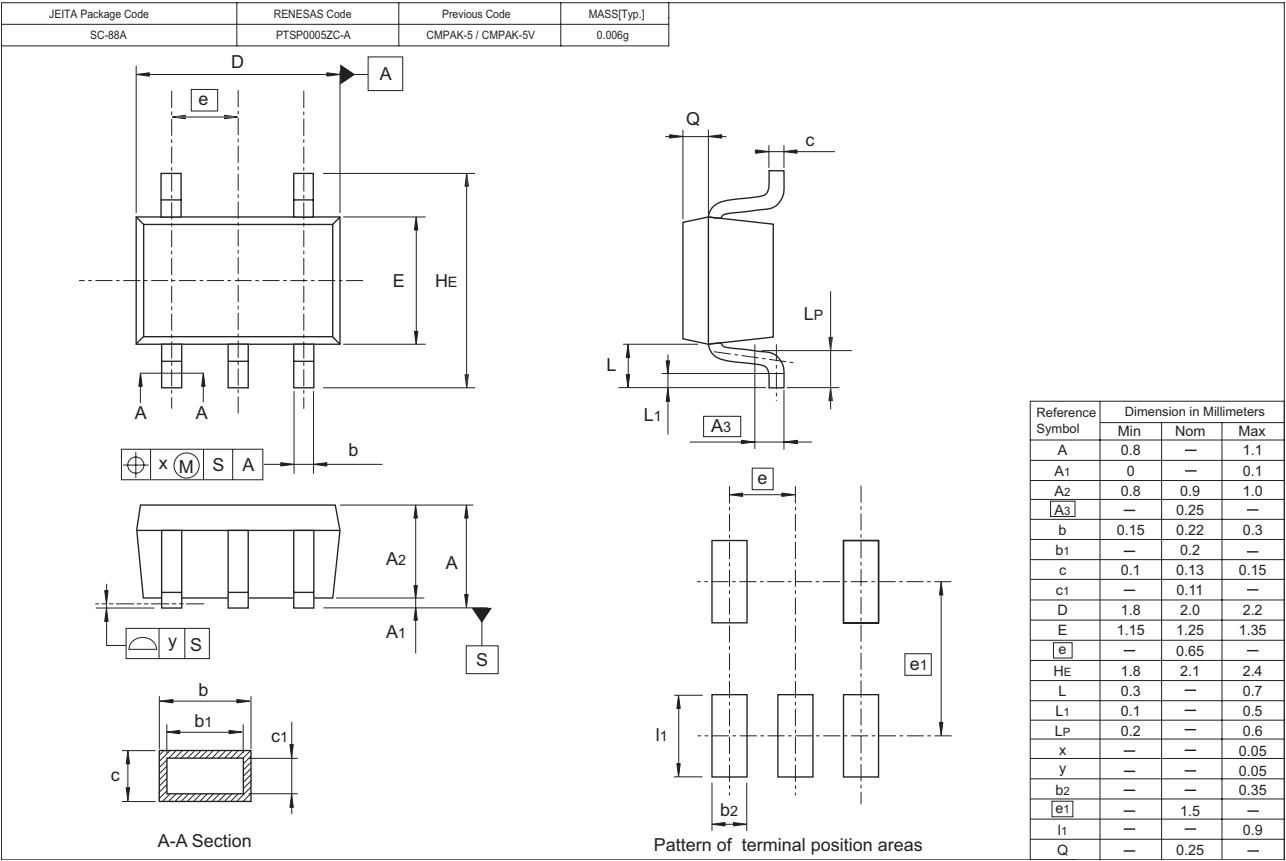
# Waveforms



$V_{CC}$ (V)	INPUTS		Vref
	$V_I$	$t_r / t_f$	
$3.3 \pm 0.3$	2.5 V	$\leq 3.0$ ns	50%
$5.0 \pm 0.5$	3 V	$\leq 3.0$ ns	1.5 V

Notes: 1. Input waveform :  $PRR \leq 1$  MHz,  $Z_o = 50 \Omega$ .  
2. The output are measured one at a time with one transition per measurement.

Package Dimensions



Notes:

1. This document is provided for reference purposes only so that Renesas customers may select the appropriate Renesas products for their use. Renesas neither makes warranties or representations with respect to the accuracy or completeness of the information contained in this document nor grants any license to any intellectual property rights or any other rights of Renesas or any third party with respect to the information in this document.
2. Renesas shall have no liability for damages or infringement of any intellectual property or other rights arising out of the use of any information in this document, including, but not limited to, product data, diagrams, charts, programs, algorithms, and application circuit examples.
3. You should not use the products or the technology described in this document for the purpose of military applications such as the development of weapons of mass destruction or for the purpose of any other military use. When exporting the products or technology described herein, you should follow the applicable export control laws and regulations, and procedures required by such laws and regulations.
4. All information included in this document such as product data, diagrams, charts, programs, algorithms, and application circuit examples, is current as of the date this document is issued. Such information, however, is subject to change without any prior notice. Before purchasing or using any Renesas products listed in this document, please confirm the latest product information with a Renesas sales office. Also, please pay regular and careful attention to additional and different information to be disclosed by Renesas such as that disclosed through our website. (<http://www.renesas.com>)
5. Renesas has used reasonable care in compiling the information included in this document, but Renesas assumes no liability whatsoever for any damages incurred as a result of errors or omissions in the information included in this document.
6. When using or otherwise relying on the information in this document, you should evaluate the information in light of the total system before deciding about the applicability of such information to the intended application. Renesas makes no representations, warranties or guarantees regarding the suitability of its products for any particular application and specifically disclaims any liability arising out of the application and use of the information in this document or Renesas products.
7. With the exception of products specified by Renesas as suitable for automobile applications, Renesas products are not designed, manufactured or tested for applications or otherwise in systems the failure or malfunction of which may cause a direct threat to human life or create a risk of human injury or which require especially high quality and reliability such as safety systems, or equipment or systems for transportation and traffic, healthcare, combustion control, aerospace and aeronautics, nuclear power, or undersea communication transmission. If you are considering the use of our products for such purposes, please contact a Renesas sales office beforehand. Renesas shall have no liability for damages arising out of the uses set forth above.
8. Notwithstanding the preceding paragraph, you should not use Renesas products for the purposes listed below:
  - (1) artificial life support devices or systems
  - (2) surgical implantations
  - (3) healthcare intervention (e.g., excision, administration of medication, etc.)
  - (4) any other purposes that pose a direct threat to human lifeRenesas shall have no liability for damages arising out of the uses set forth in the above and purchasers who elect to use Renesas products in any of the foregoing applications shall indemnify and hold harmless Renesas Technology Corp., its affiliated companies and their officers, directors, and employees against any and all damages arising out of such applications.
9. You should use the products described herein within the range specified by Renesas, especially with respect to the maximum rating, operating supply voltage range, movement power voltage range, heat radiation characteristics, installation and other product characteristics. Renesas shall have no liability for malfunctions or damages arising out of the use of Renesas products beyond such specified ranges.
10. Although Renesas endeavors to improve the quality and reliability of its products, IC products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Please be sure to implement safety measures to guard against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a Renesas product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other applicable measures. Among others, since the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or system manufactured by you.
11. In case Renesas products listed in this document are detached from the products to which the Renesas products are attached or affixed, the risk of accident such as swallowing by infants and small children is very high. You should implement safety measures so that Renesas products may not be easily detached from your products. Renesas shall have no liability for damages arising out of such detachment.
12. This document may not be reproduced or duplicated, in any form, in whole or in part, without prior written approval from Renesas.
13. Please contact a Renesas sales office if you have any questions regarding the information contained in this document, Renesas semiconductor products, or if you have any other inquiries.



**RENESAS SALES OFFICES**

<http://www.renesas.com>

Refer to "<http://www.renesas.com/en/network>" for the latest and detailed information.

**Renesas Technology America, Inc.**  
450 Holger Way, San Jose, CA 95134-1368, U.S.A  
Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

**Renesas Technology Europe Limited**  
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.  
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

**Renesas Technology (Shanghai) Co., Ltd.**  
Unit 204, 205, AZIA Center, No.1233 Lujiazui Ring Rd, Pudong District, Shanghai, China 200120  
Tel: <86> (21) 5877-1818, Fax: <86> (21) 6887-7858/7898

**Renesas Technology Hong Kong Ltd.**  
7th Floor, North Tower, World Finance Centre, Harbour City, Canton Road, Tsimshatsui, Kowloon, Hong Kong  
Tel: <852> 2265-6688, Fax: <852> 2377-3473

**Renesas Technology Taiwan Co., Ltd.**  
10th Floor, No.99, Fushing North Road, Taipei, Taiwan  
Tel: <886> (2) 2715-2888, Fax: <886> (2) 3518-3399

**Renesas Technology Singapore Pte. Ltd.**  
1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632  
Tel: <65> 6213-0200, Fax: <65> 6278-8001

**Renesas Technology Korea Co., Ltd.**  
Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea  
Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

**Renesas Technology Malaysia Sdn. Bhd**  
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia  
Tel: <603> 7955-9390, Fax: <603> 7955-9510