RENESAS HD74LV2GT66A 2-channel Analog Switch

REJ03D0145–0200Z (Previous ADE-205-698A (Z)) Rev.2.00 Oct.17.2003

Description

The HD74LV2GT66A has 2–channel analog switch in an 8 pin package. Each switch section has its own enable input control (C). High-level voltage applied to C turns on the associated switch section. Applications include signal gating, chopping, modulation, or demodulation (modem), and signal multiplexing for analog to digital and digital to analog conversion systems. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

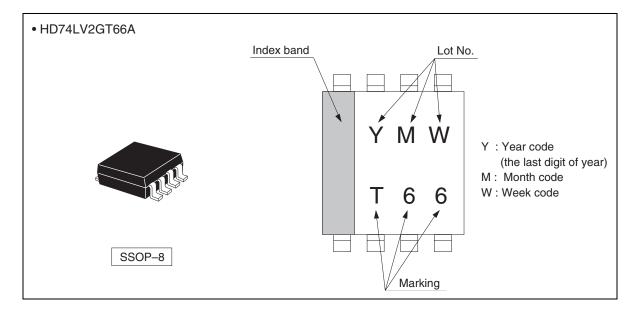
Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- Control input is TTL compatible input level. Supply voltage range : 3.0 to 5.5 V Operating temperature range : -40 to +85°C
- Control inputs V_{IH} (Max.) = 5.5 V (@V_{CC} = 0 V to 5.5 V)
- Control inputs have hysteresis voltage for the slow transition.
- Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LV2GT66AUSE	SSOP-8 pin	TTP-8DBV	US	E (3,000 pcs/reel)



Outline and Article Indication



Function Table

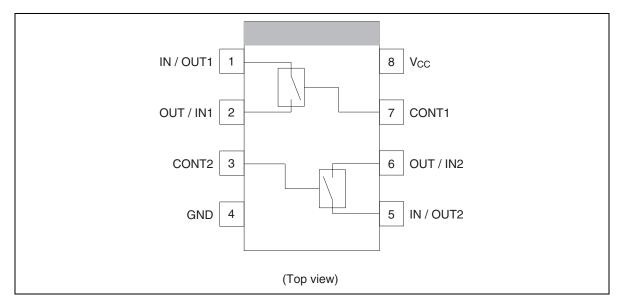
Control	Switch
L	OFF
Н	ON

H : High level

L : Low level



Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V _{CC}	-0.5 to 7.0	V	
Input voltage range *1	VI	-0.5 to 7.0	V	
Output voltage range *1, 2	Vo	–0.5 to V _{CC} + 0.5	V	Output : H or L
Input clamp current	I _{IK}	-20	mA	V ₁ < 0
Output clamp current	I _{ОК}	±50	mA	$V_0 < 0 \text{ or } V_0 > V_{CC}$
Continuous output current	lo	±25	mA	$V_0 = 0$ to V_{CC}
Continuous current through V_{CC} or GND	I_{CC} or I_{GND}	±50	mA	
Maximum power dissipation at Ta = 25° C (in still air) ^{*3}	PT	200	mW	
Storage temperature	Tstg	-65 to 150	°C	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

2. This value is limited to 5.5 V maximum.

3. The maximum package power dissipation was calculated using a junction temperature of 150°C.



Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	Vcc	3.0	5.5	V	
Input voltage range	VI	0	5.5	V	
Input / output voltage range	V _{I/O}	0	V _{CC}	V	
Input transition rise or fall rate	Δt / Δv	0	100	ns / V	$V_{CC} = 3.0$ to 3.6 V
		0	20		$V_{CC} = 4.5$ to 5.5 V
Operating free-air temperature	Ta	-40	85	°C	

Note: Unused or floating control inputs must be held high or low.

Electrical Characteristics

			T _a = 25°C		T _a = -40 to 85°C					
ltem	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Тур	Max	Unit	Test Conditions
Input voltage	V _{IH}	3.0 to 3.6	_	_	_	1.5	_	_	V	Control input only
		4.5 to 5.5	—	_	_	2.0	—	_	-	
	VIL	3.0 to 3.6	_	_	_	_	_	0.6	-	
		4.5 to 5.5	_	_	_	_	_	0.8	-	
Hysteresis	V _H	3.3	_	_	_	_	0.10	_	V	$V_T^+ - V_T^-$
voltage		5.0	_	—	_	_	0.15	_	-	
On-state switch	R _{ON}	3.0	_	50	150	_	_	190	Ω	$V_{IN} = V_{CC}$ or GND
resistance		4.5	_	40	75	_	_	100	-	$V_{\rm C} = V_{\rm IH}$
<u> </u>				100	400			005	0	$I_T = 1 \text{ mA}$
Peak on resistance	R _{ON (P)}	3.0		100	180		_	225	Ω	$V_{IN} = V_{CC}$ to GND $V_{C} = V_{IH}$
resistance		4.5	_	50	100	_	_	125		$V_{\rm C} = V_{\rm H}$ I _T = 1 mA
Difference of	ΔR_{ON}	3.0	_	10	20	_		30	Ω	$V_{IN} = V_{CC}$ to GND
on-state		4.5		7	15	_	_	20	-	$V_{\rm C} = V_{\rm IH}$
resistance between										$I_T = 1 \text{ mA}$
switches										
Off-state switch	$\mathbf{I}_{\text{s}(\text{OFF})}$	5.5	—	—	±0.1	—	—	±1.0	μΑ	$V_{IN} = V_{CC}, V_{OUT} = GND$
leakage current										or $V_{IN} = GND$, $V_O = V_{CC}$, $V_C = V_{IL}$
On-state switch	I _{s (ON)}	5.5	—	—	±0.1	—	_	±1.0	μΑ	$V_{IN} = V_{CC} \text{ or } GND$
leakage current										$V_{C} = V_{IH}$
Input current	I _{IN}	0 to 5.5	—	—	±0.1	—	—	±1.0	μΑ	$V_{IN} = 5.5 V \text{ or GND}$
Quiescent	Icc	5.5	—	—	—	—	—	10	μΑ	$V_{IN} = V_{CC}$ or GND
supply current	ΔI_{CC}	5.5	—	—	—	—	—	1.5	mΑ	$V_{IN} = 3.4 V$
Control input capacitance	C _{IC}	_	—	3.5	—	—	—	—	pF	
Switch terminal capacitance	$c_{\text{in/out}}$	_	_	4.0	_	_	_	_	pF	
Feed through capacitance	$C_{\text{IN-OUT}}$	_	_	0.5	_	_	_	_	pF	

Switching Characteristics

• $V_{CC} = 3.3 \pm 0.3 V$

		Ta = 2	25°C		Ta = -40 to 85°C			Test	FROM	то
ltem	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	—	1.5	6.0		10.0	ns	$C_L = 15 \text{ pF}$	IN/OUT	OUT/IN
delay time	t _{PHL}	_	4.0	9.0	_	12.0	_	$C_L = 50 \text{ pF}$	or OUT/IN	or IN/OUT
Enable time	t _{ZH}	_	4.0	11.0	_	15.0	ns	$C_L = 15 \text{ pF}$	С	IN/OUT
	t _{ZL}	_	6.0	18.0	_	22.0	_	$C_L = 50 \text{ pF}$	_	or OUT/IN
Disable time	t _{HZ}	—	5.0	11.0	_	15.0	ns	$C_L = 15 \text{ pF}$	С	IN/OUT
	t _{LZ}	_	8.0	18.0		22.0	_	$C_L = 50 \text{ pF}$	_	or OUT/IN

• $V_{CC} = 5.0 \pm 0.5 V$

		Ta = 2	25°C		Ta = -40 to 85°C			Test	FROM	то
ltem	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	1.0	4.0	_	7.0	ns	$C_L = 15 \text{ pF}$	IN/OUT	OUT/IN
delay time	t _{PHL}	_	3.0	6.0	_	8.0		$C_L = 50 \text{ pF}$	or OUT/IN	or IN/OUT
Enable time	t _{ZH}	—	3.0	7.0	_	10.0	ns	$C_L = 15 \text{ pF}$	С	IN/OUT
	t _{ZL}	_	5.0	12.0	_	16.0		$C_L = 50 \text{ pF}$	-	or OUT/IN
Disable time	t _{HZ}	_	4.0	7.0	_	10.0	ns	$C_L = 15 \text{ pF}$	С	IN/OUT
	t _{LZ}	_	6.0	12.0		16.0		$C_L = 50 \text{ pF}$	_	or OUT/IN

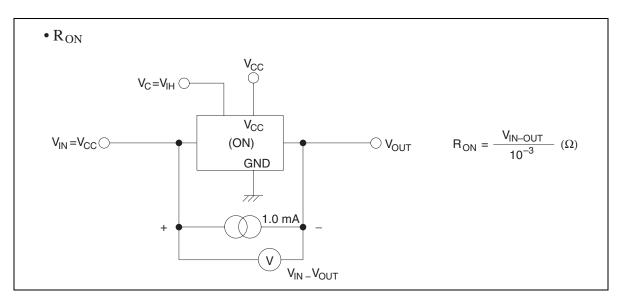
Operating Characteristics

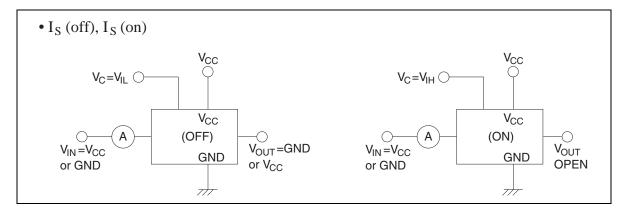
• $C_L = 50 \text{ pF}$

			Ta = 2	5°C			
ltem	Symbol	V _{cc} (V)	Min	Тур	Max	Unit	Test Conditions
Power dissipation capacitance	C_{PD}	5.0	_	4.0	—	pF	f = 10 MHz

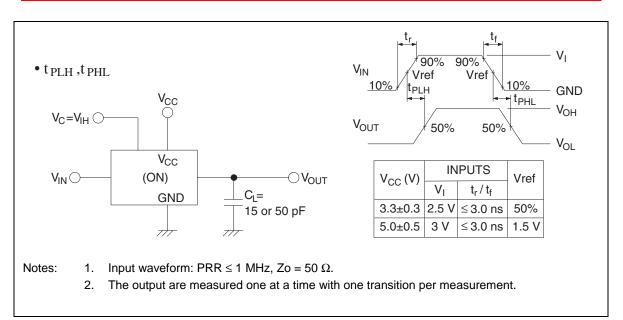


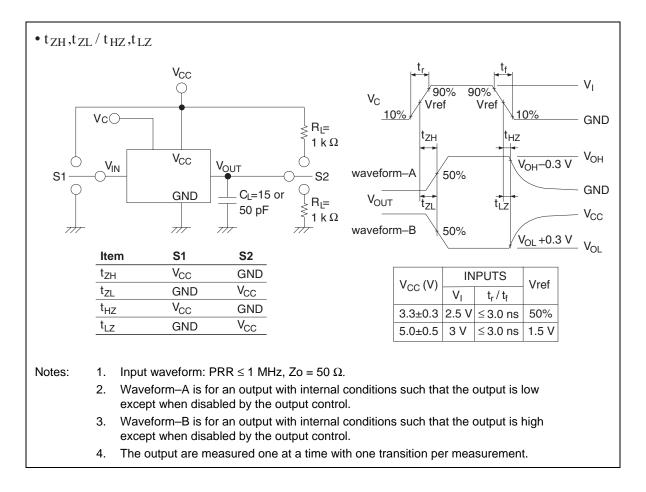
Test Circuit



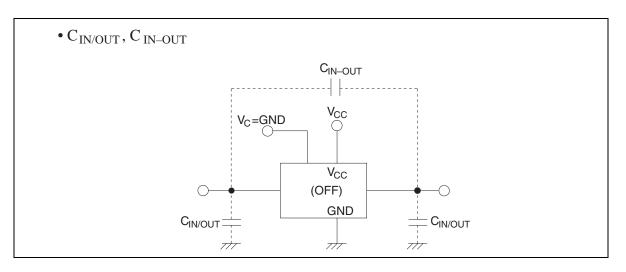






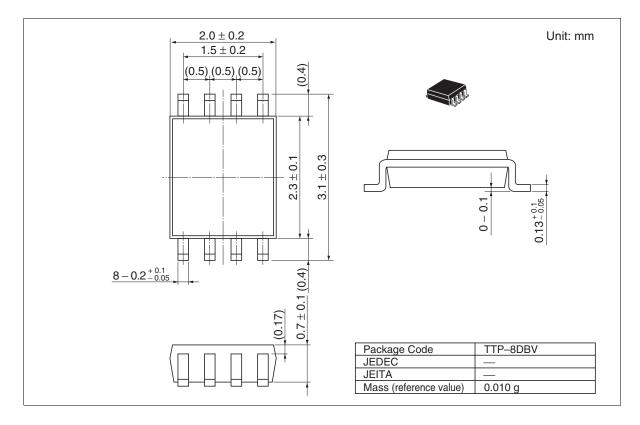








Package Dimensions





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