HD74LVC1G17

Schmitt-trigger Buffer

REJ03D0508–0100
Rev.1.00
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Description

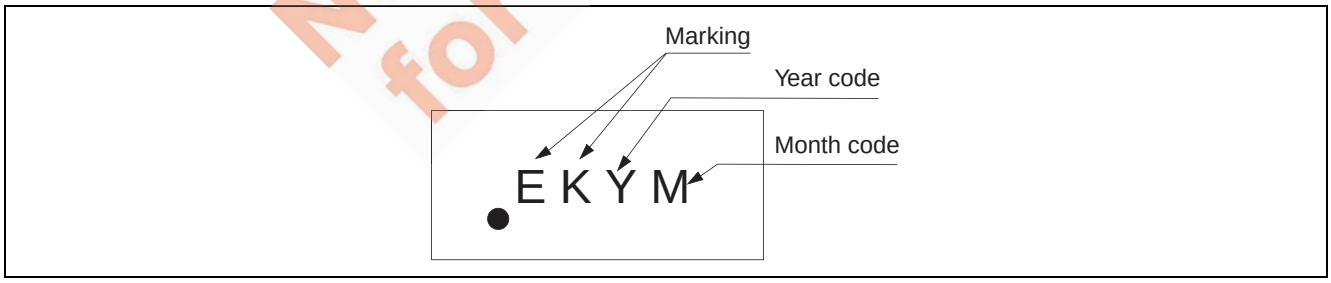
The HD74LVC1G17 has a Schmitt-trigger buffer in a 5-pin package. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

Features

- The basic gate function is lined up as Renesas uni logic series.
- Supply voltage range : 1.65 to 5.5 V
  Operating temperature range: –40 to +85°C
- All inputs: V_{IH} (Max.) = 5.5 V (@V_{CC} = 0 V to 5.5 V)
  All outputs: V_O (Max.) = 5.5 V (@V_{CC} = 0 V)
- Output current: ±4 mA (@V_{CC} = 1.65 V)
  ±8 mA (@V_{CC} = 2.3 V)
  ±24 mA (@V_{CC} = 3.0 V)
  ±32 mA (@V_{CC} = 4.5 V)
- Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LVC1G17CLE	WCSP-5 pin	SXBG0005KB–A (TBS-5AV)	CL	E (3,000 pcs/reel)

Article Indication



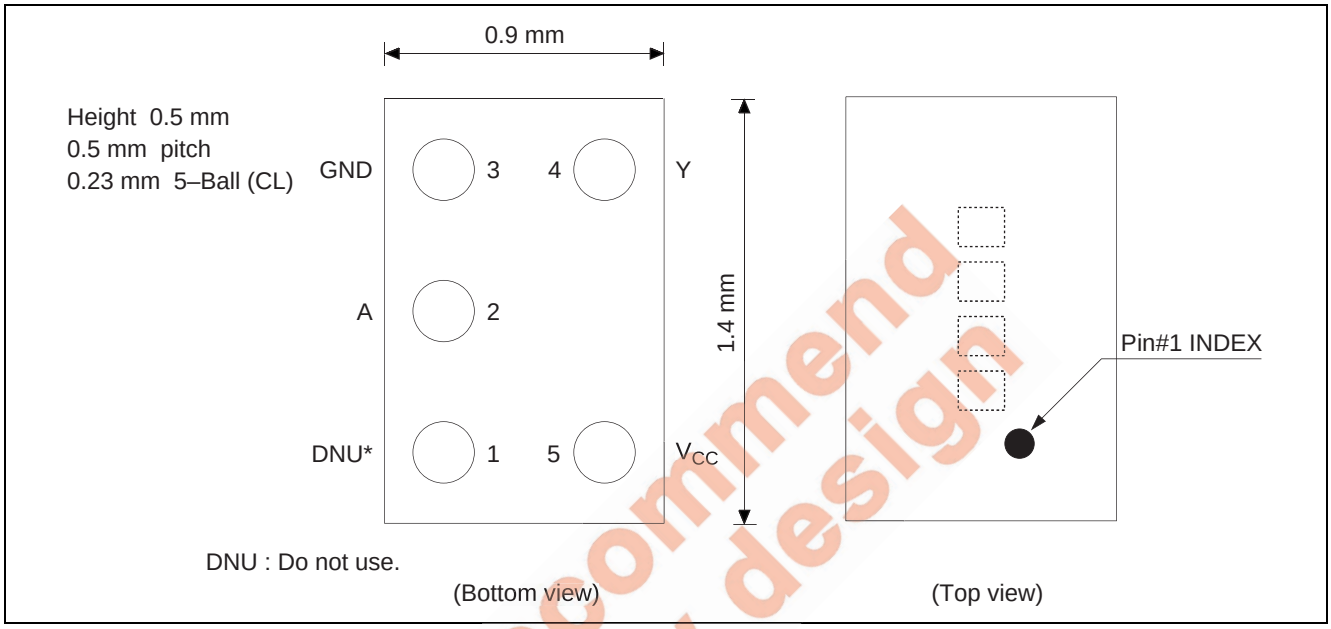
Function Table

Input A	Output Y
H	H
L	L

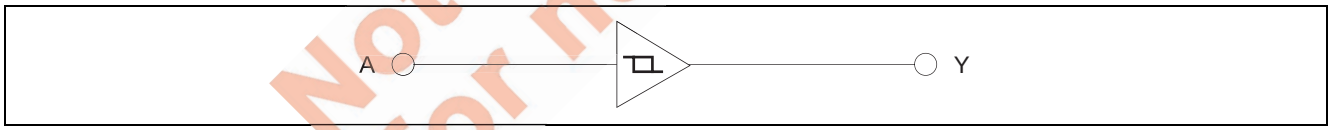
H: High level

L: Low level

Pin Arrangement



Logic Diagram



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V_{CC}	–0.5 to 6.5	V	
Input voltage range [*1]	V_I	–0.5 to 6.5	V	
Output voltage range [*1, 2]	V_O	–0.5 to V_{CC} +0.5	V	Output : H or L
		–0.5 to 6.5		V_{CC} : OFF
Input clamp current	I_{IK}	–50	mA	V_I < 0
Output clamp current	I_{OK}	–50	mA	V_O < 0
Continuous output current	I_O	±50	mA	V_O = 0 to V_{CC}
Continuous current through V_{CC} or GND	I_{CC} or I_{GND}	±100	mA	
Package Thermal impedance	θ_{ja}	132	°C/W	CL
Storage temperature	Tstg	–65 to 150	°C	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
2. This value is limited to 5.5 V maximum.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V_{CC}	1.65	5.5	V	
Input voltage range	V_I	0	5.5	V	
Output voltage range	V_O	0	V_{CC}	V	
Output current	I_{OL}	—	4	mA	V_{CC} = 1.65 V
		—	8		V_{CC} = 2.3 V
		—	16		V_{CC} = 3.0 V
		—	24		
		—	32		V_{CC} = 4.5 V
	I_{OH}	—	–4		V_{CC} = 1.65 V
		—	–8		V_{CC} = 2.3 V
		—	–16		V_{CC} = 3.0 V
		—	–24		
		—	–32		V_{CC} = 4.5 V
Operating free-air temperature	T_a	–40	85	°C	

Note: Unused or floating inputs must be held high or low.

Electrical Characteristics

Ta = –40 to 85°C

Item	Symbol	V_{CC} (V)	Min	Typ	Max	Unit	Test condition
Threshold voltage	V_T^+	1.8	0.8	—	1.4	V	
		2.5	1.2	—	1.7		
		3.3	1.6	—	2.3		
		5.0	2.3	—	3.0		
	V_T^-	1.8	0.4	—	0.7		
		2.5	0.6	—	1.0		
		3.3	0.9	—	1.4		
		5.0	1.5	—	2.0		
	ΔV_T	1.8	0.4	—	0.7		
		2.5	0.4	—	0.8		
		3.3	0.4	—	0.9		
		5.0	0.4	—	1.0		
Output voltage	V_{OH}	1.65 to 5.5	V_{CC}–0.1	—	—	V	I_{OH} = –100 µA
		1.65	1.2	—	—		I_{OH} = –4 mA
		2.3	1.9	—	—		I_{OH} = –8 mA
		3.0	2.4	—	—		I_{OH} = –16 mA
			2.3	—	—		I_{OH} = –24 mA
		4.5	3.8	—	—		I_{OH} = –32 mA
	V_{OL}	1.65 to 5.5	—	—	0.1		I_{OL} = 100 µA
		1.65	—	—	0.45		I_{OL} = 4 mA
		2.3	—	—	0.3		I_{OL} = 8 mA
		3.0	—	—	0.4		I_{OL} = 16 mA
			—	—	0.55		I_{OL} = 24 mA
		4.5	—	—	0.55		I_{OL} = 32 mA
Input current	I_{IN}	0 to 5.5	—	—	±5	µA	V_{IN} = 5.5 V or GND
Quiescent supply current	I_{CC}	5.5	—	—	10	µA	V_{IN} = V_{CC} or GND, I_O = 0
	ΔI_{CC}	3 to 5.5	—	—	500		One input at V_{CC}–0.6 V, Other input at V_{CC} or GND
Output leakage current	I_{OFF}	0	—	—	±10	µA	V_{IN} or V_O = 0 to 5.5 V
Input capacitance	C_{IN}	3.3	—	3.5	—	pF	V_{IN} = V_{CC} or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

V_{CC} = 1.8 ± 0.15 V

Item	Symbol	Ta = –40 to 85°C		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Max				
Propagation delay time	t_{PLH}	2.8	9.9	ns	C_L = 15 pF, R_L = 1 MΩ	A	Y
	t_{PHL}	3.8	11.0		C_L = 30 pF, R_L = 1.0 kΩ		

V_{CC} = 2.5 ± 0.2 V

Item	Symbol	Ta = –40 to 85°C		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Max				
Propagation delay time	t_{PLH}	1.6	5.5	ns	C_L = 15 pF, R_L = 1 MΩ	A	Y
	t_{PHL}	2.0	6.5		C_L = 30 pF, R_L = 500 Ω		

V_{CC} = 3.3 ± 0.3 V

Item	Symbol	Ta = –40 to 85°C		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Max				
Propagation delay time	t_{PLH}	1.5	4.6	ns	C_L = 15 pF, R_L = 1 MΩ	A	Y
	t_{PHL}	1.8	5.5		C_L = 50 pF, R_L = 500 Ω		

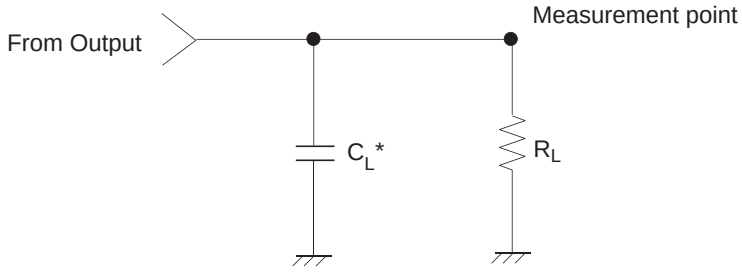
V_{CC} = 5.0 ± 0.5 V

Item	Symbol	Ta = –40 to 85°C		Unit	Test Conditions	FROM (Input)	TO (Output)
		Min	Max				
Propagation delay time	t_{PLH}	0.9	4.4	ns	C_L = 15 pF, R_L = 1 MΩ	A	Y
	t_{PHL}	1.2	5.0		C_L = 50 pF, R_L = 500 Ω		

Operating Characteristics

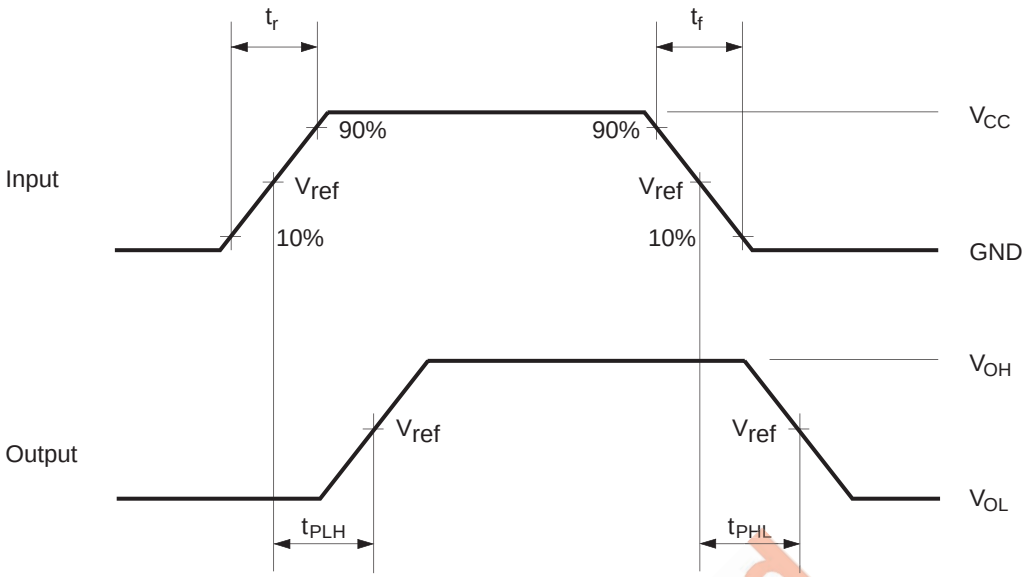
Item	Symbol	V_{CC} (V)	Ta = 25°C			Unit	Test Conditions
			Min	Typ	Max		
Power dissipation capacitance	C_{PD}	1.8	—	20	—	pF	f = 10 MHz
		2.5	—	21	—		
		3.3	—	22	—		
		5.0	—	26	—		

Test Circuit



Note: C_L includes probe and jig capacitance.

• Waveforms



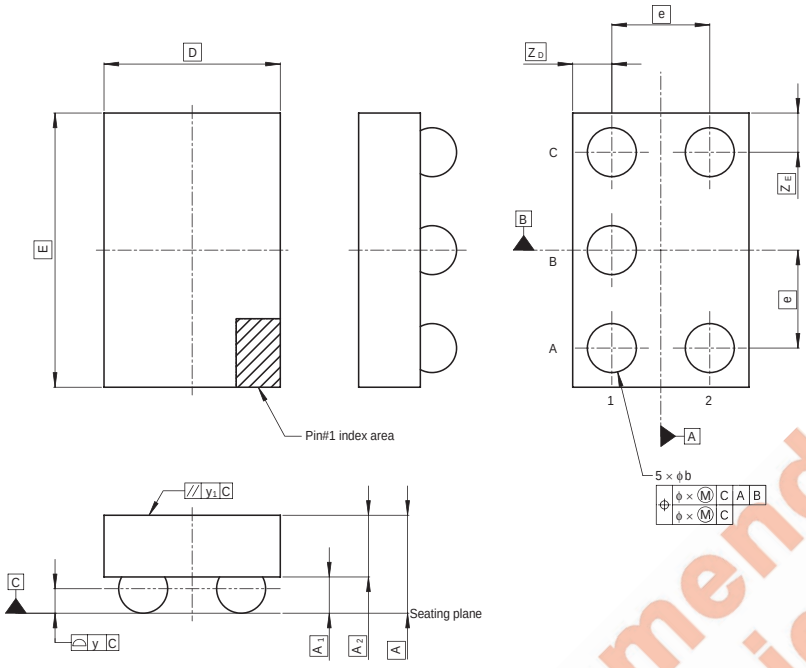
V_{CC} (V)	INPUTS		Vref	C_L	R_L
	V_I	t_r / t_f			
1.8±0.15	V_{CC}	≤ 2 ns	V_{CC} / 2	15 pF	1 MΩ
2.5±0.2	V_{CC}	≤ 2 ns	V_{CC} / 2	15 pF	1 MΩ
3.3±0.3	3 V	≤ 2.5 ns	1.5 V	15 pF	1 MΩ
5.0±0.5	V_{CC}	≤ 2.5 ns	V_{CC} / 2	15 pF	1 MΩ

V_{CC} (V)	INPUTS		Vref	C_L	R_L
	V_I	t_r / t_f			
1.8±0.15	V_{CC}	≤ 2 ns	V_{CC} / 2	30 pF	1.0 kΩ
2.5±0.2	V_{CC}	≤ 2 ns	V_{CC} / 2	30 pF	500 Ω
3.3±0.3	3 V	≤ 2.5 ns	1.5 V	50 pF	500 Ω
5.0±0.5	V_{CC}	≤ 2.5 ns	V_{CC} / 2	50 pF	500 Ω

Notes: 1. Input waveform: PRR ≤ 10 MHz, Zo = 50 Ω.
2. The output are measured one at a time with one transition per measurement.

Package Dimensions

JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
S-XFBGA5-0.9x1.4-0.50	SXBG0005KB-A	TBS-5AV	0.001g



Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
A	——	——	0.50
A_1	0.155	——	0.185
A_2	——	——	(0.315) *
b	0.20	——	0.25
D	——	0.90	——
E	——	1.40	——
e	——	0.50	——
x	——	——	0.05
y	——	——	0.05
y_1	——	——	0.20
Z_D	——	0.20	——
Z_E	——	0.20	——

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