

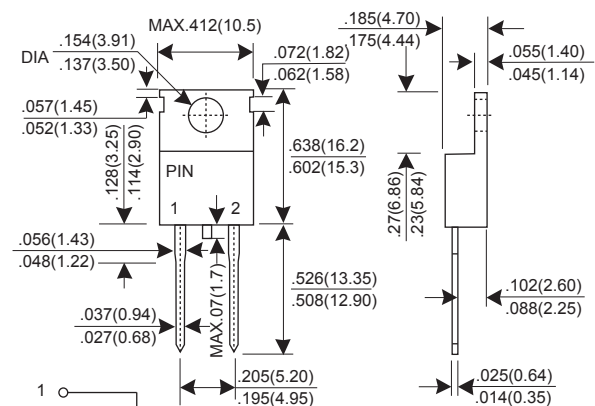


Glass Passivated Hyperfast Recovery Rectifier

- ★ Fast switching for high efficiency
- ★ Low noise
- ★ $T_{rr} = 40\text{ns}$
- ★ Low reverse leakage current
- ★ High voltage super FRD
- ★ PFC application

- ★ Case: Molded plastic TO-220AC
- ★ Epoxy: UL 94V-0 rate flame retardant ,
- ★ Terminals: Solderable per MIL-STD-202 method 208
- ★ Mounting position: Any
- ★ Weight: 2.07 grams

TO-220AC



Dimensions in inches and (millimeters)

Rating at 25°C ambient temperature unless otherwise specified.
Single phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

CHARACTERISTIC	SYMBOL	HFR30A06			UNIT
		Min.	Typ.	Max.	
Recurrent Peak Reverse Voltage	VRRM	-	-	600	V
RMS Voltage	VRMS	-	-	420	V
DC Blocking Voltage	VDC	-	-	600	V
Average Forward Rectified Current Tc=140°C	IF(AV)	-	-	30.0	A
Peak Forward Surge Current, 8.3ms single Half sine-wave superimposed on rated load (JEDEC method)	IFSM	-	-	325	A
Instantaneous Forward Voltage @30A(25°C) @30A(150°C)	VF	-	-	2.5 2.2	V
DC Reverse Current @Tj=25°C At Rated DC Blocking Voltage @Tj=150°C	IR	-	-	20 500	uA uA
Maximum Reverse Recovery Time (Note 1)	Trr	-	-	40	nS
Maximum Reverse Recovery Time (Note 2)	Trr	-	-	65	nS
Typical junction Capacitance (Note 3)	CJ	-	60	-	pF
Typical Thermal Resistance (Note 4)	RθJC	-	-	1.5	°CW
Operating Junction and Storage Temperature Range	TJ, TSTG	-65	-	175	°C

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RATINGS AND CHARACTERISTIC CURVES HFR30A06

FIG.1 - FORWARD CURRENT DERATING CURVE

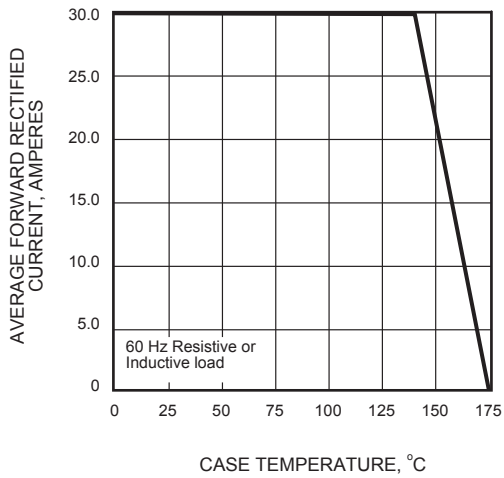


FIG.2 - MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

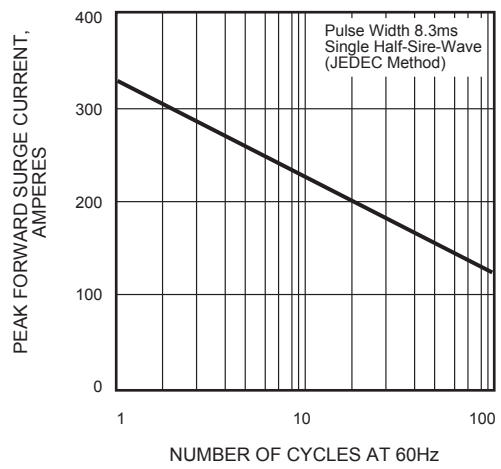


FIG.3 - TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

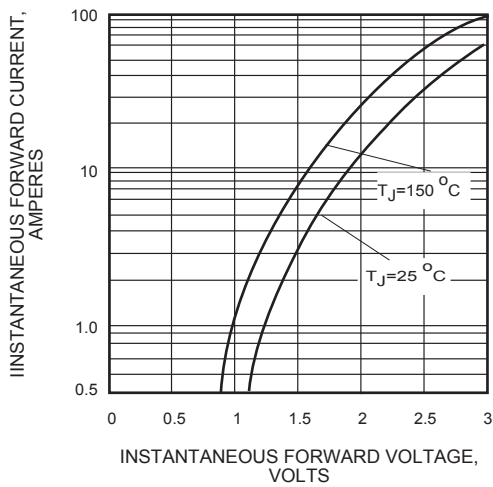


FIG.4 - TYPICAL REVERSE CHARACTERISTICS

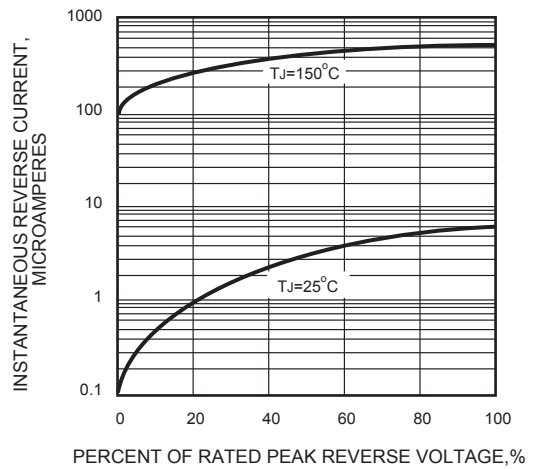


FIG.5 - t_{rr} , t_a AND t_b CURVES vs FORWARD CURRENT

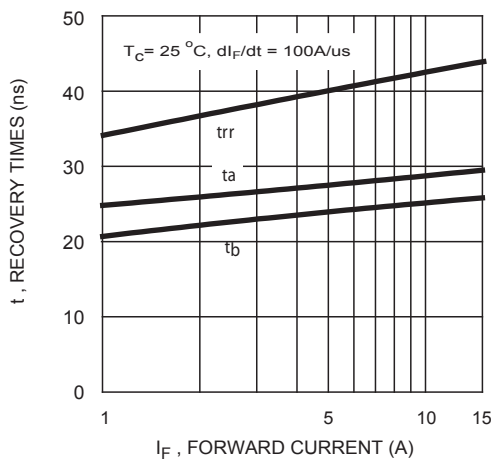
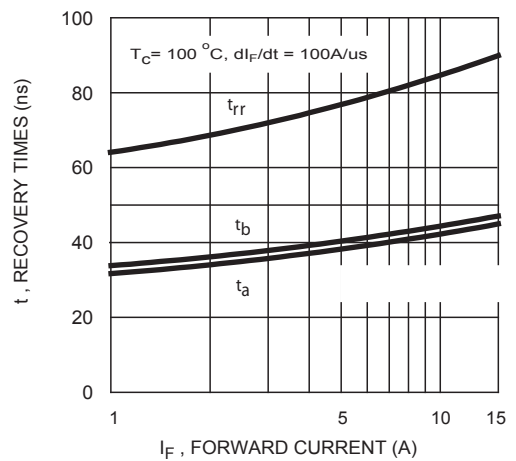


FIG.6 - t_{rr} , t_a AND t_b CURVES vs FORWARD CURRENT



RATINGS AND CHARACTERISTIC CURVES HFR30A06

FIG.7 - T_{rr} , t_a AND t_b CURVES vs FORWARD CURRENT

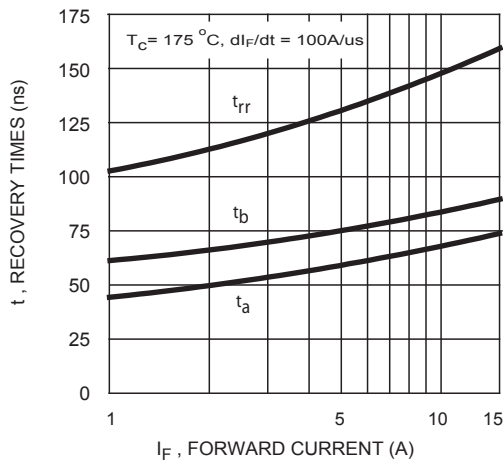
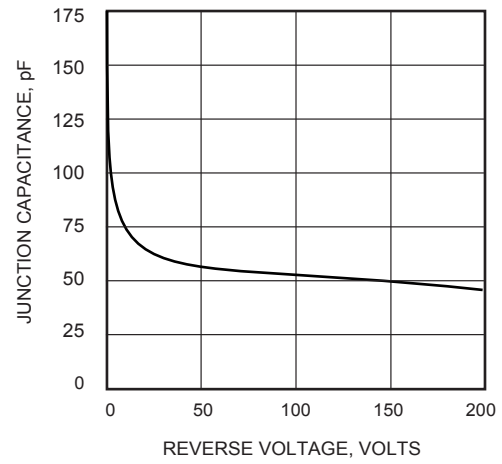


FIG.8 - TYPICAL JUNCTION CAPACITANCE



Test Circuits and Waveforms

FIG.9 - t_{rr} TEST CIRCUIT

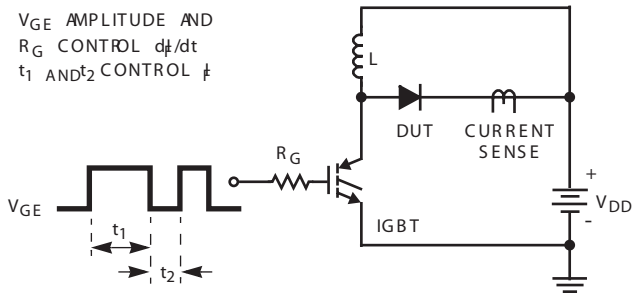


FIG.10 t_{rr} WAVEFORMS AND DEFINITIONS

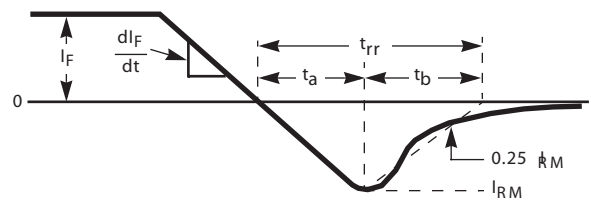


FIG.11 AVALANCHE ENERGY TEST CIRCUIT

$I_{MAX} = 1A$
 $L = 40mH$
 $R < 0.1$
 $E_{AVL} = 1/2 L I_L^2 [V_{R(AVL)}/(V_{R(AVL)} - V_{DD})]$
 $Q_1 = IGBT (BV_{CES} > DU V_{R(AVL)})$

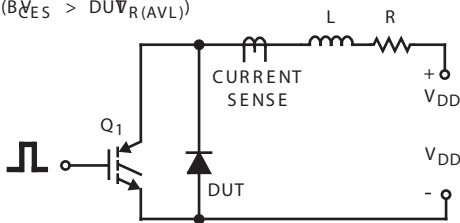


FIG.12 AVALANCHE CURRENT AND VOLTAGE WAVEFORMS

