



# Silicon N-Channel Power MOSFET



®

## HGP028N03A

### General Description:

HGP028N03A, the silicon N-channel Enhanced VDMOSFETs, is obtained by the high density Trench technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy. This device is suitable for use as A load switch and PWM applications. the package form is TO-220AB, which accords with the RoHS standard.

VDSS	30	V
ID (Silicon limited)	160	A
ID (Package limited)	60	A
PD	104.1	W
R <sub>DS(ON)Typ</sub>	2.3	mΩ

### Features:

- Fast Switching
- Low ON Resistance
- Low Gate Charge
- Low Reverse transfer capacitances
- 100% Single Pulse avalanche energy Test
- Halogen Free

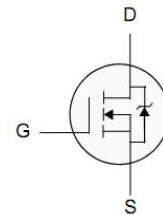
### Applications:

Power switch circuit of adaptor and charger.

TO-220AB



1. Gate 2. Drain 3. Source



### Absolute (T<sub>j</sub>= 25°C unless otherwise specified) :

Symbol	Parameter	Rating	Units
V <sub>DSS</sub>	Drain-to-Source Voltage	30	V
I <sub>D</sub>	Continuous Drain Current T <sub>C</sub> = 25 °C (Silicon limited)	160	A
	Continuous Drain Current T <sub>C</sub> = 25 °C (Package limited)	60	A
	Continuous Drain Current T <sub>C</sub> = 100 °C (Package limited)	60	A
I <sub>DM</sub> <sup>a1</sup>	Pulsed Drain Current T <sub>C</sub> = 25 °C	240	A
V <sub>GS</sub>	Gate-to-Source Voltage	±18	V
E <sub>AS</sub> <sup>a2</sup>	Avalanche Energy	287.3	mJ
P <sub>D</sub>	Power Dissipation T <sub>C</sub> = 25 °C	104.1	W
	Derating Factor above 25°C	0.8	W/°C
T <sub>j</sub> , T <sub>stg</sub>	Operating Junction and Storage Temperature Range	150, -55 to 150	°C
T <sub>L</sub>	Maximum Temperature for Soldering	300	°C



# HGP028N03A



**Electrical Characteristics** ( $T_j = 25^\circ\text{C}$  unless otherwise specified) :

## OFF Characteristics

Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$V_{DSS}$	Drain to Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu\text{A}$	30	--	--	V
$I_{DSS}$	Drain to Source Leakage Current	$V_{DS}=30\text{V}, V_{GS}=0\text{V}, T_j = 25^\circ\text{C}$	--	--	1	$\mu\text{A}$
		$V_{DS}=24\text{V}, V_{GS}=0\text{V}, T_j = 125^\circ\text{C}$	--	--	100	
$I_{GSS(F)}$	Gate to Source Forward Leakage	$V_{GS}=18\text{V}$	--	--	100	nA
$I_{GSS(R)}$	Gate to Source Reverse Leakage	$V_{GS}=-18\text{V}$	--	--	-100	nA

## ON Characteristics

Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$R_{DS(ON)}$	Drain-to-Source On-Resistance	$V_{GS}=10\text{V}, I_D=30\text{A}$	--	2.3	2.8	$\text{m}\Omega$
	Drain-to-Source On-Resistance	$V_{GS}=4.5\text{V}, I_D=30\text{A}$	--	3.5	4.5	$\text{m}\Omega$
$V_{GS(\text{TH})}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1	--	2.5	V

## Dynamic Characteristics

Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$R_g$	Gate resistance	$V_{GS}=0\text{V}, V_{DS}=0\text{V}, f=1\text{MHz}$	--	1.8	--	$\Omega$
$C_{iss}$	Input Capacitance	$V_{GS} = 0\text{V}$ $V_{DS} = 15\text{V}$ $f = 1.0\text{MHz}$	--	2723	--	$\text{pF}$
$C_{oss}$	Output Capacitance		--	2024	--	
$C_{rss}$	Reverse Transfer Capacitance		--	338	--	

## Resistive Switching Characteristics

Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$t_{d(\text{ON})}$	Turn-on Delay Time	$V_{GS}=10\text{V}$ $R_g=10\Omega$ $VDD=15\text{V}$ $I_d=30\text{A}$	--	16	--	ns
$t_r$	Rise Time		--	24	--	
$t_{d(\text{OFF})}$	Turn-Off Delay Time		--	82	--	
$t_f$	Fall Time		--	68	--	
$Q_g$	Total Gate Charge	$V_{GS} = 10\text{V}$ $VDD = 15\text{V}$ $I_d = 30\text{A}$	--	56	--	nC
$Q_{gs}$	Gate to Source Charge		--	7.7	--	
$Q_{gd}$	Gate to Drain ("Miller")Charge		--	14.5	--	

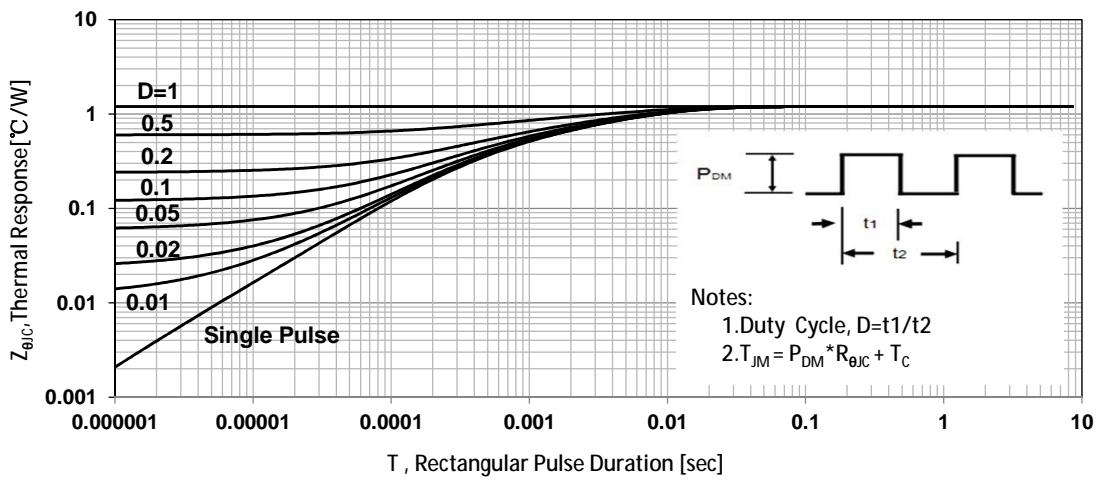
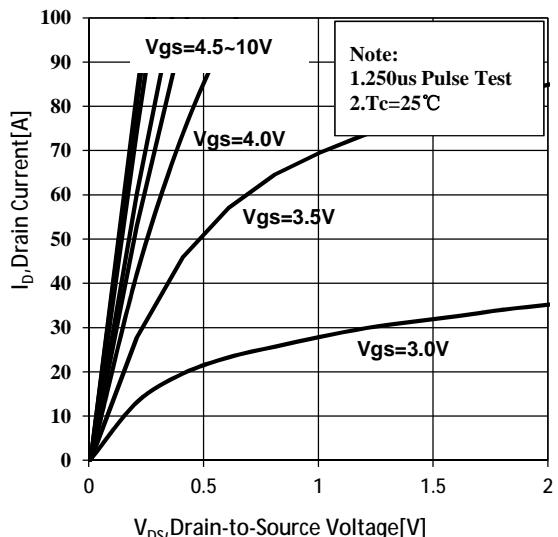
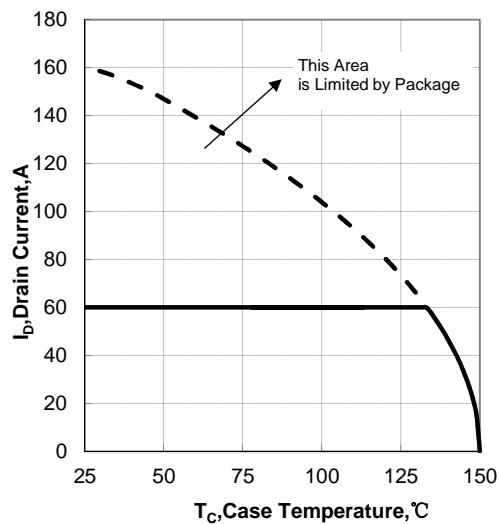
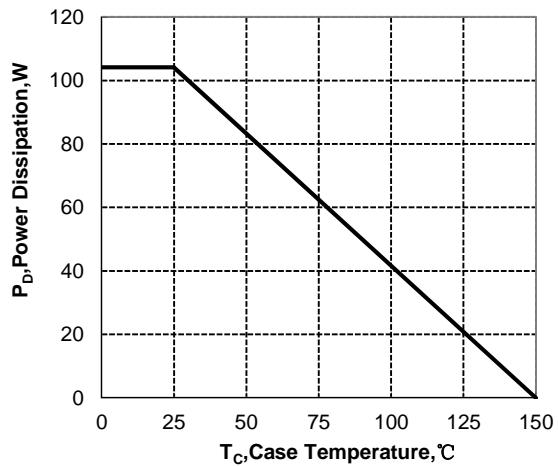
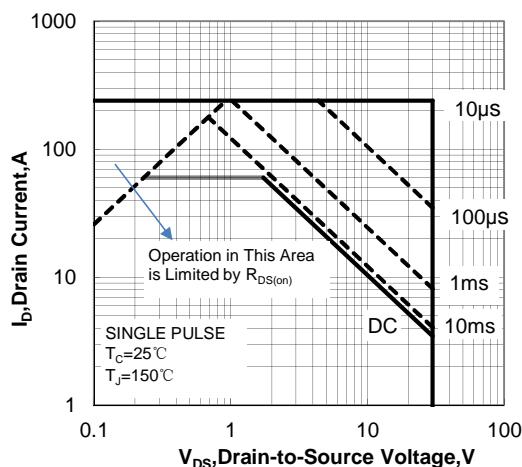
**Source-Drain Diode Characteristics**

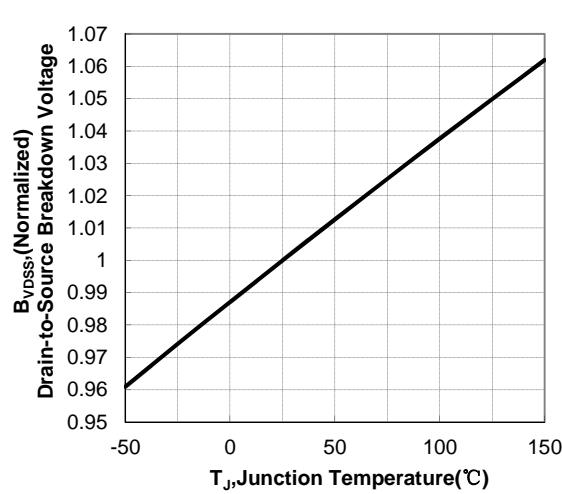
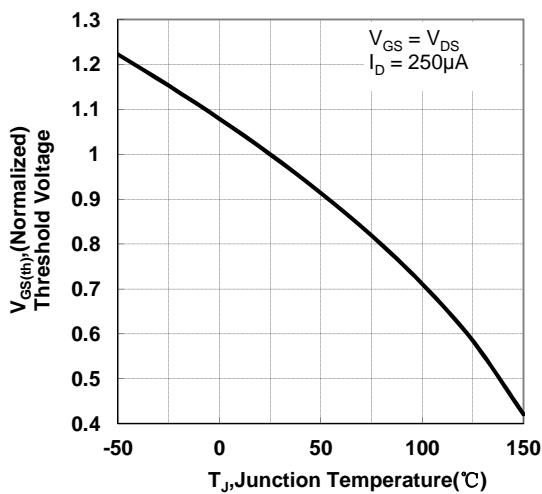
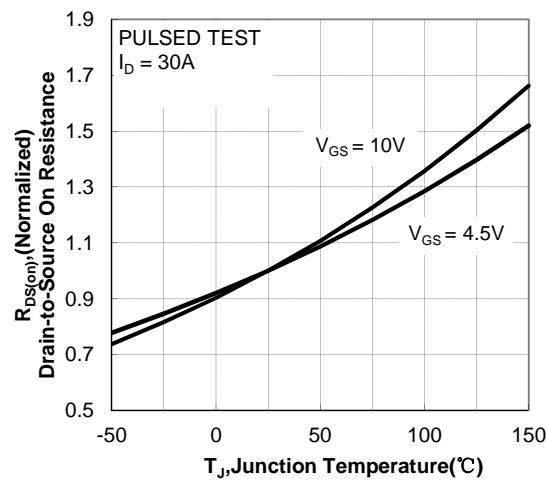
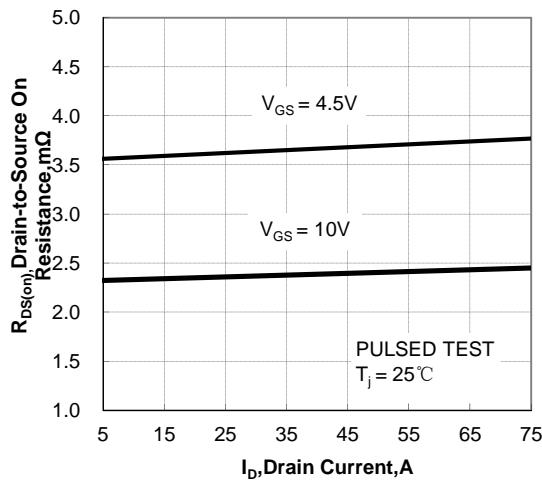
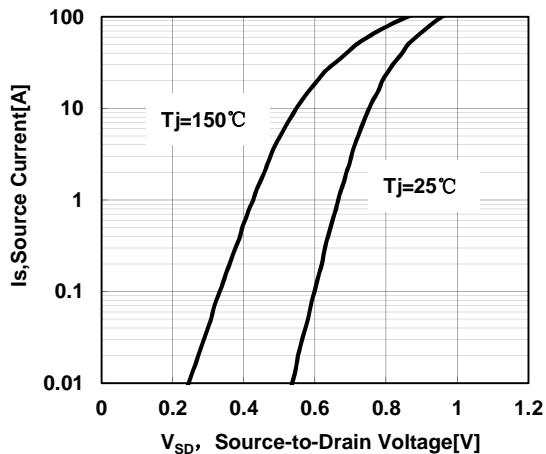
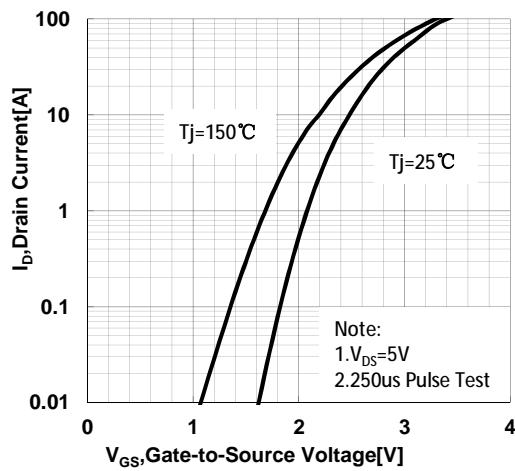
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
I <sub>S</sub>	Continuous Source Current (Body Diode)	T <sub>C</sub> = 25 °C	--	--	60	A
I <sub>SM</sub>	Maximum Pulsed Current (Body Diode)		--	--	240	A
V <sub>SD</sub>	Diode Forward Voltage	I <sub>S</sub> =30A, V <sub>GS</sub> =0V	--	--	1.2	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>S</sub> =30A di/dt=100A/us, V <sub>GS</sub> =0V	--	68	--	ns
Q <sub>rr</sub>	Reverse Recovery Charge		--	72	--	nC
Pulse width tp≤300μs, δ≤2%						

Symbol	Parameter	Max.	Units
R <sub>θJC</sub>	Junction-to-Case	1.2	°C/W
R <sub>θJA</sub>	Junction-to-Ambient	62.5	°C/W

**Notes:**

- a1: Repetitive rating; pulse width limited by maximum junction temperature  
a2: L=0.5mH, I<sub>as</sub>=33.9A Start TJ=25°C

**Characteristics Curve:**



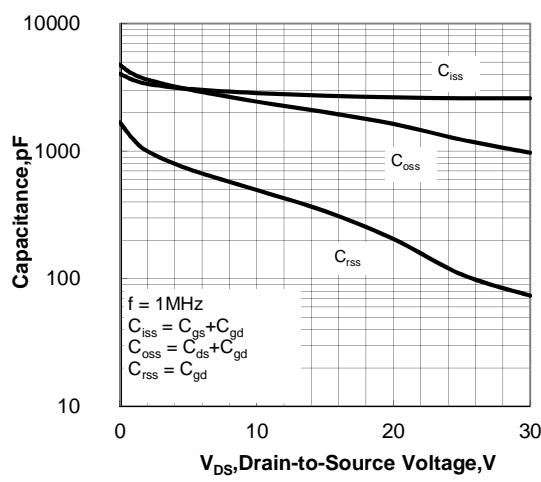


Figure 12. Capacitance Characteristics

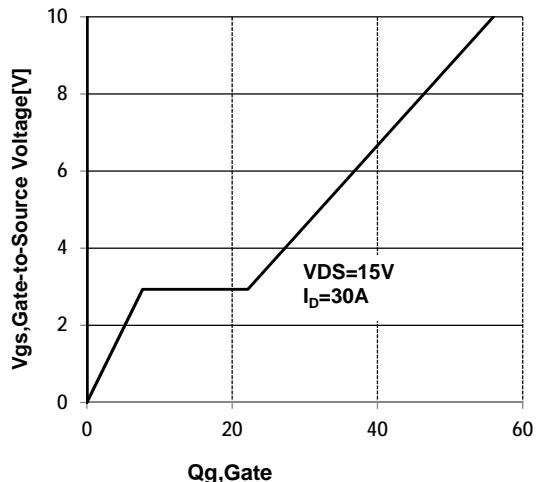


Figure 13 Typical Gate Charge vs Gate to Source Voltage



### Test Circuit and Waveform

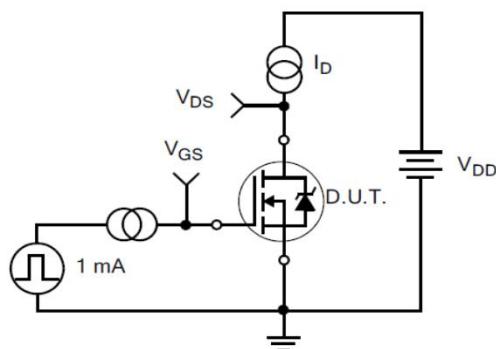


Figure 14. Gate Charge Test Circuit

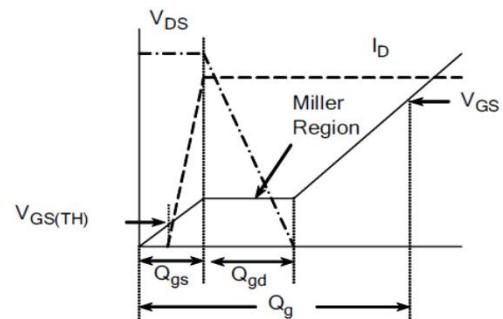


Figure 15. Gate Charge Waveforms

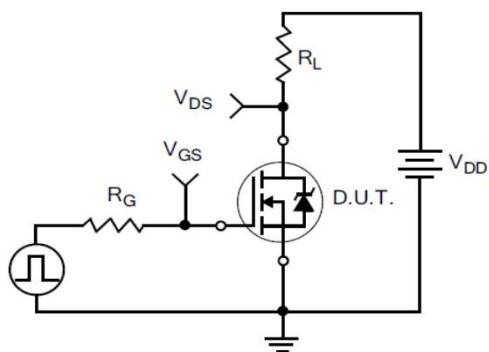


Figure 16. Resistive Switching Test Circuit

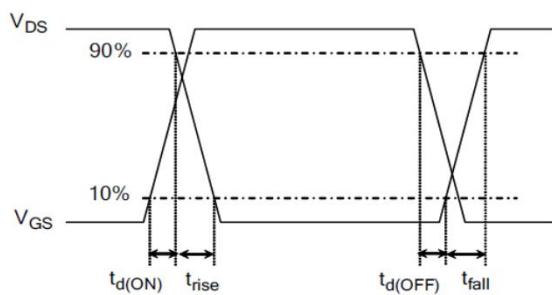


Figure 17. Resistive Switching

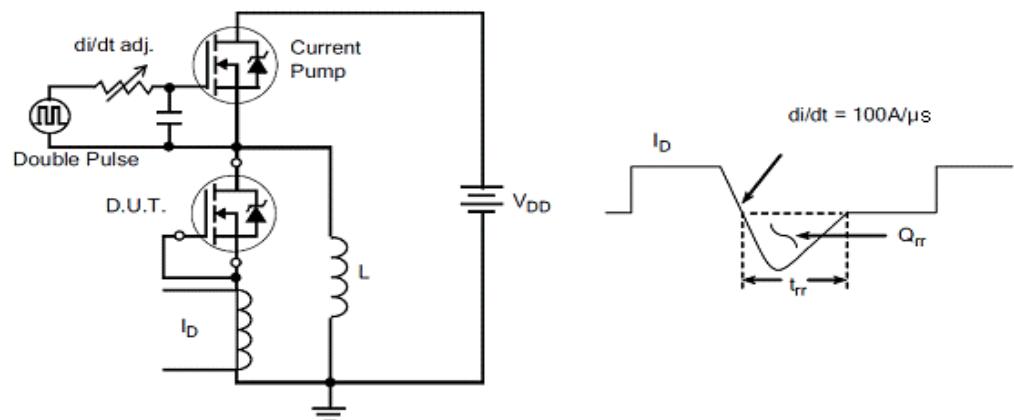


Figure 18. Diode Reverse Recovery Test Circuit

Figure 19. Diode Reverse Recovery Waveforms

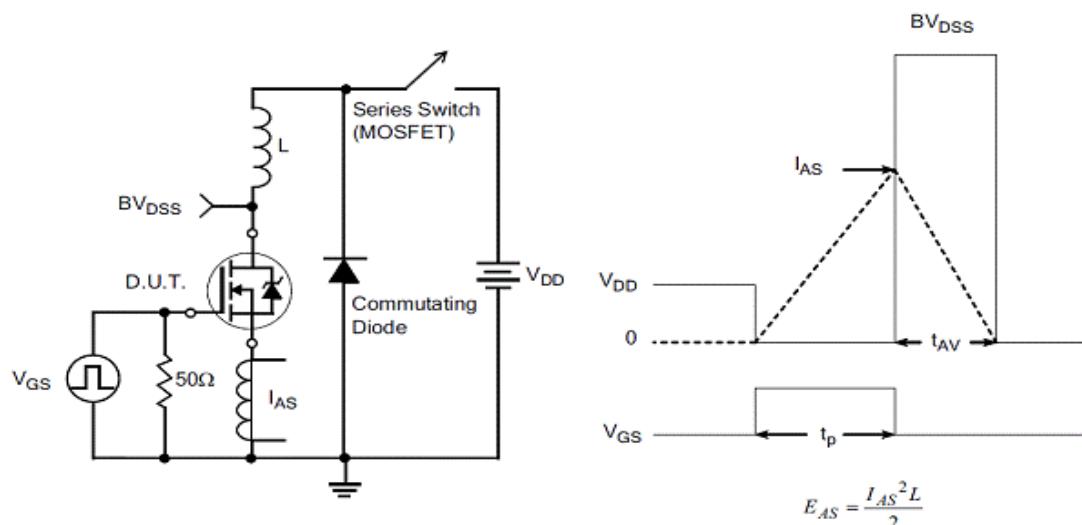
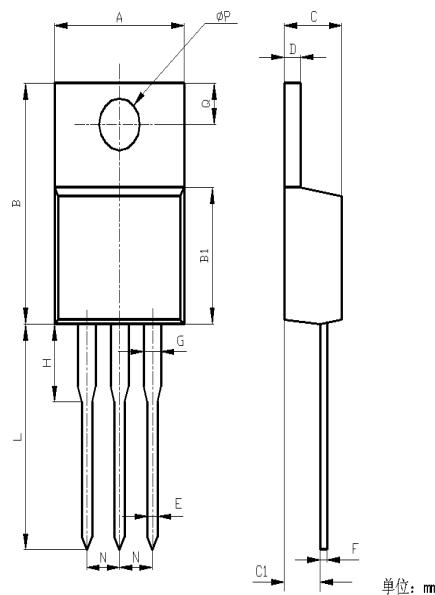


Figure 20. Unclamped Inductive Switching Test Circuit

Figure 21. Unclamped Inductive Switching

**Package Information:**

Symbol	Values(mm)	
	MIN	MAX
A	9.6	10.6
B	15	16
B1	8.9	9.5
C	4.3	4.8
C1	2.3	3.1
D	1.2	1.4
E	0.7	0.9
F	0.3	0.6
G	1.17	1.37
H	2.7	3.8
L*	12.6	14.8
N	2.34	2.74
Q	2.4	3
Φ P	3.5	3.9

TO-220AB Package



**The name and content of poisonous and harmful material in products**

Part's Name	Hazardous Substance									
	Pb	Hg	Cd	Cr (VI)	PBB	PBDE	DIBP	DEHP	DBP	BBP
Limit	≤ 0.1%	≤ 0.1%	≤ 0.01%	≤ 0.1%						
Lead Frame	○	○	○	○	○	○	○	○	○	○
Molding	○	○	○	○	○	○	○	○	○	○
Chip	○	○	○	○	○	○	○	○	○	○
Wire Bonding	○	○	○	○	○	○	○	○	○	○
Solder	×	○	○	○	○	○	○	○	○	○
Note	○: Means the hazardous material is under the criterion of 2011/65/EU. ×: Means the hazardous material exceeds the criterion of 2011/65/EU. The plumbum element of solder exist in products presently, but within the allowed range of Eurogroup's RoHS.									

## Warnings

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. It is suggested to be used under 80 percent of the maximum ratings of the device.
2. When installing the heatsink, please pay attention to the torsional moment and the smoothness of the heatsink.
3. VDMOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. This publication is made by Huajing Microelectronics and subject to regular change without notice.

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