# HGTG30N60B3D, HGT4E30N60B3DS

Data Sheet

December 2001

# 60A, 600V, UFS Series N-Channel IGBT with Anti-Parallel Hyperfast Diode

The HGTG30N60B3D, and HGT4E30N60B3DS are MOS gated high voltage switching devices combining the best features of MOSFETs and bipolar transistors. These devices have the high input impedance of a MOSFET and the low on-state conduction loss of a bipolar transistor. The much lower on-state voltage drop varies only moderately between 25°C and 150°C. The IGBT used is the development type TA49170. The diode used in anti-parallel with the IGBT is the development type TA49053.

The IGBT is ideal for many high voltage switching applications operating at moderate frequencies where low conduction losses are essential, such as: AC and DC motor controls, power supplies and drivers for solenoids, relays and contactors.

Formerly Developmental Type TA49172.

## **Ordering Information**

PART NUMBER	PACKAGE	BRAND
HGTG30N60B3D	TO-247	G30N60B3D
HGT4E30N60B3DS	TO-268AA	G30N60B3D

NOTE: When ordering, use the entire part number.

#### **Features**

- 60A, 600V, T<sub>C</sub> = 25°C
- · 600V Switching SOA Capability
- Typical Fall Time........... 90ns at T<sub>J</sub> = 150°C
- · Short Circuit Rating
- · Low Conduction Loss
- · Hyperfast Anti-Parallel Diode

## **Packaging**

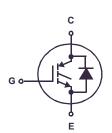




**TO-268AA** 



#### **Symbol**



FAIRCHILD CORPORATION IGBT PRODUCT IS COVERED BY ONE OR MORE OF THE FOLLOWING U.S. PATENTS							
4,364,073	4,417,385	4,430,792	4,443,931	4,466,176	4,516,143	4,532,534	4,587,713
4,598,461	4,605,948	4,620,211	4,631,564	4,639,754	4,639,762	4,641,162	4,644,637
4,682,195	4,684,413	4,694,313	4,717,679	4,743,952	4,783,690	4,794,432	4,801,986
4,803,533	4,809,045	4,809,047	4,810,665	4,823,176	4,837,606	4,860,080	4,883,767
4,888,627	4,890,143	4,901,127	4,904,609	4,933,740	4,963,951	4,969,027	

# HGTG30N60B3D, HGT4E30N60B3DS

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## **Absolute Maximum Ratings** $T_C = 25^{\circ}C$ , Unless Otherwise Specified

HGTG30N60B3D, HGT4E30N60B3DS	UNITS
600	V
60	Α
30	Α
25	Α
220	Α
±20	V
±30	V
60A at 600V	
208	W
1.67	W/ <sup>o</sup> C
-55 to 150	°C
260	°C
4	μs
10	μs
	HGT4E30N60B3DS 600  60 30 25 220 ±20 ±30 60A at 600V 208 1.67 -55 to 150 260 4

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

#### NOTES:

- 1. Pulse width limited by maximum junction temperature.
- 2.  $V_{CE(PK)} = 360V$ ,  $T_J = 125^{\circ}C$ ,  $R_G = 3\Omega$ .

# **Electrical Specifications** $T_C = 25^{\circ}C$ , Unless Otherwise Specified

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNITS
Collector to Emitter Breakdown Voltage	BV <sub>CES</sub>	$I_C = 250 \mu A, V_{GE} = 0 V$		600	-	-	V
Collector to Emitter Leakage Current	I <sub>CES</sub>	V <sub>CE</sub> = BV <sub>CES</sub>	$T_{\rm C} = 25^{\rm o}{\rm C}$	-	-	250	μΑ
		T <sub>C</sub>	$T_{\rm C} = 150^{\rm o}{\rm C}$	-	-	3	mA
Collector to Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	SAT) $I_{C} = I_{C110},$ $V_{GE} = 15V$	$T_{\rm C} = 25^{\rm o}{\rm C}$	-	1.45	1.9	V
			$T_{\rm C} = 150^{\rm o}{\rm C}$	-	1.7	2.1	V
Gate to Emitter Threshold Voltage	V <sub>GE(TH)</sub>	I <sub>C</sub> = 250μA, V <sub>CE</sub> = V <sub>GE</sub>		4.2	5	6	V
Gate to Emitter Leakage Current	I <sub>GES</sub>	V <sub>GE</sub> = ±20V		-	-	±250	nA
Switching SOA	SSOA	Voc = 15V I = 100uH	V <sub>CE</sub> (PK) = 480V	200	-	-	А
			V <sub>CE</sub> (PK) = 600V	60	-	-	А
Gate to Emitter Plateau Voltage	V <sub>GEP</sub>	I <sub>C</sub> = I <sub>C110</sub> , V <sub>CE</sub> = 0.5 BV <sub>CES</sub>		-	7.2	-	V
On-State Gate Charge	Q <sub>G(ON)</sub>	I <sub>C</sub> = I <sub>C110</sub> , V <sub>GE</sub> = 15V		-	170	190	nC
		$V_{CE} = 0.5 \text{ BV}_{CES}$	V <sub>GE</sub> = 20V	-	230	250	nC
Current Turn-On Delay Time	t <sub>d(ON)I</sub>	IGBT and Diode at $T_J = 25^{\circ}C$ , $I_{CE} = I_{C110}$ , $V_{CE} = 0.8 \; BV_{CES}$ , $V_{GE} = 15V$ , $R_G = 3\Omega$ , $L = 1mH$ , Test Circuit (Figure 19)		-	36	-	ns
Current Rise Time	t <sub>rl</sub>			-	25	-	ns
Current Turn-Off Delay Time	t <sub>d</sub> (OFF)I			-	137	-	ns
Current Fall Time	t <sub>fl</sub>			-	58	-	ns
Turn-On Energy	E <sub>ON</sub>			-	550	800	μJ
Turn-Off Energy (Note 3)	E <sub>OFF</sub>			-	680	900	μЈ

## HGTG30N60B3D, HGT4E30N60B3DS

**Electrical Specifications**  $T_C = 25^{\circ}C$ , Unless Otherwise Specified (Continued)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
Current Turn-On Delay Time	<sup>t</sup> d(ON)I	IGBT and Diode at T <sub>J</sub> = 150 <sup>o</sup> C,	-	32	-	ns
Current Rise Time	t <sub>rl</sub>	$\begin{bmatrix} I_{CE} = I_{C110}, \\ V_{CE} = 0.8 \text{ BV}_{CES}, \end{bmatrix}$	-	24	-	ns
Current Turn-Off Delay Time	t <sub>d(OFF)I</sub>	$V_{GE} = 15V$ ,	-	275	320	ns
Current Fall Time	t <sub>fl</sub>	$R_G = 3\Omega$ , L = 1mH,	-	90	150	ns
Turn-On Energy	E <sub>ON</sub>	Test Circuit (Figure 19)	-	1300	1550	μЈ
Turn-Off Energy (Note 3)	E <sub>OFF</sub>		-	1600	1900	μЈ
Diode Forward Voltage	V <sub>EC</sub>	I <sub>EC</sub> = 30A	-	1.95	2.5	V
Diode Reverse Recovery Time	t <sub>rr</sub>	$I_{EC} = 1A$ , $dI_{EC}/dt = 200A/\mu s$	-	32	40	ns
		$I_{EC} = 30A$ , $dI_{EC}/dt = 200A/\mu s$	-	45	55	ns
Thermal Resistance Junction To Case	$R_{\theta JC}$	IGBT	-	-	0.6	°C/W
		Diode	-	-	1.3	°C/W

#### NOTE:

## Typical Performance Curves Unless Otherwise Specified

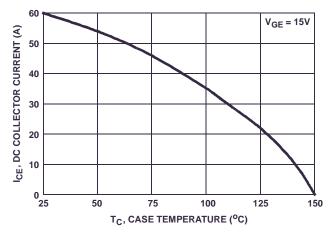


FIGURE 1. DC COLLECTOR CURRENT vs CASE TEMPERATURE

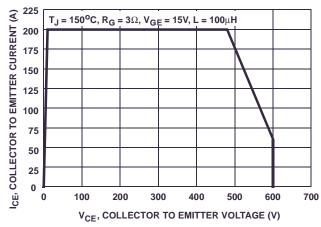


FIGURE 2. MINIMUM SWITCHING SAFE OPERATING AREA

<sup>3.</sup> Turn-Off Energy Loss (E<sub>OFF</sub>) is defined as the integral of the instantaneous power loss starting at the trailing edge of the input pulse and ending at the point where the collector current equals zero (I<sub>CE</sub> = 0A). All devices were tested per JEDEC Standard No. 24-1 Method for Measurement of Power Device Turn-Off Switching Loss. This test method produces the true total Turn-Off Energy Loss.

#### Typical Performance Curves Unless Otherwise Specified (Continued)

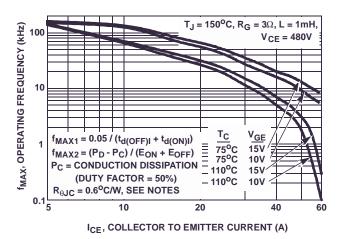


FIGURE 3. OPERATING FREQUENCY vs COLLECTOR TO EMITTER CURRENT

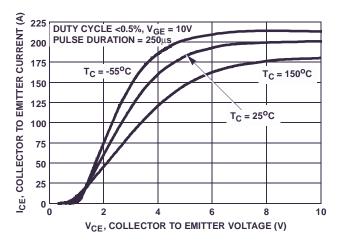


FIGURE 5. COLLECTOR TO EMITTER ON-STATE VOLTAGE

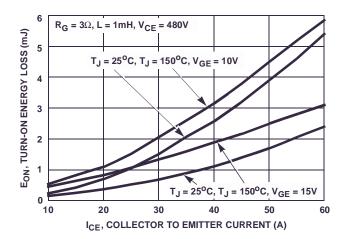


FIGURE 7. TURN-ON ENERGY LOSS vs COLLECTOR TO EMITTER CURRENT

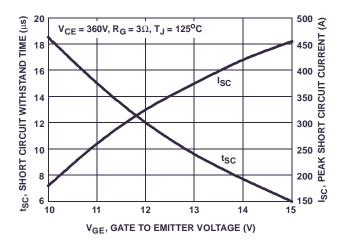


FIGURE 4. SHORT CIRCUIT WITHSTAND TIME

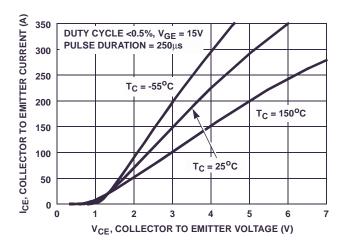


FIGURE 6. COLLECTOR TO EMITTER ON-STATE VOLTAGE

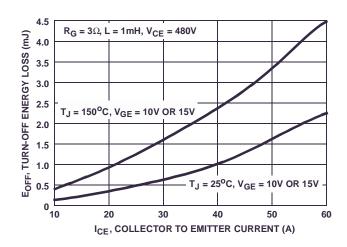


FIGURE 8. TURN-OFF ENERGY LOSS vs COLLECTOR TO EMITTER CURRENT

## Typical Performance Curves Unless Otherwise Specified (Continued)

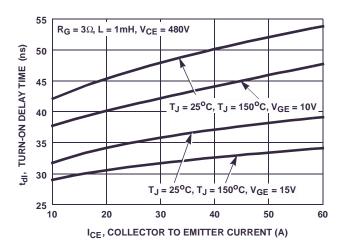


FIGURE 9. TURN-ON DELAY TIME vs COLLECTOR TO EMITTER CURRENT

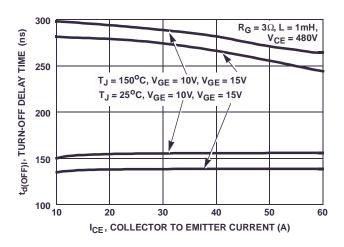


FIGURE 11. TURN-OFF DELAY TIME vs COLLECTOR TO EMITTER CURRENT

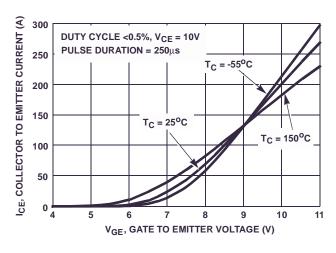


FIGURE 13. TRANSFER CHARACTERISTIC

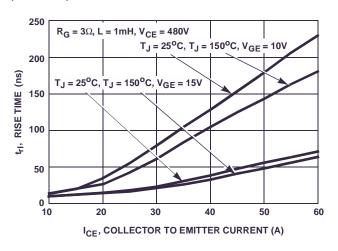


FIGURE 10. TURN-ON RISE TIME vs COLLECTOR TO EMITTER CURRENT

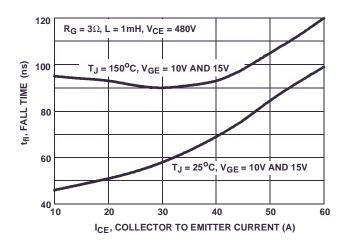


FIGURE 12. FALL TIME vs COLLECTOR TO EMITTER CURRENT

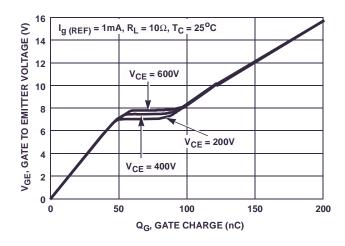


FIGURE 14. GATE CHARGE WAVEFORMS

## Typical Performance Curves Unless Otherwise Specified (Continued)

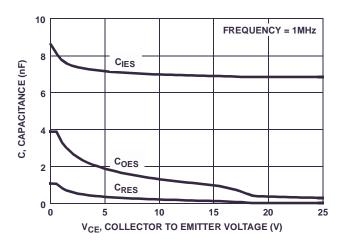


FIGURE 15. CAPACITANCE vs COLLECTOR TO EMITTER VOLTAGE

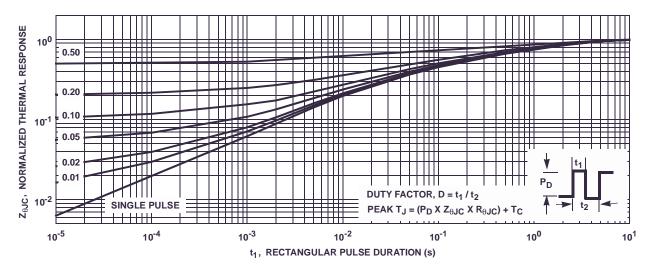


FIGURE 16. NORMALIZED TRANSIENT THERMAL RESPONSE, JUNCTION TO CASE

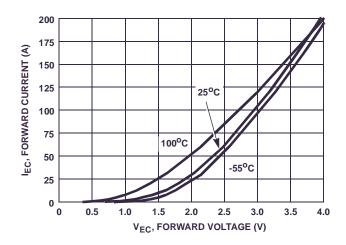


FIGURE 17. DIODE FORWARD CURRENT vs FORWARD VOLTAGE DROP

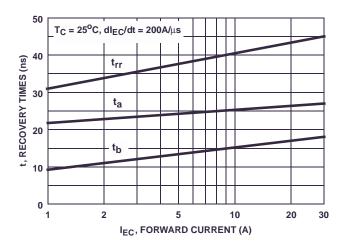


FIGURE 18. RECOVERY TIME vs FORWARD CURRENT

#### Test Circuit and Waveforms

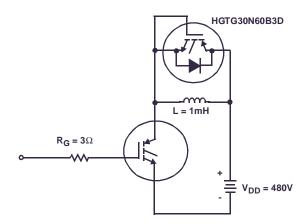


FIGURE 19. INDUCTIVE SWITCHING TEST CIRCUIT

## Handling Precautions for IGBTs

Insulated Gate Bipolar Transistors are susceptible to gate-insulation damage by the electrostatic discharge of energy through the devices. When handling these devices, care should be exercised to assure that the static charge built in the handler's body capacitance is not discharged through the device. With proper handling and application procedures, however, IGBTs are currently being extensively used in production by numerous equipment manufacturers in military, industrial and consumer applications, with virtually no damage problems due to electrostatic discharge. IGBTs can be handled safely if the following basic precautions are taken:

- Prior to assembly into a circuit, all leads should be kept shorted together either by the use of metal shorting springs or by the insertion into conductive material such as "ECCOSORBD™ LD26" or equivalent.
- 2. When devices are removed by hand from their carriers, the hand being used should be grounded by any suitable means for example, with a metallic wristband.
- 3. Tips of soldering irons should be grounded.
- Devices should never be inserted into or removed from circuits with power on.
- Gate Voltage Rating Never exceed the gate-voltage rating of V<sub>GEM</sub>. Exceeding the rated V<sub>GE</sub> can result in permanent damage to the oxide layer in the gate region.
- 6. Gate Termination The gates of these devices are essentially capacitors. Circuits that leave the gate open-circuited or floating should be avoided. These conditions can result in turn-on of the device due to voltage buildup on the input capacitor due to leakage currents or pickup.
- Gate Protection These devices do not have an internal monolithic Zener diode from gate to emitter. If gate protection is required an external Zener is recommended.

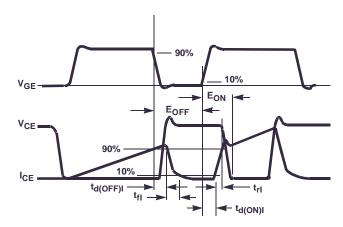


FIGURE 20. SWITCHING TEST WAVEFORMS

## Operating Frequency Information

Operating frequency information for a typical device (Figure 3) is presented as a guide for estimating device performance for a specific application. Other typical frequency vs collector current (I\_{CE}) plots are possible using the information shown for a typical unit in Figures 5, 6, 7, 8, 9 and 11. The operating frequency plot (Figure 3) of a typical device shows  $f_{MAX1}$  or  $f_{MAX2}$ ; whichever is smaller at each point. The information is based on measurements of a typical device and is bounded by the maximum rated junction temperature.

 $f_{MAX1}$  is defined by  $f_{MAX1}=0.05/(t_{d(OFF)I}+t_{d(ON)I}).$  Deadtime (the denominator) has been arbitrarily held to 10% of the on-state time for a 50% duty factor. Other definitions are possible.  $t_{d(OFF)I}$  and  $t_{d(ON)I}$  are defined in Figure 20. Device turn-off delay can establish an additional frequency limiting condition for an application other than  $T_{JM}.\ t_{d(OFF)I}$  is important when controlling output ripple under a lightly loaded condition.

 $f_{MAX2}$  is defined by  $f_{MAX2} = (P_D - P_C)/(E_{OFF} + E_{ON}).$  The allowable dissipation (P\_D) is defined by  $P_D = (T_{JM} \, . \, T_C)/R_{\theta JC}$ . The sum of device switching and conduction losses must not exceed  $P_D$ . A 50% duty factor was used (Figure 3) and the conduction losses (P\_C) are approximated by  $P_C = (V_{CE} \, x \, I_{CE})/2$ .

 $E_{ON}$  and  $E_{OFF}$  are defined in the switching waveforms shown in Figure 20.  $E_{ON}$  is the integral of the instantaneous power loss ( $I_{CE} \times V_{CE}$ ) during turn-on and  $E_{OFF}$  is the integral of the instantaneous power loss ( $I_{CE} \times V_{CE}$ ) during turn-off. All tail losses are included in the calculation for  $E_{OFF}$ ; i.e., the collector current equals zero ( $I_{CF} = 0$ ).

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