



# Silicon N-Channel Power MOSFET



## HGU09N06A-G

### General Description:

HGU09N06A-G, the silicon N-channel Enhanced VDMOSFETs, is obtained by the high density Trench technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy. This device is suitable for use as a load switch and PWM applications. The package form is TO-251, which accords with the RoHS standard.

### Features:

- Fast Switching
- Low ON Resistance ( $R_{DS(on)} \leq 9.8 \text{ m}\Omega$ )
- Low Gate Charge
- Low Reverse transfer capacitances
- 100% Single Pulse avalanche energy Test
- Halogen Free

### Applications:

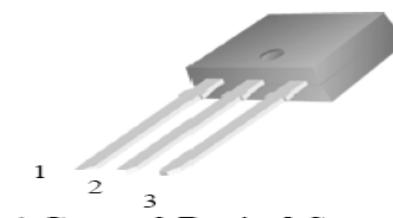
Power switch circuit of adaptor and charger.

E-cigarette, Electric Tool

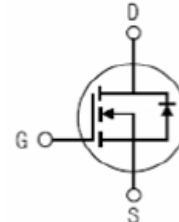
**Absolute** ( $T_j = 25^\circ\text{C}$  unless otherwise specified)

$V_{DSS}$	60	V
$I_D$	55	A
$P_D$	59.5	W
$R_{DS(ON)Typ}$ $V_{GS}=10\text{V}$	8.1	$\text{m}\Omega$

TO-251



Inner Equivalent Principium Chart



Symbol	Parameter	Rating	Units
$V_{DSS}$	Drain-to-Source Voltage	60	V
$I_D$	Continuous Drain Current $T_C = 25^\circ\text{C}$	55	A
	Continuous Drain Current $T_C = 100^\circ\text{C}$	34.8	A
$I_{DM}^{a2}$	Pulsed Drain Current	220	A
$V_{GS}$	Gate-to-Source Voltage	$\pm 20$	V
$E_{AS}^{a2}$	Single Pulse Avalanche Energy	145	mJ
$P_D$	Power Dissipation $T_C = 25^\circ\text{C}$	59.5	W
	Derating Factor above $25^\circ\text{C}$	0.47	$\text{W}/^\circ\text{C}$
$T_J, T_{stg}$	Operating Junction and Storage Temperature Range	150, -55 to 150	$^\circ\text{C}$
$T_L$	MaximumTemperature for Soldering	300	$^\circ\text{C}$

**Electrical Characteristics (T<sub>j</sub>= 25°C unless otherwise specified):**

OFF Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
V <sub>DSS</sub>	Drain to Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250 μA	60	--	--	V
I <sub>DSS</sub>	Drain to Source Leakage Current	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V, T <sub>j</sub> = 25°C	--	--	1	μA
		V <sub>DS</sub> = 48V, V <sub>GS</sub> = 0V, T <sub>j</sub> = 125°C	--	--	100	
I <sub>GSS(F)</sub>	Gate to Source Forward Leakage	V <sub>GS</sub> =20V	--	--	100	nA
I <sub>GSS(R)</sub>	Gate to Source Reverse Leakage	V <sub>GS</sub> = -20V	--	--	-100	nA

ON Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
R <sub>DS(ON)</sub>	Drain-to-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =19A		8.1	9.8	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =19A		11	13.5	mΩ
V <sub>GS(TH)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	1.0	1.75	2.5	V
Pulse width t <sub>p</sub> ≤ 300 μs, δ ≤ 2%						

Dynamic Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
R <sub>g</sub>	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz	--	1.5	--	Ω
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V V <sub>DS</sub> = 30V f = 1.0MHz	--	1226	--	pF
C <sub>oss</sub>	Output Capacitance		--	348	--	
C <sub>rss</sub>	Reverse Transfer Capacitance		--	9.97	--	

Resistive Switching Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
t <sub>d(ON)</sub>	Turn-on Delay Time	V <sub>GS</sub> =10V, R <sub>G</sub> =3Ω V <sub>DD</sub> =30V, I <sub>D</sub> =14A	--	11.9	--	ns
t <sub>r</sub>	Rise Time		--	5.5	--	
t <sub>d(OFF)</sub>	Turn-Off Delay Time		--	30.6	--	
t <sub>f</sub>	Fall Time		--	4.8	--	
Q <sub>g</sub>	Total Gate Charge	I <sub>D</sub> =14A V <sub>DD</sub> =30V V <sub>GS</sub> = 10V	--	18	--	nC
Q <sub>gs</sub>	Gate to Source Charge		--	3.2	--	
Q <sub>gd</sub>	Gate to Drain ("Miller")Charge		--	2.95	--	

**Source-Drain Diode Characteristics**

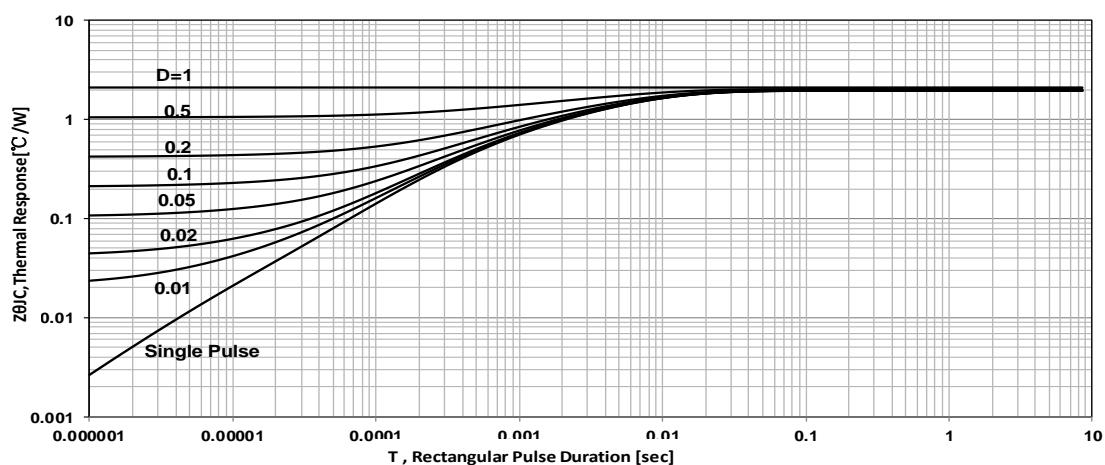
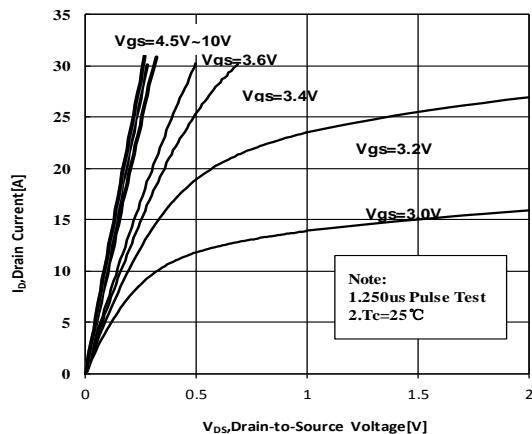
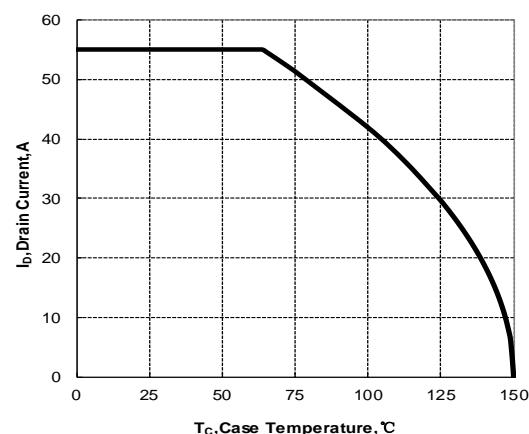
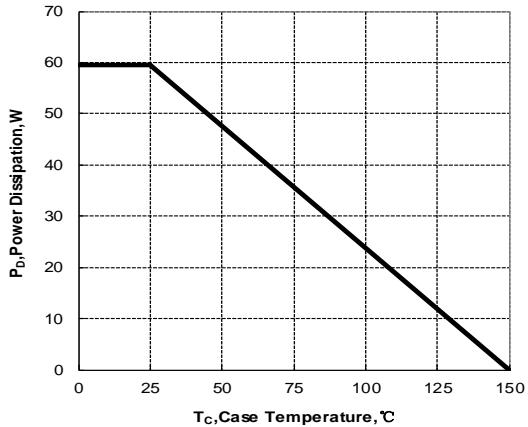
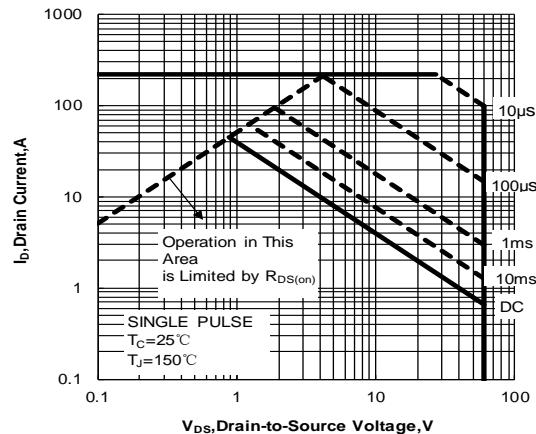
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
I <sub>S</sub>	Continuous Source Current (Body Diode)	T <sub>C</sub> = 25 °C	--	--	55	A
I <sub>SM</sub>	Maximum Pulsed Current (Body Diode)		--	--	220	A
V <sub>SD</sub>	Diode Forward Voltage	I <sub>S</sub> =19A, V <sub>GS</sub> =0V	--	--	1.2	V
trr	Reverse Recovery Time	di/dt=100A/us IF=14A	--	42	--	ns
Qrr	Reverse Recovery Charge		--	52.5	--	nC
Pulse width t <sub>p</sub> ≤300μs, δ ≤2%						

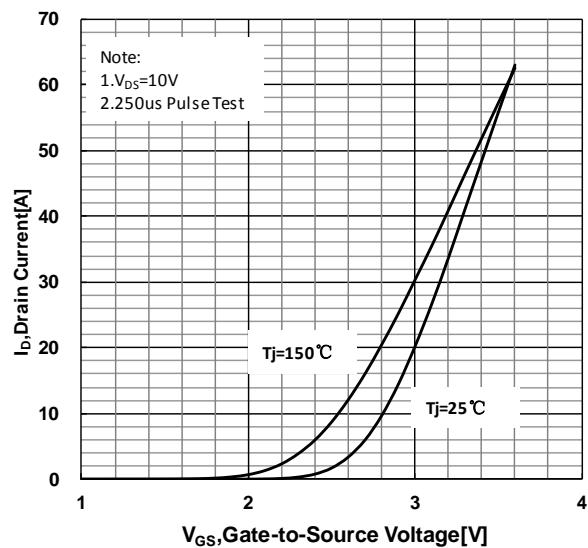
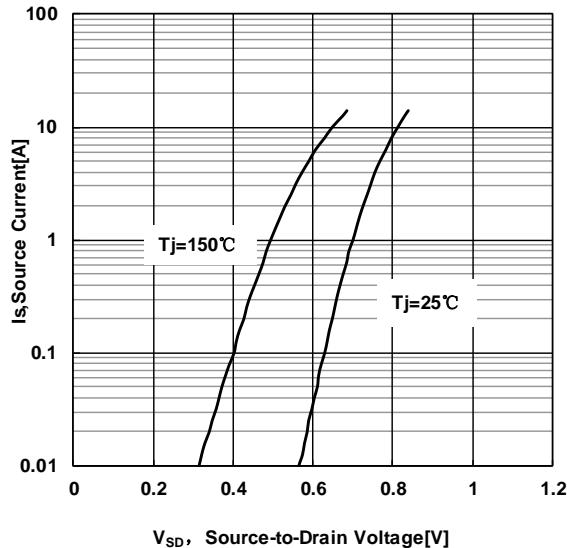
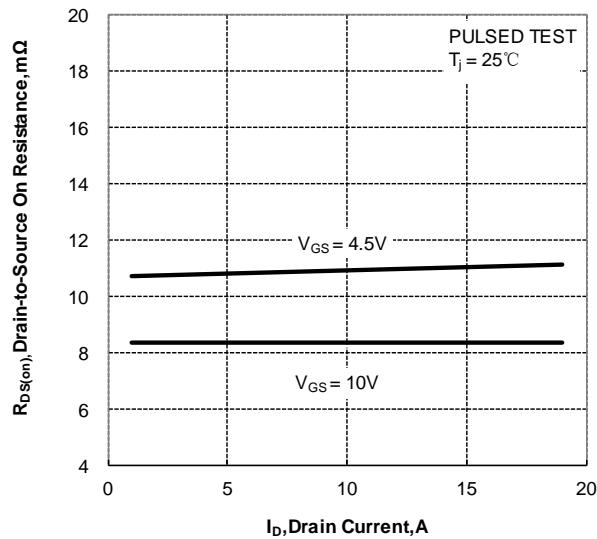
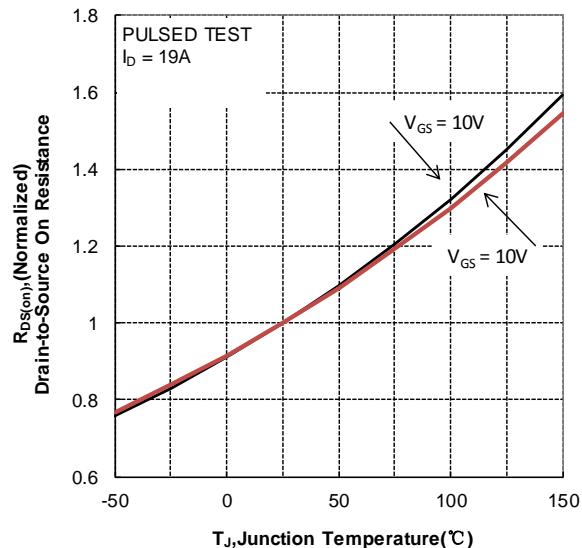
Symbol	Parameter	Max.	Units
R <sub>θ JC</sub>	Junction-to-Case	2.1	°C/W
R <sub>θ JA</sub>	Junction-to-Ambient	75	°C/W

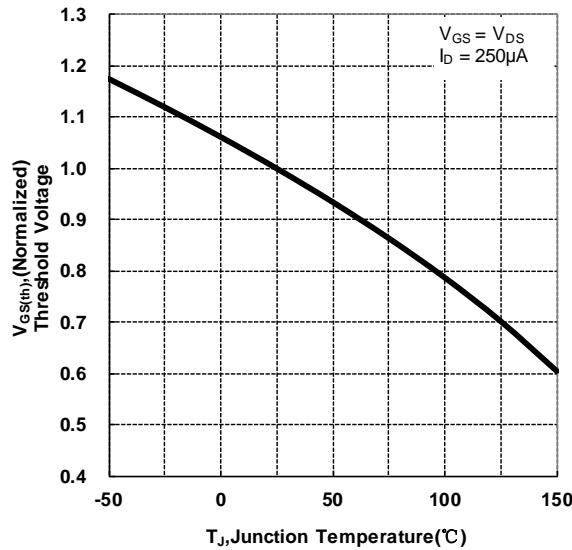
<sup>a1</sup>: Calculated continuous current based on maximum allowable junction temperature. Bond wire current limit is 100A by source bonding technology. Note that current limitations arising from heating of the device leads may occur with some lead mounting arrangements.

<sup>a2</sup>: L=0.5mH, I<sub>D</sub>=24.1A, Start T<sub>J</sub>=25°C

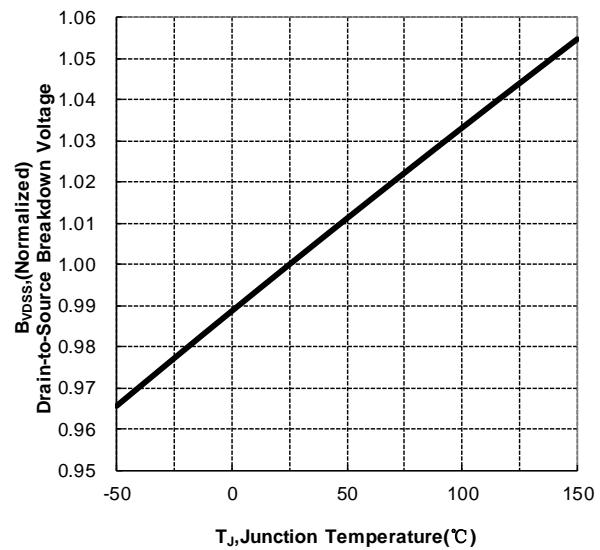
## Characteristics Curve:



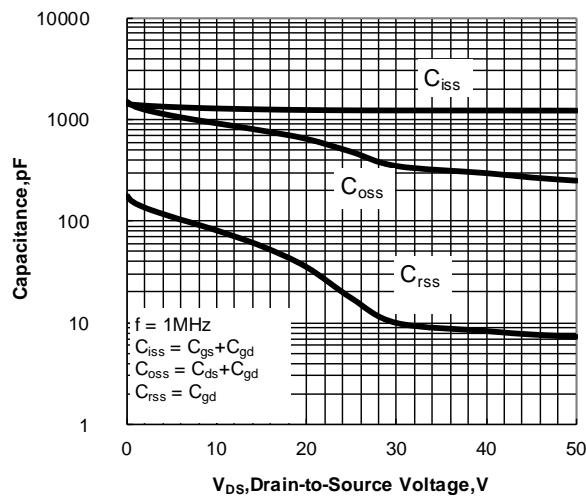

**Figure 6 Typical Transfer Characteristics**

**Figure 7 Typical Body Diode Transfer Characteristics**

**Figure 8. Drain-to-Source On Resistance vs Drain Current**

**Figure 9. Normalized On Resistance vs Junction Temperature**



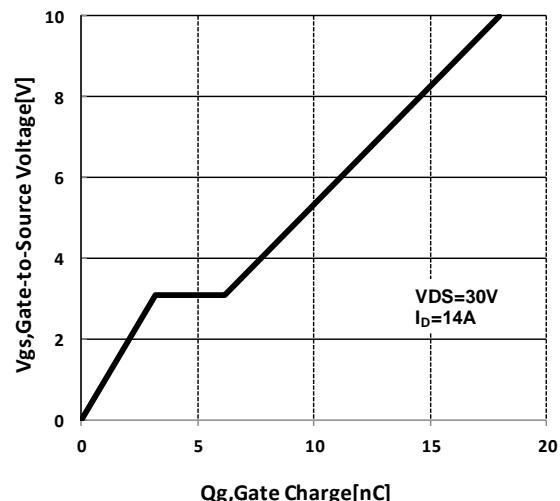
**Figure10. Normalized Threshold Voltage vs Junction Temperature**



**Figure 11. Normalized Breakdown Voltage vs Junction Temperature**

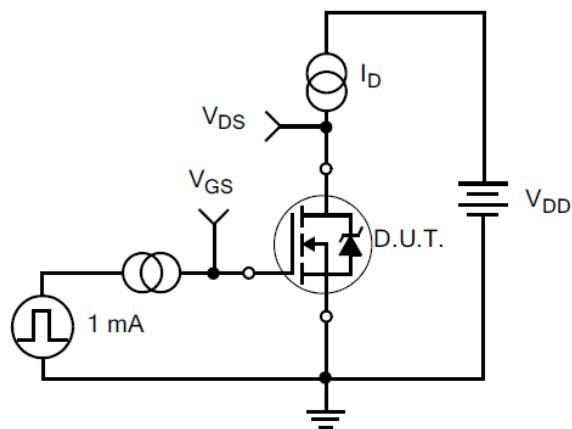


**Figure 12. Capacitance Characteristics**

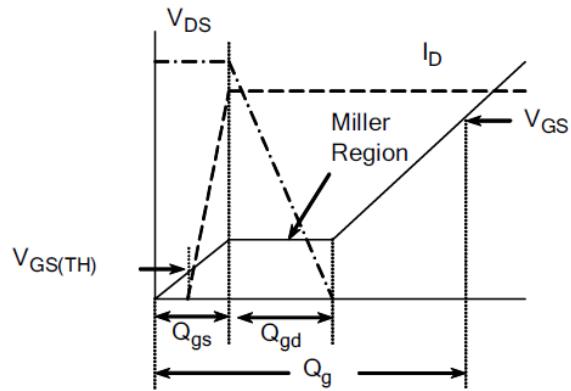


**Figure 13 Typical Gate Charge vs Gate to Source Voltage**

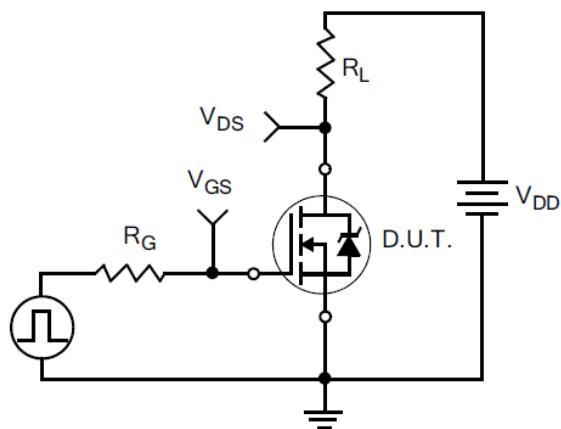
## Test Circuit and Waveform



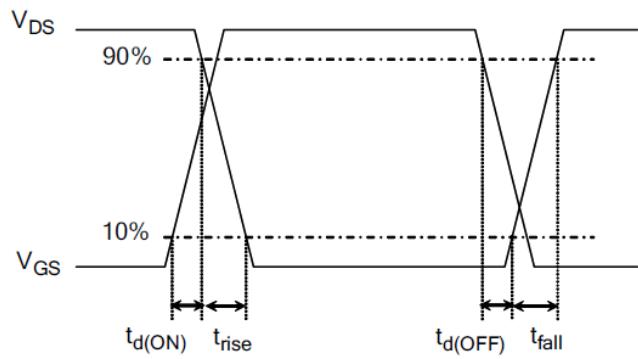
**Figure 14. Gate Charge Test Circuit**



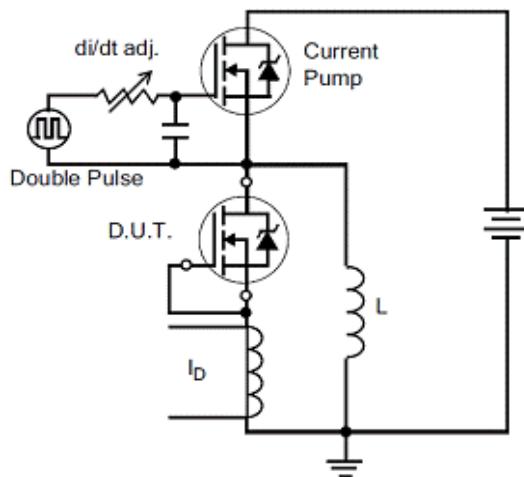
**Figure 15. Gate Charge Waveforms**



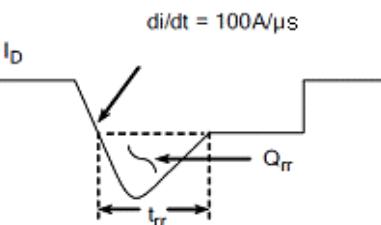
**Figure 16. Resistive Switching Test Circuit**



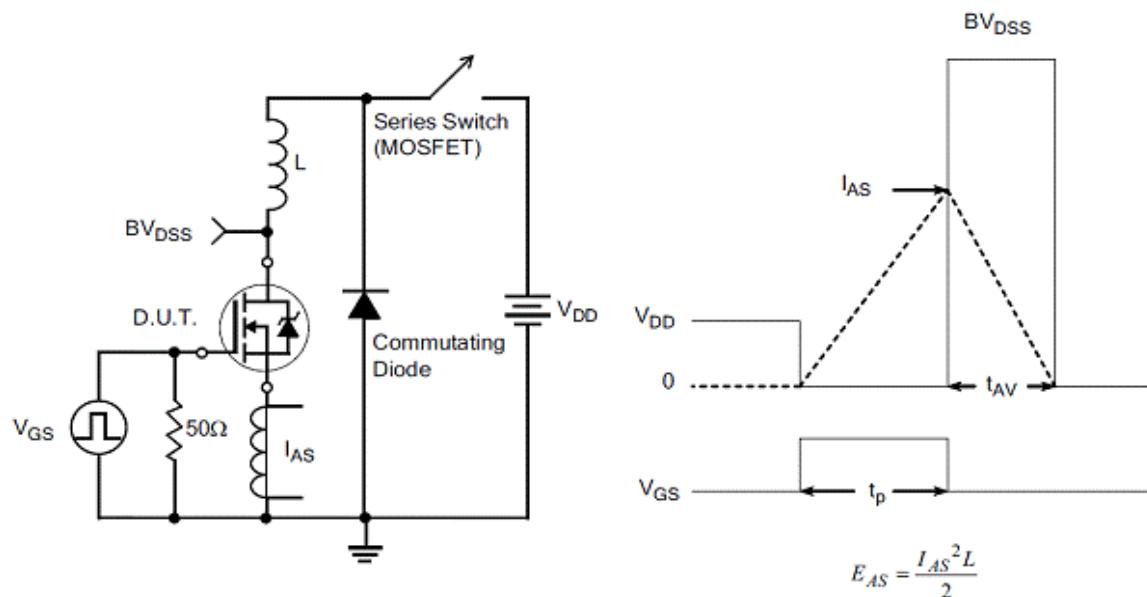
**Figure 17. Resistive Switching Waveforms**



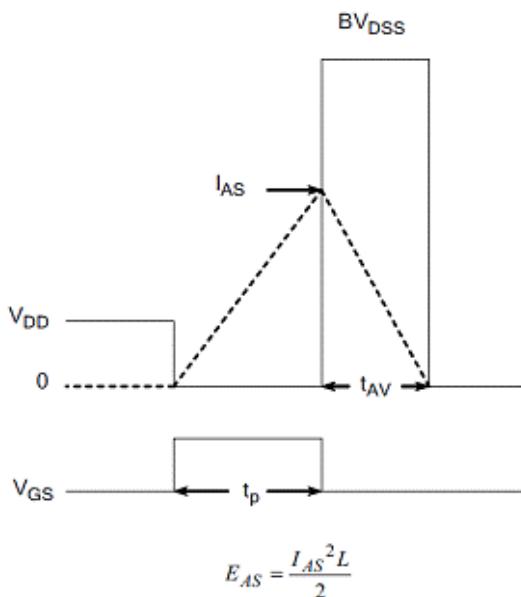
**Figure 18. Diode Reverse Recovery Test Circuit**



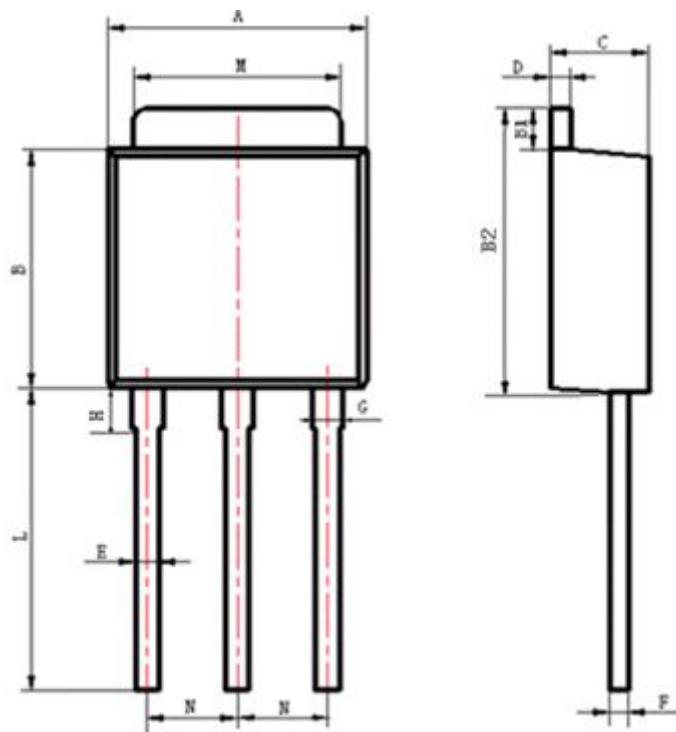
**Figure 19. Diode Reverse Recovery Waveform**



**Figure 20. Unclamped Inductive Switching Test Circuit**



**Figure 21. Unclamped Inductive Switching Waveform**

**Package Information:**

Items	Values(mm)	
	MIN	MAX
A	6.30	6.90
B	5.20	6.30
B1	0.70	1.30
B2	6.80	7.40
C	2.10	2.50
D	0.30	0.60
E	0.50	0.86
F	0.30	0.60
G	0.70	1.00
H	1.40	2.40
L*	9.00	9.80
M	5.10	5.50
N	2.09	2.49

\*adjustable

TO-251 Package

**The name and content of poisonous and harmful material in products**

	Hazardous Substance									
	Pb	Hg	Cd	Cr(VI)	PBB	PBDE	DIBP	DEHP	DBP	BBP
Limit	≤0.1%	≤0.1%	≤0.01%	≤0.1%	≤0.1%	≤0.1%	≤0.1%	≤0.1%	≤0.1%	≤0.1%
Lead Frame	○	○	○	○	○	○	○	○	○	○
Molding	○	○	○	○	○	○	○	○	○	○
Chip	○	○	○	○	○	○	○	○	○	○
Wire Bonding	○	○	○	○	○	○	○	○	○	○
Solder	×	○	○	○	○	○	○	○	○	○
Note	<p>○: Means the hazardous material is under the criterion of 2011/65/EU. ×: Means the hazardous material exceeds the criterion of 2011/65/EU.</p> <p>The plumbum element of solder exist in products presently, but within the allowed range of Eurogroup's RoHS.</p>									

**Warnings**

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. It is suggested to be used under 80 percent of the maximum ratings of the device.
2. When installing the heatsink, please pay attention to the torsional moment and the smoothness of the heatsink.
3. VDMOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. This publication is made by Huajing Microelectronics and subject to regular change without notice.

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