深圳市福瑞达显示技术有限公司 Shenzhen Frida LCD Co., Ltd

LCD MODULE SPECIFICATION

Custome	er.				
Model N	ame:	HJ070IA-0	<u>2F</u>		
Date:		2013/02/27	7		
Version		V02			
Final S	pecifi	Specification			
For Customer's Acce	ptance	·			
Approved by		Co	mment		
	<i>*</i>				
Approved by	Rev	eviewed by Prepared by			

深圳市福瑞达显示技术有限公司 Shenzhen Frida LCD Co., Ltd

Record of Revision

Version	Revise Date	Page	Content
V01	2012/12/20	all	Initial Release.
V02	2013/02/27	2	Update PIN Assignment
		4	Optical Specifications
		17	Mechanical Drawing

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1. GENERAL Specifications

No.	Item	Specification	Remark
1	LCD size	7.00"	
2	Driver element	a-Si TFT active matrix	
3	Resolution	1280 × 3(RGB) × 800	
4	Display mode	Normally Black, Transmissive	
5	Dot pitch	0.039 (W) 0.117 (H)	
6	Active area	149.76(H) × 93.6(V)	
7	Module size	161.0(H) ×107.0(V) ×2.5(T)	Note 1
8	Surface treatment	Hard coating	
9	Color arrangement	RGB-stripe	
10	Interface	LVDS	
11	Backlight power consumption	1.28W(Typ.)	
12	Panel power consumption	0.81W(Typ.)	
13	Weight	101.6g	

Note 1: Refer to Mechanical Drawing.

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2.Pin Assignment

FPC Connector is used for the module electronics interface. The model is F62240-H1210A manufactured by Vigorconn.

Pin No.	Symbol	I/O	Function	Remark
1	VCOM	Р	Common Voltage	
2	VDD	Р	Power Voltage for digital circuit	
3	VDD	Р	Power Voltage for digital circuit	
4	NC		No connection	
5	NC		No connection	
6	NC		No connection	
7	GND	Р	Ground	
8	RXIN0-	I	- LVDS differential data input	
9	RXIN0+	I	+ LVDS differential data input	
10	GND	Р	Ground	
11	RXIN1-	I	- LVDS differential data input	
12	RXIN1+	I	+ LVDS differential data input	
13	GND	Р	Ground	
14	RXIN2-	I	- LVDS differential data input	
15	RXIN2+		+ LVDS differential data input	
16	GND	Р	Ground	
17	RXCLKIN-	N I	- LVDS differential clock input	
18	RXCLKIN+	I	+ LVDS differential clock input	
19	GND	Р	Ground	
20	RXIN3-	I	- LVDS differential data input	
21	RXIN3+	I	+ LVDS differential data input	
22	GND	Р	Ground	
23	NC		No connection	
24	NC		No connection	
25	GND	Р	Ground	
26	NC		No connection	

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27	DIMO	0	Backlight CABC controller signal output	
28	NC		No connection	
29	AVDD	Р	Power for Analog Circuit	
30	GND	Р	Ground	
31	LED-	Р	LED Cathode	
32	LED-	Р	LED Cathode	
33	NC		No connection	
34	NC		No connection	
35	VGL	Р	Gate OFF Voltage	
36	NC		No connection	
37	CABC_EN	I	CABC Enable Input	Note1
38	VGH	Р	Gate ON Voltage	
39	LED+	Р	LED Anode	
40	LED+	Р	LED Anode	

I: input, O: output, P: Power

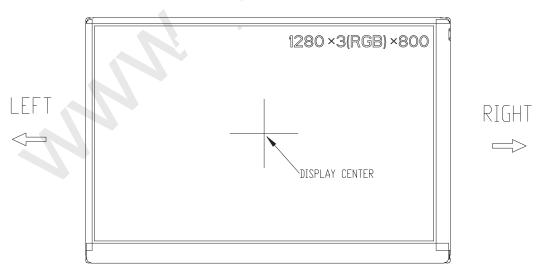
Note1: When CABC_EN="0" or "open", CABC OFF.

When CABC_EN="1", CABC Enable.

When CABC off, don't connect DIMO, else connect it to backlight.

Note: Definition of scanning direction. Refer to the figure as below:





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3. Operation Specifications

3.1. Absolute Maximum Ratings

(Note 1)

Item	Symbol	Val	ues	Unit	Remark
iteiii	Syllibol	Min.	Max.	Onit	Remark
	V_{DD}	-0.3	5.0	V	
	AV_{DD}	-0.5	15	V	
Power voltage	V_{GH}	-0.3	40	V	
	V_{GL}	-20.0	0.3	V	
	V _{GH} -V _{GL}	-0.3	40.0	V	
Operation Temperature	T _{OP}	-10	50	$^{\circ}\!\mathbb{C}$	
Storage Temperature	T _{ST}	-20	60	$^{\circ}\! \mathbb{C}$	
LED Reverse Voltage	VR	-	-	V	Each LED
LED Forward Current	lF	-	-	mA	Each LED

Note 1: The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

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3.1.1. Typical Operation Conditions

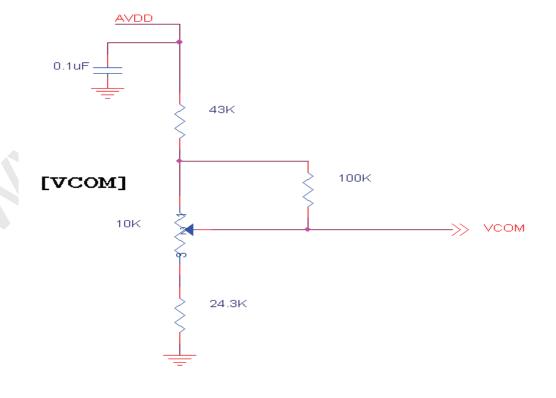
(Note 1)

Item	Symbol		Values	Unit	Remark	
item	Syllibol	Min. Typ. Max.		Max.	oill.	Remark
	V_{DD}	3.1	3.3	3.5	V	Note 2
Power voltage	AV_{DD}	9.54	9.74	9.94	V	
	V_{GH}	22.4	22.7	23	V	
	V_{GL}	-7.7	-8	-8.3	V	
Input signal voltage	V _{COM}	3.0	3.3	3.6	V	Note 4
Input logic high voltage	V _{IH}	$0.7V_{DD}$	-	V_{DD}	V	Note 3
Input logic low voltage	V _{IL}	0		0.3V _{DD}	V	INOLE 3

Note 1: Be sure to apply V_{DD} and V_{GL} to the LCD first, and then apply V_{GH} .

Note 2: V_{DD} setting should match the signals output voltage (refer to Note 3) of customer's system board.

Note 4: Typical V_{COM} is only a reference value, it must be optimized according to each LCM. Be sure to use VR.



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3.1.2. Current Consumption

	Symbol		Values		Unit	Remark	
Item	Symbol	Min.	Тур.	Max.	Oilit	Keillaik	
	I _{GH}	-	0.5	1	mA	V _{GH} =22.7V	
Current for Driver	I _{GL}	-	0.5	1	mA	$V_{GL} = -8V$	
Current for Driver	IV _{DD}	-	135	150	mA	V _{DD} =3.3V	
	IAV _{DD}	-	35	60	mA	AV _{DD} =9.74V	

3.1.3. Backlight Driving Conditions

Item	Symbol		Values	Unit	Remark		
item	Symbol	Min.	Тур.	Max.	Offic	Remark	
Voltage for LED backlight	V _L	-	12.8	13.2	V	Note 1	
Current for LED backlight	I <u>L</u>	96	100	104	mA		
LED life time	_	15000	-	-	Hr	Note 2	

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25 $^{\circ}\mathbb{C}$ and I_{L} =100mA.

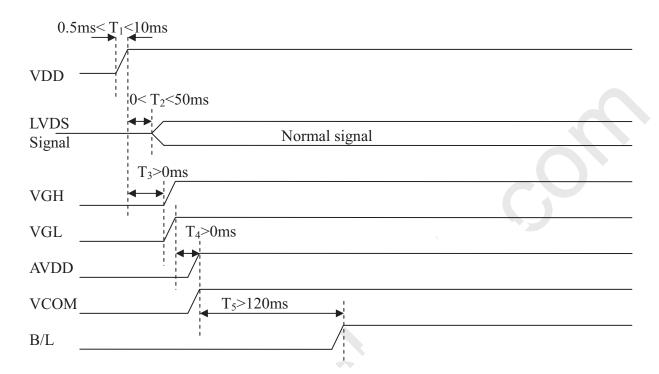
Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25 $^{\circ}$ C and I_L =100mA. The LED lifetime could be decreased if operating I_L is lager than 100mA.

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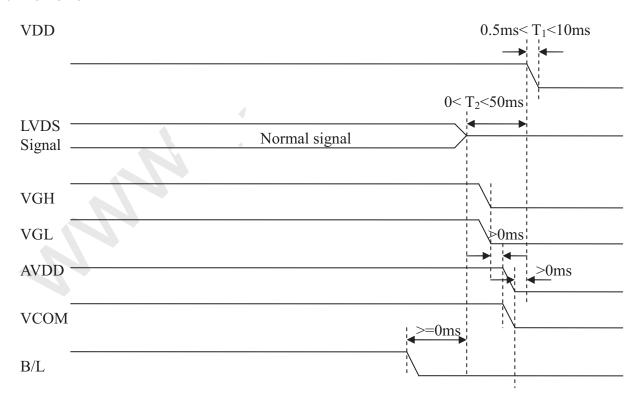
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3.2. Power Sequence

a. Power on:



b. Power off:



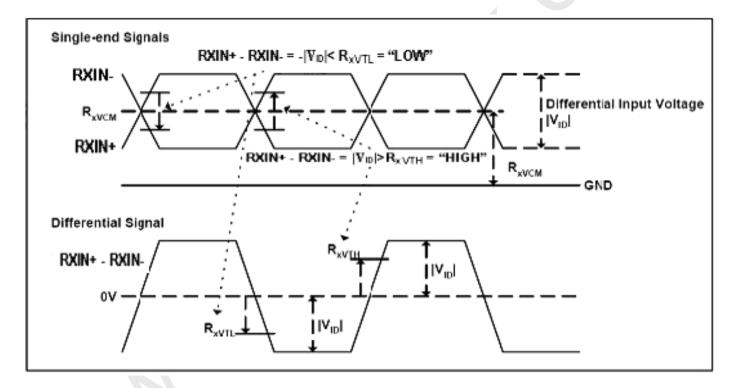
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3.3. Timing Characteristics

3.3.1. DC Electrical Characteristics

Parameter	Symbol		Values	Unit	Remark	
		Min.	Min. Typ. Max.			
Differential input high Threshold voltage	R _{xVTH}	-	-	+0.1	V	R _{XVCM} =1.2V
Differential input low Threshold voltage	R _{xVTH}	-0.1	-	-	V	11XXVCM—1.2V
Differential input common mode voltage	R _{xVCM}	1.125	ı	1.375	V	
Differential voltage	V _{ID}	0.2	-	0.6	V	



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3.3.2. Timing

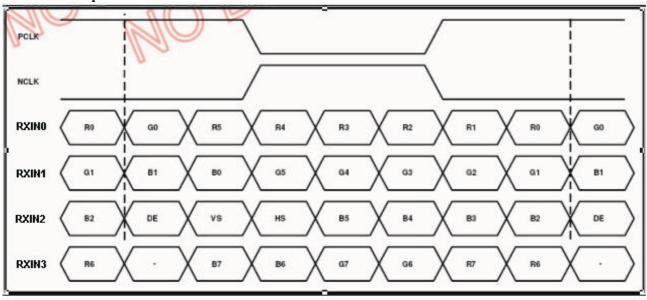
Item	Symbol		Values	Unit	Remark	
item	Symbol	Min.	Min. Typ.		Offic	Remark
Clock Frequency	fclk	(67.55)	(71.11)	(73)	MHz	Frame rate =60Hz
Horizontal display area	thd		1280			
HS period time	th	(1410)	(1440)	(1470)	DCLK	
HS Blanking	thb	130	160	190	DCLK	
Vertical display area	tvd		800			
VS period time	tv	(813)	(823)	(833)	Н	
VS Blanking	thb	13	23	33	Н	

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3.3.3. Data Input Format

8bit LVDS input



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4. Optical Specifications

Item	Symbol	Condition	Values			Unit	Remark
item		Condition	Min.	Тур.	Max.	Onit	Remark
Viewing angle (CR≥ 10)	θ_{L}	Ф=180°(9 o'clock)	80	85	-		
	θ_{R}	Φ=0°(3 o'clock)	80	85	-	dograe	Note 1
	θ_{T}	Φ=90°(12 o'clock)	80	85	-	degree	Note 1
	θ_{B}	Φ=270°(6 o'clock)	80	85	-		
Response time	T _{ON}		-	15	20	msec	Note 3
	T_{OFF}		-	20	30	msec	Note 3
Contrast ratio	CR		600	800	-	-	Note 4
Color chromaticity	W _X	Normal θ=Φ=0°	0.26	0.31	0.36	-	Note 2
	W_{Y}		0.28	0.33	0.38	-	Note 5 Note 6
Luminance	L		270	350	-	cd/m²	Note 6
Luminance uniformity	Yu	^	70	75	-	%	Note 7

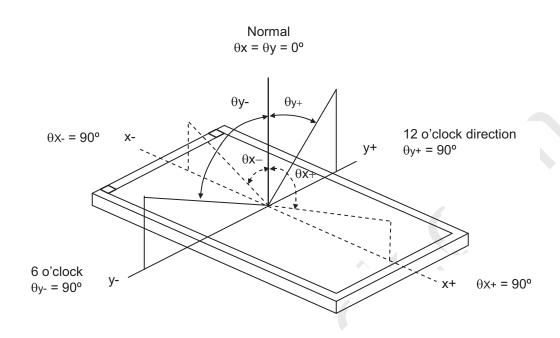
Test Conditions:

- 1. VDD=3.3V, IL=100mA (Backlight current), the ambient temperature is 25°C.
- 2. The test systems refer to Note 2.

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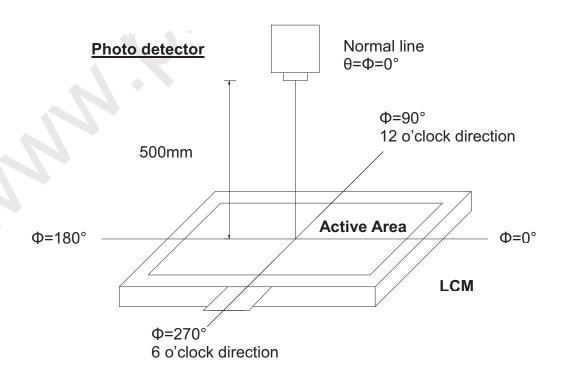
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Note 1: Definition of viewing angle range



Note 2: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1° /Height: 500mm.)



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Fig. 4-2 Optical measurement system setup

Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.

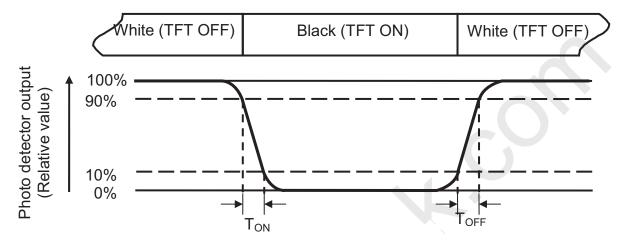


Fig. 4-3 Definition of response time

Note 4: Definition of contrast ratio

Contrast ratio (CR) = Luminance measured when LCD on the "White" state

Luminance measured when LCD on the "Black" state

Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel. The LED driving condition is I_L=160mA.

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Note 7: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to Fig. 4-4). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (Yu) =
$$\frac{B_{min}}{B_{max}}$$

L-----Active area length

W----- Active area width

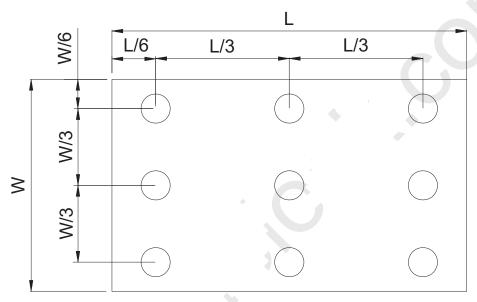


Fig. 4-4 Definition of measuring points

 \mathbf{B}_{max} : The measured maximum luminance of all measurement position.

B_{min}: The measured minimum luminance of all measurement position.

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5. Reliability Test Items

(Note3)

Item	Test	Remark	
High Temperature Storage	Ta = 60°C	120hrs	Note 1, Note 4
Low Temperature Storage	Ta = -20°C	120hrs	Note 1, Note 4
High Temperature Operation	Ts = 50°C	120hrs	Note 2, Note 4
Low Temperature Operation	Ta = -10°C	120hrs	Note 1, Note 4
Operate at High Temperature and Humidity	+40℃, 90%RH	120hrs	Note 4
Thermal Shock	-10°C/30 min ~ +50 /30 min for a total 100 cycles, Start with cold temperature and end with high temperature.		Note 4
Vibration Test	Frequency range:10 Stroke:1.5mm Sweep:10Hz~55Hz 2 hours for each dir (6 hours for total)		
Mechanical Shock	100G 6ms,±X, ±Y, :	ŁZ 3 times for each	
Package Vibration Test	Random Vibration : ISTA-3A 1Hz~200Hz,Grms=0.53 Half hours for direction of Z.		
Package Drop Test	Height:60 cm 1 corner, 3 edges, 6		
Electro Static Discharge	± 2KV, Human Bo		

- Note 1: Ta is the ambient temperature of samples.
- Note 2: Ts is the temperature of panel's surface.
- Note 3: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.
- Note 4: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

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6. General Precautions

6.1. Safety

Liquid crystal is poisonous. Do not put it in your mouth. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

6.2. Handling

- 1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
- 2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
- 3. To avoid contamination on the display surface, do not touch the module surface with bare hands.
 - 4. Keep a space so that the LCD panels do not touch other components.
- 5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
- 6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
 - 7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

6.3. Static Electricity

- 1. Be sure to ground module before turning on power or operating module.
- 2. Do not apply voltage which exceeds the absolute maximum rating value.

6.4. Storage

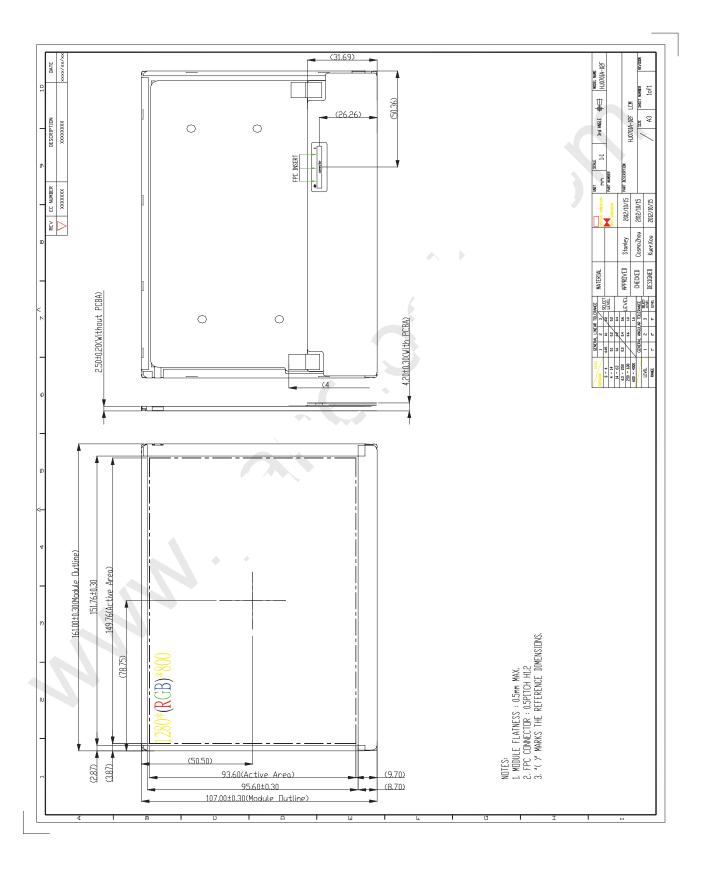
- 1. Store the module in a dark room where must keep at 25±10°C and 65%RH or less.
- 2. Do not store the module in surroundings containing organic solvent or corrosive
 - 3. Store the module in an anti-electrostatic container or bag.

6.5. Cleaning

- 1. Do not wipe the polarizer with dry cloth. It might cause scratch.
- 2. Only use a soft sloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.

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7. Mechanical Drawing



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8. Package Drawing

8.1. Packaging Material Table

No.	Item	Model (Material)	Dimensions(mm)	Unit Weight (kg)	Quantity	Remark	
1	LCM Module	HJ070IA-02F	161.0×107.0×2.5	TBD	TBD		
2	Partition	BC Corrugated paper	TBD	TBD	TBD		
3	Corrugated Paper	B Corrugated paper	TBD	TBD	TBD		
4	Corrugated Bar	B Corrugated paper	TBD	TBD	TBD		
5	Dust-Proof Bag	PE	TBD	TBD	TBD		
6	A/S Bag	PE	TBD	TBD	TBD		
7	Carton	Corrugated paper	TBD	TBD	TBD		
8	Total weight	TBD					

8.2. Packaging Quantity

Total LCM quantity in Carton: 4PCS/Tray x 20PCS Tray/carton=80PCS

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8.3. Packaging Drawing

