

**SPECIFICATION  
OF  
LCD MODULE**

**MODULE NO.: HL024T01-01**

**Customer Approval:**

☐ **Accept**

☐ **Reject**

	<b>SIGNATURE</b>	<b>DATE</b>
<b>PREPARED BY</b>		
<b>CHECKED BY</b>		
<b>APPROVED BY</b>		



**DOCUMENT REVISION HISTORY**

<b>Sample Version</b>	<b>Doc. Version</b>	<b>DATE</b>	<b>DESCRIPTION</b>	<b>CHECKED BY</b>
0001	A0	2023-08-29	First Release.	



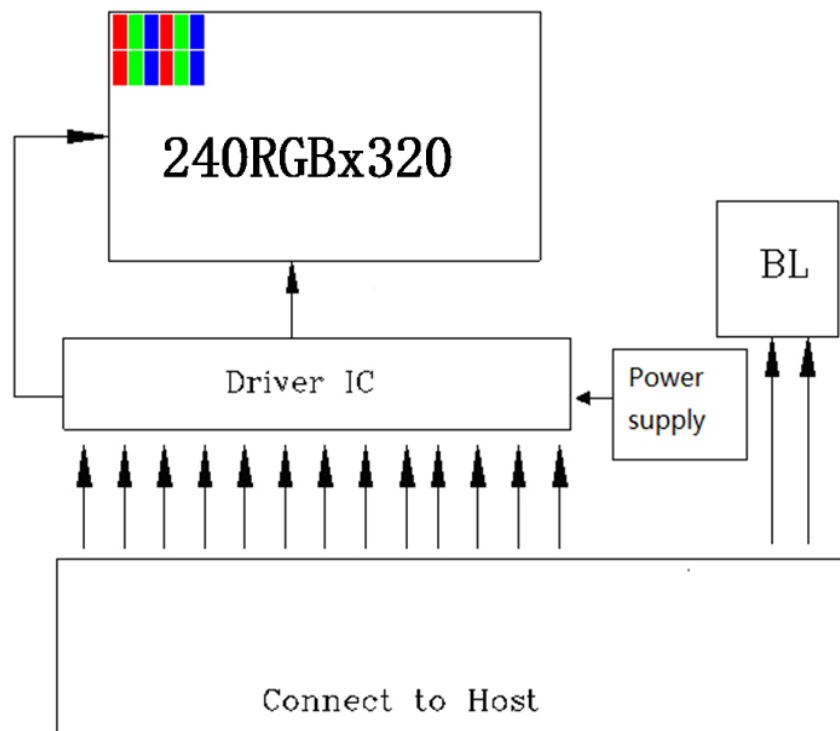
## 1. MECHANICAL SPECIFICATIONS:

ITEM	SPECIFICATION	UNIT
OUTLINE DIMENSIONS	42.72 (W) X60.26(H) X2.5(D)	mm
ACTIVEAREA	36.72 (W) X48.96(H)	mm
DISPLAY SIZE	2.4	inch
DOT PITCH	0.153mmX0.153mm	mm
NUMBER OF DOTS	240* (RGB) *320	-
DRIVER IC	ST7789V or EQU	-
LCD TYPE	TFT(65K/262K) TRANSFLECTIVE	-
BACKLIGHT TYPE	LED White	-
VIEWING DIRECTION	6:00	-

**\*See attached drawing for details.**

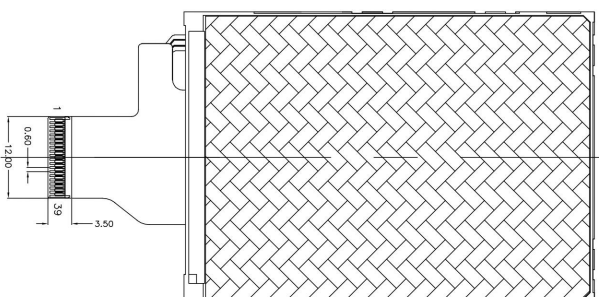
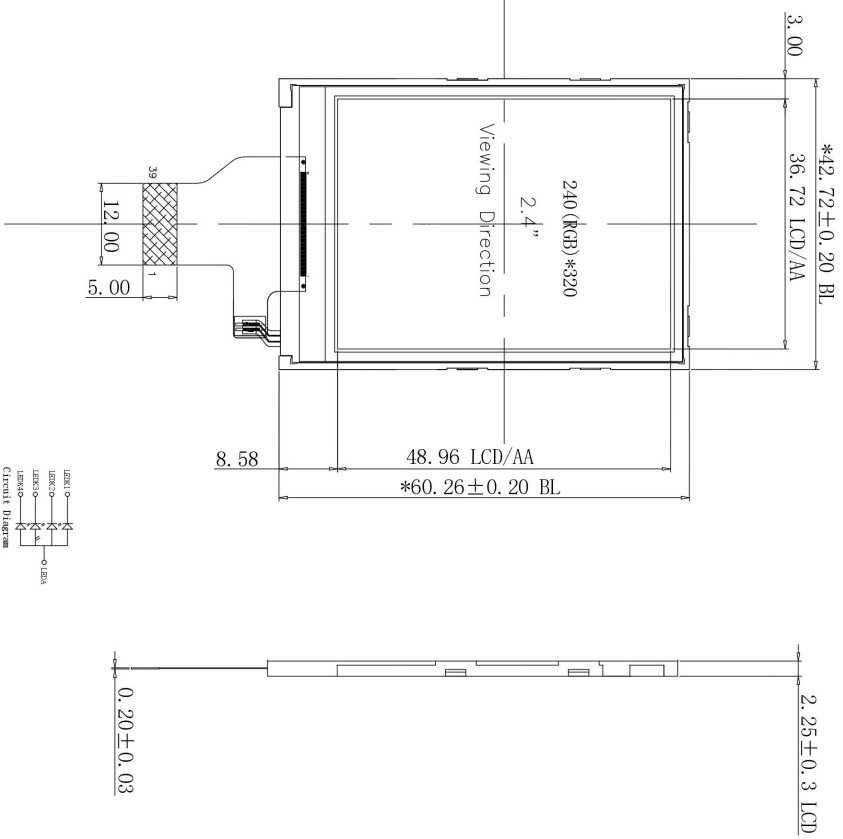


## 2.BLOCK DIAGRAM:





### 3.DIMENSIONAL



1	YDD
2	YDDO
3	RSET
4	CS
5	5
6	WRRSP (ASO)
7	RD
8	GND
9	SYNDC
10	HSYNC
11	HYNC
12	DB1
13	DB2
14	DB3
15	DB4
16	DB5
17	DB6
18	DB7
19	DB8
20	DB9
21	DB10
22	DB11
23	DB12
24	DB13
25	DB14
26	DB15
27	EN_RCB
28	FC_RCB
29	LIDA
30	LHDI
31	LHDI3
32	LHDI4
33	GND
34	SDA
35	SDA
36	SDD
37	IN0
38	IN1
39	IN2

Display Type	2.4" TFT-LCD/Transmissive, normal white
Display Resolution	240GB(H)320(V)
Interface	MCU/RGB
Logic Voltage	3.3V
Operation Temperature	-20°C TO 70°C
Storage Temperature	-30°C TO 80°C
Remark	—

1.0	First version	
MARK REV.	CONTENTS MODIFIED	DATE

 海罗光电有限公司 Hello Lighting co.,LTD	
HL024T01-01	
TITLE: LCM	DIMENSION
DESIGN	REV. : 2.0
CHECKED	PROJECTION:
APPROVED	SCALE : -

**HL** 海罗光电有限公司  
Hello Lighting co.,LTD



## **4. PIN DESCRIPTION:**

Pin define for TFT

NO.	PIN NAME	I/O	Description
1	VDD	P	Power supply for core
2	VDDIO	P	Power supply for IO
3	RESET	I	Reset signal
4	CS	I	Chip select input pin ("Low" enable). Fix this pin at IOVCC or GND when not in use
5	RS	I	This pin is used to select "Data or Command" in the parallel interface. When D/CX = '1', data is selected. When D/CX = '0', command is selected. This pin is used serial interface clock in 3-wire 9-bit / 4-wire 8-bit serial data interface. Fix this pin at IOVCC or GND when not in use.
6	WR	I	write signal Fix this pin at IOVCC or GND when not in use
7	RD	I	read signal Fix this pin at IOVCC or GND when not in use
8	GND	P	Power ground
9	VS	I	Vertical synchronizing signal for RGB interface operation. Fix this pin at IOVCC or GND when not in use.
10	HS	I	Horizontal synchronizing signal for RGB interface operation. Fix this pin at IOVCC or GND when not in use.
11	DB0	IO	Data bus
12	DB1	IO	
13	DB2	IO	
14	DB3	IO	
15	DB4	IO	
16	DB5	IO	
17	DB6	IO	
18	DB7	IO	
19	DB8	IO	
20	DB9	IO	
21	DB10	IO	
22	DB11	IO	
23	DB12	IO	
24	DB13	IO	
25	DB14	IO	



<b>26</b>	DB15	IO	
<b>27</b>	ENB	I	Data enable signal for RGB interface operation. Fix this pin at IOVCC or GND when not in use.
<b>28</b>	PCLK	I	Dot clock signal for RGB interface operation. Fix this pin at IOVCC or GND when not in use.
<b>29</b>	LEDA	P	Back light ANODE
<b>30</b>	LEDK1	P	Back light cathode
<b>31</b>	LEDK2	P	Back light cathode
<b>32</b>	LEDK3	P	Back light cathode
<b>33</b>	LEDK4	P	Back light cathode
<b>34</b>	GND	P	Power ground
<b>35</b>	SDA	O	SPI interface output pin. If not used, let this pin open.
<b>36</b>	SDO	O	Serial data output
<b>37</b>	IM0	I	IM3=0 IM[2:0]=0001 default
<b>38</b>	IM1	I	
<b>39</b>	IM2	I	

note1:

IM2	IM1	IM0	Interface	Read Back Data Bus Selection
0	0	0	80-8bit parallel I/F	DB[7:0]
0	0	1	80-16bit parallel I/F	DB[15:0]
0	1	0	80-9bit parallel I/F	DB[8:0]
0	1	1	80-18bit parallel I/F	DB[17:0]
1	0	1	3-line 9bit serial I/F	SDA: in/out
			2 data lane serial I/F	SDA: in/out, WRX: in
1	1	0	4-line 8bit serial I/F	SDA: in/out

note2:

I : host to LCD

O: LCD to host

IO: host to LCD or LCD to host

P: power



## 5. MAXIMUM ABSOLUTE LIMIT:

Item	Symbol	Value	Unit
Power supply voltage for core	VCI	-0.3~3.6	V
Power supply voltage for IO	IOVCC	-0.3~3.6	V
Power supply voltage for CTP	VDDCTP	-0.3~3.6	V
Input voltage	Vin	$V_{DD}+0.3$	V
Operating temperature	Topr	-20 to 70	°C
Storage temperature	Tstg	-30 to 80	°C

**Note:** Note1: Absolute maximum rating is the limit value beyond which the IC maybe broken.

They do not assure operations.

Note2: Background color changes slightly depending on ambient temperature. This Phenomenon is reversible.

$T_a \leq 70^{\circ}\text{C}$ : 75%RH max

$T_a > 70^{\circ}\text{C}$ : absolute humidity must be lower than the humidity of 75%RH at  $70^{\circ}\text{C}$

Note3:  $T_a$  at  $-30^{\circ}\text{C}$  will be <48hrs, at  $80^{\circ}\text{C}$  will be <120hrs

## 6.ELECTRICAL CHARACTERISTICS

### 6.1 DC Characteristics ( $T_a=25^{\circ}\text{C}$ )

Item	Symbol	Min	Type	Max	Unit	Test condition
Power supply for core	VCI	2.7	3.3	3.5	V	-
Power supply for IO	IOVCC	1.65	1.8	3.4	V	
Power supply for CTP	VDDctp	2.7	3.3	3.5	V	-
Supply current	$I_{DD}$	-	TBD	-	mA	$V_{DD}=3.3\text{V}, T_a=25^{\circ}\text{C}$
Input voltage	$V_{IH}$	0.7VDD	-	VDD	V	-
	$V_{IL}$	0	-	0.3VDD	V	
Input leakage current	$I_{IL}$	-1.0	-	1.0	$\mu\text{A}$	$V_{IN}=V_{DD}$ or $V_{SS}$

Note: Voltage greater than above may damage the module.

All voltages are specified relative to  $V_{SS}=0\text{V}$ .



## 6.2 Backlight Electrical-optical Characteristics

### 1. Stander Lamp Styles(Edge Lighting Type):

The LED chips are distributed over the edge light area of the illumination unit, which gives the less power consumption:

### 2. The Main Advantages of the LED Backlight are as following:

2.1 The brightness of the backlight can simply be adjusted by a resistor or a potentiometer.

### 3. Data About LED Backlight:

Item	Symbol	MIN	TYP	MAX	UNIT	Test Condition	Note
Supply Voltage	Vf	3.0	3.2	3.4	V	If=80 mA	-
Supply Current	If	-	80	-	mA	-	-
Reverse Voltage	Vr	-	-	5	V	10uA	
Power dissipation	Pd	-	256	-	mW	-	
Uniformity for LCM	-	80	-	-	%	If=80mA	3
Life Time	-	30000	-	-	Hr	If=80 mA	-
Backlight Color	White						

### NOTE:

1. Average Luminous Intensity of P1-P9

2. Uniformity = Min/Max \* 100%

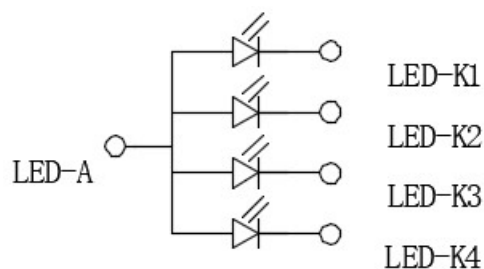
3.LED life time defined as follows: The final brightness is at 70% of original brightness

Measured Method: (X\*Y: Light Area)(Left Draft as follow)

Internal Circuit Diagram(Right Draft as follow)

(Effective spatial Distribution)

Hole Diameter ø 3mm ; 1 to 9 per Position Measured Luminous:





## **7.AC TIMING**

**Please refer to IC spec.**



## 8. OPTICAL CHARACTERISTICS:

Driving the backlight

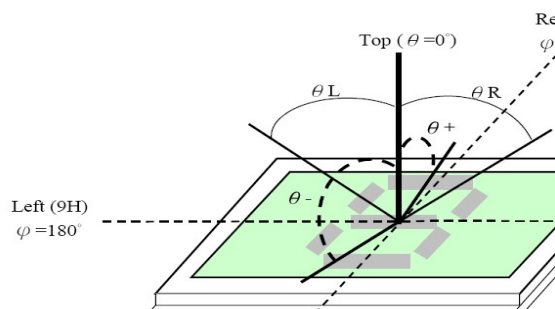
No.	ITEM		Symbol	Conditions	Specification			Unit	Note
					Min	Typ	Max		
1	Response Time		Tr+Tf	25℃	-	25	30	Ms	(1)(2)
2	Contrast Rate		Cr	θ=0, Normal viewing angle		150	-	-	(1)(3)
3	Viewing Angle	Hor.	θL	CR>10	-	50	-	Deg	-
			θR		-	40	-		
		Ver.	Θ+		-	40	-		
			Θ-		-	40	-		
4	Chromaticity	White	x	Brightness is ON	0.278	0.298	0.318		
			y		0.311	0.331	0.51		
		Red	x		0.587	0.607	0.627		
			y		0.310	0.330	0.350		
		Green	x		0.258	0.278	0.298		
			y		0.526	0.546	0.566		
		Blue	x		0.121	0.141	0.161		
			y		0.138	0.158	0.178		
5	NTSC		S			50		%	
6	luminance		L			400		cd/m2	
7	Uniformity		U		80			%	

Measure Conditions:

1. Measure surrounding : dark room;
2. Ambient temperature:  $25\pm 2^\circ\text{C}$ ;
3. 30min.warm-up time.

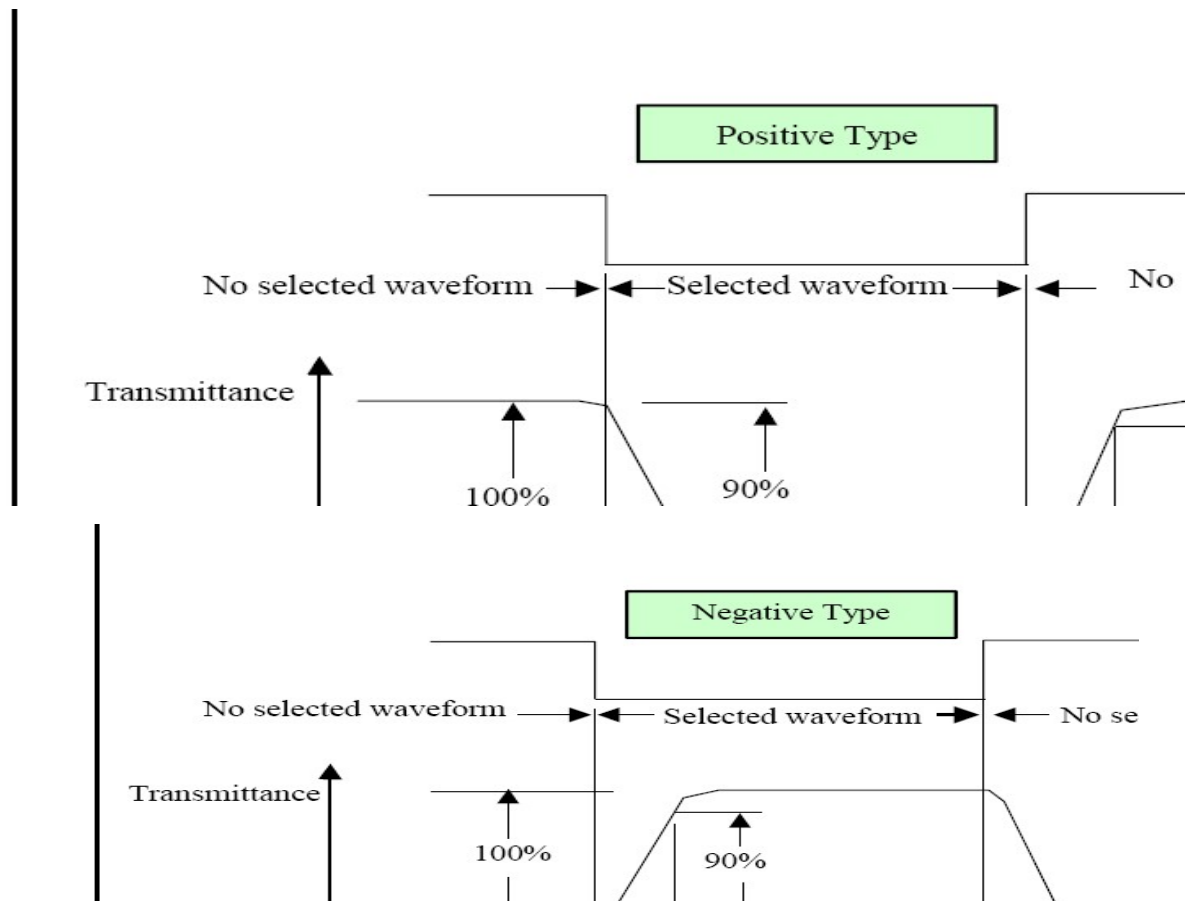
### Note Definition:

Note(1)Viewing angle range:

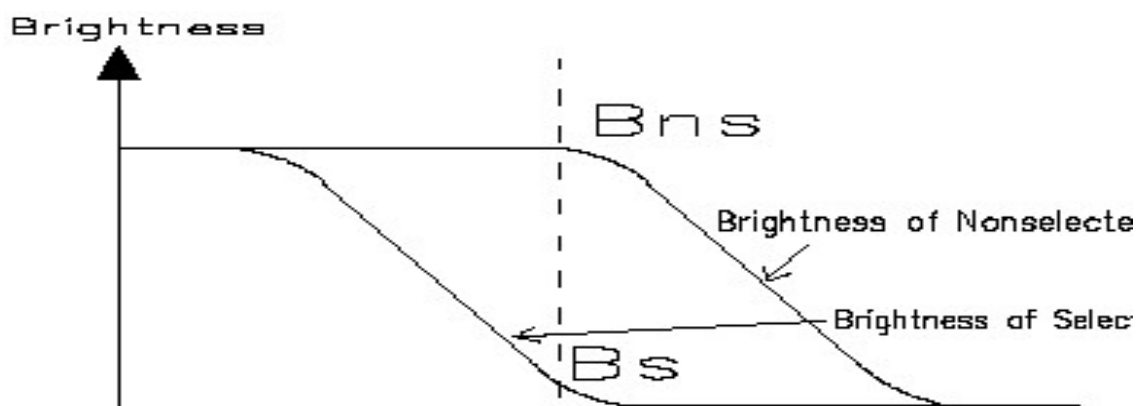




Note(2) Response Time:



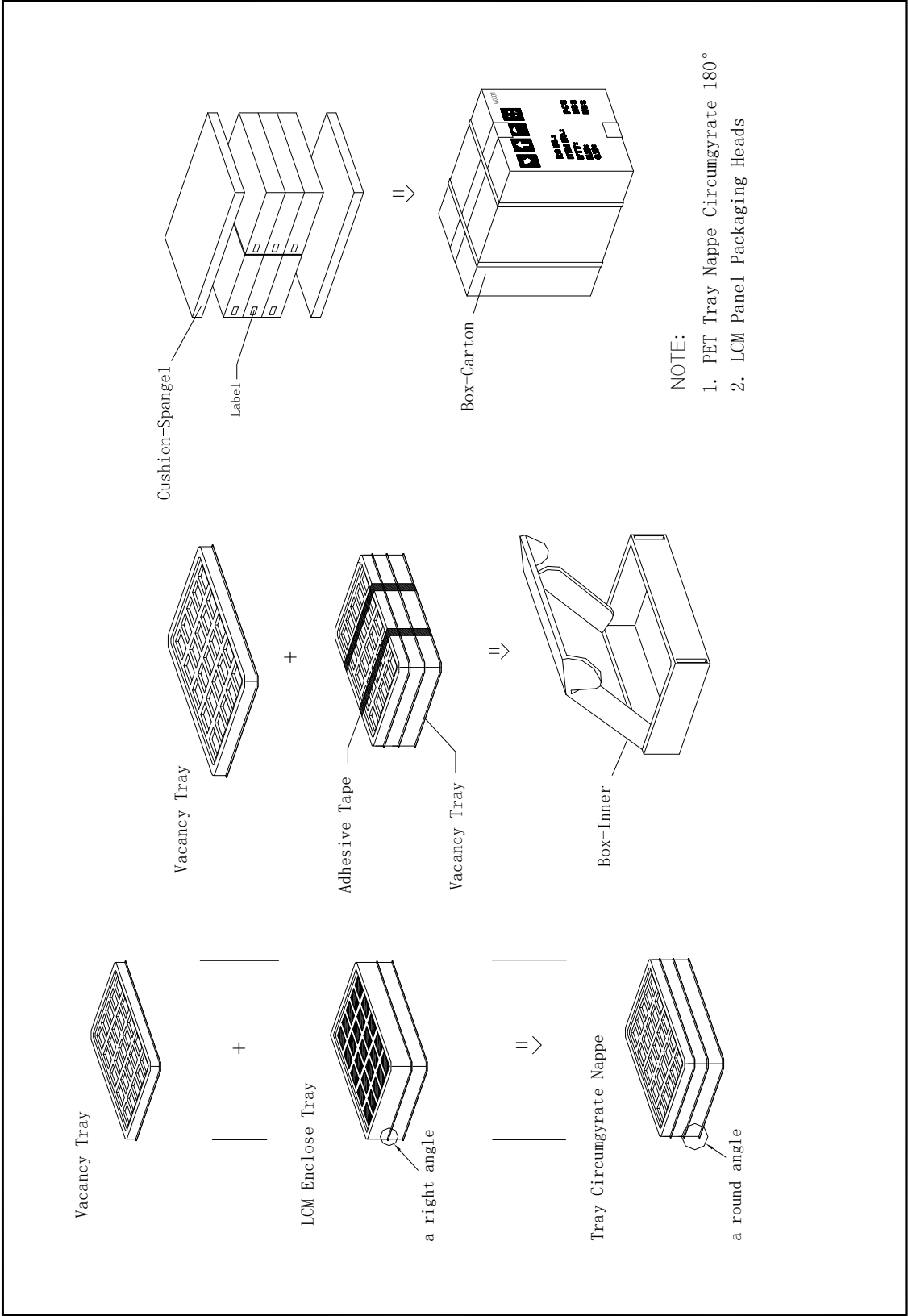
Note(3) Contrast Ratio Definition:



$$\text{Contrast Ratio (Cr)} = \frac{\text{Luminance with all pixel white}}{\text{Luminance with all pixel black}}$$



9.PACKAGE.





## **10. STANDARD SPECIFICATION FOR RELIABILITY:**

Item	Condition		Time (hrs)	Assessment
High temp. Storage	80°C		120	No abnormalities in functions and appearance
High temp. Operating	70°C		120	
Low temp. Storage	-30°C		120	
Low temp. Operating	-20°C		120	
Humidity	40°C/ 90%RH		120	
Thermal Shock Temp. Cycle	-20°C ← →70°C (0.5hour ← → 0.5 hour)		10cycles	
ESD Testing	HBM:	±8KV		330Ω/150PF
	MM:	±200V		200PF/0Ω

Functions, performance, appearance, etc. shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature ( $25\pm10^{\circ}\text{C}$ ), normal humidity ( $45\pm20\%$  RH), and in area not exposed to direct sun light. (Life time of backlight, please refer to Data about backlight.)

### **Testing Conditions and Inspection Criteria:**

For the final test the testing sample must be stored at room temperature for 24 hours, after the tests listed in up Table, Standard specifications for Reliability have been executed in order to ensure stability.

Item	Test Model	In section Criteria
Current Consumption	Refer To Specification	The current consumption should conform to the product specification.
Contrast	Refer To Specification	After the tests have been executed, the contrast must be larger than half of its initial value prior to the tests.
Appearance	Visual inspection	Defect free.



## **11.SPECIFICATION OF QUALITY ASSURANCE:**

### **11.1 Purpose**

This standard for Quality Assurance should affirm the quality of LCD Module products to supply.

### **11.2 Standard for Quality Test**

#### **a. Inspection:**

Before delivering, the supplier should take the following tests, and affirm the quality of product.

#### **b. Electro-Optical Characteristics:**

According to the individual specification to test the product.

#### **c. Test of Appearance Characteristics:**

According to the individual specification to test the product.

#### **d. Test of Reliability Characteristics:**

According to the definition of reliability on the specification for testing products.

#### **e. Delivery Test:**

Before delivering, the supplier should take the delivery test.

(i) Test method: According to MIL-STD105E.General Inspection Level II take a single time.

(ii) The defects classify of AQL as following:

Major defect: AQL = 0.65

Minor defect: AQL = 2.5

Total defects: AQL = 2.5

### **11.3. Nonconforming Analysis & Deal With Manners**

#### **a. Nonconforming Analysis:**

(i) Purchaser should supply the detail data of non- conforming sample and the non- conforming.

(ii) After accepting the detail data from purchaser, the analysis of nonconforming should be finished in two weeks.

(iii) If supplier can not finish analysis on time, must announce purchaser before two weeks.

#### **b. Disposition of nonconforming:**

(i) If find any product defect of supplier during assembly time, supplier must change the good product for every defect after recognition.

(ii) Both supplier and customer should analyze the reason and discuss the disposition of nonconforming when the reason of nonconforming is not sure.

### **11.4. Agreement items**

Both sides should discuss together when the following problems happen.

a. There is any problem of standard of quality assurance, and both sides think that it must be modified.

b. There is any argument item which does not record in the standard of quality assurance.

c. Any other special problem.

### **11.5 Standard of The Product Appearance Test**

a. Manner of appearance test: This specification should be applied for both light on and off situation.

(i) The test must be under  $20W \times 2$  or  $40W$  fluorescent light, and the distance of view must be at  $30 \pm 5cm$ .

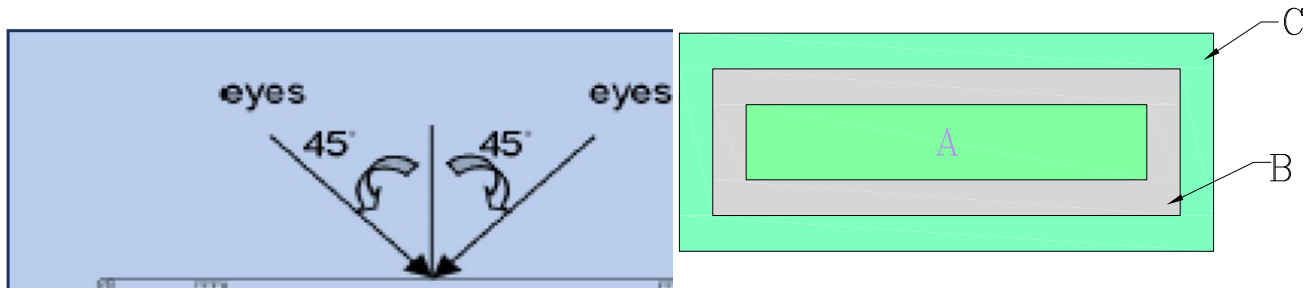


(ii) When test the model of transmissive product must add the reflective plate.

(iii)The test direction is base on about around 10° of vertical line (Left graph)

(iiii)Temperature: 25±5°C Humidity: 65±10%RH

It is base on about around 45° of vertical



(iv) Definition of area (Right graph)

A. Area: Viewing area. B. Area: Out of viewing area.(Outside viewing area)

b. Basic principle:

(i) It will accord to the AQL when the standard can not be described.

(ii) The sample of the lowest acceptable quality level must be discussed by both supplier and customer when any dispute happened.

(iii) Must add new item on time when it is necessary.

c. Standard of inspection: (Unit: mm)

Allowable limits defined in follow Dot defect Table should be met for each white, black , R, G, B raster.

The limits apply to the entire area. Missing white in 60% or more of typical (one color, R or G or B) pixel aperture is defined as a bright defect, less than 60% is acceptable .Black spot in 60% or more of typical pixel aperture is defined as a dark defect, less than 60% is acceptable.

Dot defect table:

Item		White dot defect	Black dot defect	Total
1	Defect counts	3	3	3
2	Combined defect Counts	No combined dot defect allowed. Two Single dot defect that within 5mm during each dot defect should becouneted as combined dot defect.		



## 11.6 Inspection specification

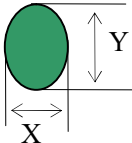
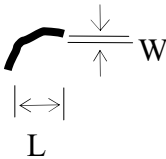
AQL inspection standard

Sampling method: MIL-STD-105E, Level II, single sampling

Classify	Item		Note	AQL	
Major	Display state	Short or open circuit	1	0.65	
		Contrast defect (dim, ghost)			
		LC leakage			
		Flickering			
		No display			
		Wrong viewing direction	2		
		Wrong Back-light	7		
	Non-display	Flat cable or pin reverse	9		
		Wrong or missing component	10		
Minor	Display state	Background color deviation	2	2.5	
		Black spot and dust	3		
		Line defect	4		
		Scratch			
		Rainbow	5		
		Pin hole	6		
	Polarizer	Bubble and foreign material	3		
		Scratch	4		
	PCB,FPC	Scratch	4		
	Soldering	Poor connection	8		
	Wire	Poor connection	9		
	LCD	CHIP OUT	11		

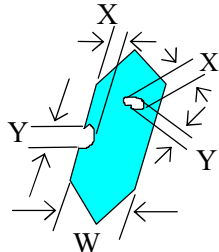
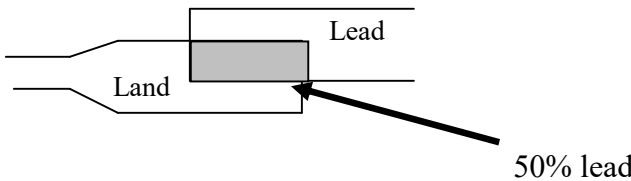


### Note on defect classification:

No.	Item	Criterion																				
1	Short or open circuit	Not allow																				
	LC leakage																					
	Flickering																					
	No display																					
	Wrong viewing direction																					
	Wrong Back-light																					
2	Contrast defect	Refer to approval sample																				
	Background color deviation																					
3	Point defect, Black spot, dust (incl. Polarizer) ex.: dirt under polarizer, Pinhole of reflector ,glass scratch, dirt under glass,scratch on polarizer $\phi = (X+Y)/2$	<div><div></div><table><tr><td>Point</td><td>Acceptable Qty.</td></tr><tr><td>Size</td><td></td></tr><tr><td><math>\phi \leq 0.20</math></td><td>Disregard</td></tr><tr><td><math>0.20 &lt; \phi \leq 0.25</math></td><td>3</td></tr><tr><td><math>0.25 &lt; \phi \leq 0.30</math></td><td>2</td></tr><tr><td><math>\phi &gt; 0.30</math></td><td>0</td></tr></table><div>Unit: mm</div></div>	Point	Acceptable Qty.	Size		$\phi \leq 0.20$	Disregard	$0.20 < \phi \leq 0.25$	3	$0.25 < \phi \leq 0.30$	2	$\phi > 0.30$	0								
Point	Acceptable Qty.																					
Size																						
$\phi \leq 0.20$	Disregard																					
$0.20 < \phi \leq 0.25$	3																					
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$\phi > 0.30$	0																					
4	Line defect	<div><div></div><table><tr><td></td><td>Line</td><td>Acceptable Qty.</td></tr><tr><td>L</td><td>W</td><td></td></tr><tr><td>---</td><td><math>0.015 \geq W</math></td><td>Disregard</td></tr><tr><td><math>3.0 \geq L</math></td><td><math>0.03 \geq W</math></td><td rowspan="2">2</td></tr><tr><td><math>2.0 \geq L</math></td><td><math>0.05 \geq W</math></td></tr><tr><td><math>1.0 \geq L</math></td><td><math>0.1 &gt; W</math></td><td>1</td></tr><tr><td>---</td><td><math>0.05 &lt; W</math></td><td>Applied as point defect</td></tr></table><div>Unit: mm</div></div>		Line	Acceptable Qty.	L	W		---	$0.015 \geq W$	Disregard	$3.0 \geq L$	$0.03 \geq W$	2	$2.0 \geq L$	$0.05 \geq W$	$1.0 \geq L$	$0.1 > W$	1	---	$0.05 < W$	Applied as point defect
	Line	Acceptable Qty.																				
L	W																					
---	$0.015 \geq W$	Disregard																				
$3.0 \geq L$	$0.03 \geq W$	2																				
$2.0 \geq L$	$0.05 \geq W$																					
$1.0 \geq L$	$0.1 > W$	1																				
---	$0.05 < W$	Applied as point defect																				
5	Rainbow	Not more than two color changes across the viewing area																				

No.	Item	Criterion
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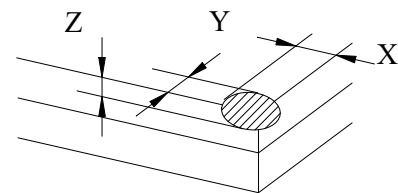
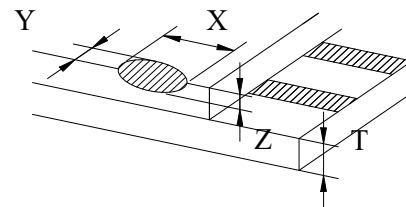
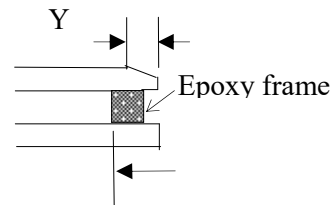
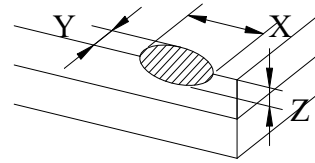
6	<p><b>Segment pattern</b></p> <p><b>W = Segment width</b></p> <p><math>\phi = (X+Y)/2</math></p>	<p><b>(1) Pin hole</b></p> <p><math>\phi &lt; 0.10\text{mm}</math> is acceptable.</p>  <table border="1"> <tr> <th>Point Size</th> <th>Acceptable Qty</th> </tr> <tr> <td><math>\phi \leq 1/4W</math></td> <td>Disregard</td> </tr> <tr> <td><math>1/4W &lt; \phi \leq 1/2W</math></td> <td>1</td> </tr> <tr> <td><math>\phi &gt; 1/2W</math></td> <td>0</td> </tr> </table> <p>Unit: mm</p>	Point Size	Acceptable Qty	$\phi \leq 1/4W$	Disregard	$1/4W < \phi \leq 1/2W$	1	$\phi > 1/2W$	0
Point Size	Acceptable Qty									
$\phi \leq 1/4W$	Disregard									
$1/4W < \phi \leq 1/2W$	1									
$\phi > 1/2W$	0									
7	<b>Back-light</b>	<p>(1) The color of backlight should correspond its specification.</p> <p>(2) Not allow flickering</p>								
8	<b>Soldering</b>	<p>(1) Not allow heavy dirty and solder ball on PCB or FPC. (The size of dirty refer to point and dust defect)</p> <p>(2) Over 50% of lead should be soldered on Land.</p> 								
9	<b>Wire</b>	<p>(1) Copper wire should not be rusted</p> <p>(2) Not allow crack on copper wire connection.</p> <p>(3) Not allow reversing the position of the flat cable.</p> <p>(4) Not allow exposed copper wire inside the flat cable.</p>								
10	<b>PCB,FPC</b>	<p>(1) Not allow screw rust or damage.</p> <p>(2) Not allow missing or wrong putting of component.</p>								



11

LCD

### 2.1.1 chip on the surface

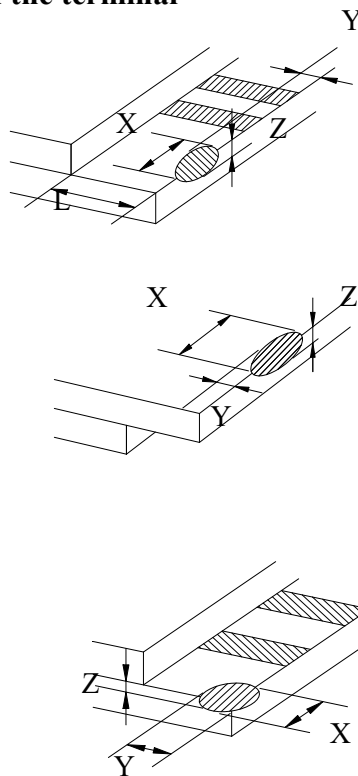


Note: A: LCD Length

X	Y	Z
$>1/8A$	$\leq 0.3\text{mm}$	$\leq 1/2T$
$\leq 1/8A$	Not enter into epoxy frame	$\leq T$
	Not enter into the inner edge of epoxy	$\leq 1/2T$



### 2.1.2 Chip on the terminal

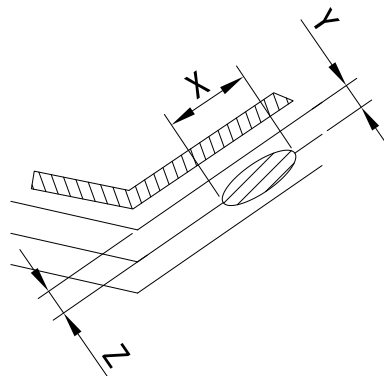


X	Y	Z
$>1/8A$	$\leq 0.3\text{mm}$	$\leq 1/2T$
$\leq 1/8A$	$\leq 1/2L$	$\leq T$
$\leq 1/8A \& \leq 1\text{mm}$	$\leq L$	$\leq T$
$\leq 1/8A \& \leq 2\text{mm}$	$\leq L$	$\leq 1/2T$

Note: A: LCD Length.

the distance between crack and contact pad must be greater than the width of 1<sup>st</sup> contact pad.

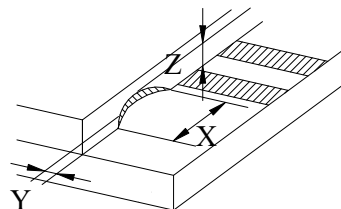
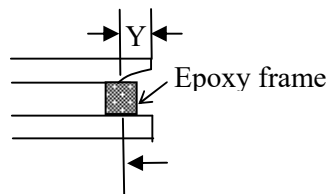
### 2.1.3 Chip out on between side





11

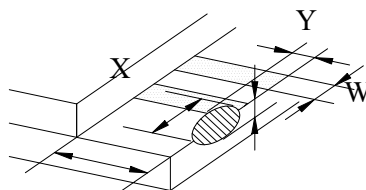
LCD



X	Y	Z
$\leq 1/8A$	Not enter into epoxy frame	$Z \leq 2T$
	Not enter into 1/2 epoxy frame	$Z \leq 1/2T$

Note: A : LCD Length

#### 2.1.4 including corner chip and side chip



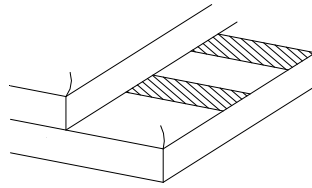
L

Note: A:LCD Length

X	Y	Z
$>1/8A$	$\leq 1/6L$	$\leq 1/2T$
$\leq 1/8A$	$\leq 1/3L$	
$\leq 1/4W$	$\leq 2/3L$	

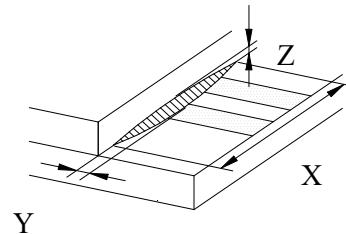


## 2.2 Chip out



- 1) Chip out is that crackles extend to inner edge.
- 2) Crackles round epoxy frame will be rejected.
- 3) Chip out on the terminal will be rejected:  $Z=T$  length  $>1\text{mm}$  or  $Z<T$  length  $>2\text{mm}$
- 4) The chip out at ITO will be rejected.

## 2.3 Poor cutting



X	Y	Z
$>1/8$ A	$\leq 0.3$	$\leq 1/2T$
$\leq 1/8$ A	According to drawing	$1/2T \leq Z \leq T$

**Note :** A: LCD Length.

11

LCD

12

SMT

According to the <Acceptable of electronic assemblies>  
IPC-A-610C class 2 stander. Component missing or function defect  
are Major defect ,the others are Minor defect.

**Any one out of the specification will be rejected.**



## **12. GENERAL PRECAUTIONS**

### **(1) Mounting Method**

The panel of the LCD Module consists of two thin glass plates with polarizers which easily get damaged since the Module is fixed by utilizing fitting holes in the printed circuit board. Extreme care should be taken when handling the LCD Modules.

### **(2) Caution of LCD handling & cleaning**

When cleaning the display surface, use soft cloth with solvent (recommended below) and wipe lightly.

- Isopropyl alcohol
- Ethyl alcohol
- Trichlorotrifluoroethane

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface. Do not use the following solvent:

- Water
- Ketone
- Aromatics

### **(3) Caution against static charge**

The LCD Module use C-MOS LSI drivers, so we recommend that you connect any unused input terminal to VDD or VSS, do not input any signals before power is turned on. And ground your body, Work/assembly table. And assembly equipment to protect against static electricity.

### **(4) Packaging**

Modules use LCD elements, and must be treated as such. Avoid intense shock and falls from a height.

- To prevent modules from degradation. Do not operate or store them exposed directly to sunshine or high temperature/humidity.

### **(5) Caution for operation**

- It is indispensable to drive LCD's within the specified voltage limit since the higher voltage than the limit shorten LCD life. An electrochemical reaction due to direct current causes LCD deterioration, Avoid the use of direct current drive.
- Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them.

However those phenomena do not mean malfunction or out of order with LCD's which will come back in the specified operating temperature range.

- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- As light dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal



open circuit.

Usage under the relative condition of 40°C, 50%RH or less is required.

(6) Storage

In the case of storing for a long period of time (for instance, for years) for the purpose or replacement use, The following ways are recommended.

- Storage in a polyethylene bag with sealed so as not to enter fresh air outside in it, And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light is.

Keeping temperature in the specified storage temperature range.

- Storing with no touch on polarizer surface by the anything else. (It is recommended to store them as they have been contained in the inner container at the time of delivery)

(7) Safety

- It is recommendable to crash damaged or unnecessary LCD into pieces and wash off liquid crystal by using solvents such as acetone and ethanol which should be burned up later.
- When any liquid crystal leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

**Hello Lighting co., ltd reserves the right to change this specification.**

**[www.hello-lighting.com](http://www.hello-lighting.com)**

**- END -**