

DATA SHEET

HLD 1210A-500

12.1" SVGA TFT - Hibrite

Product No. HD-10-507

Rev 1.0

November 2002

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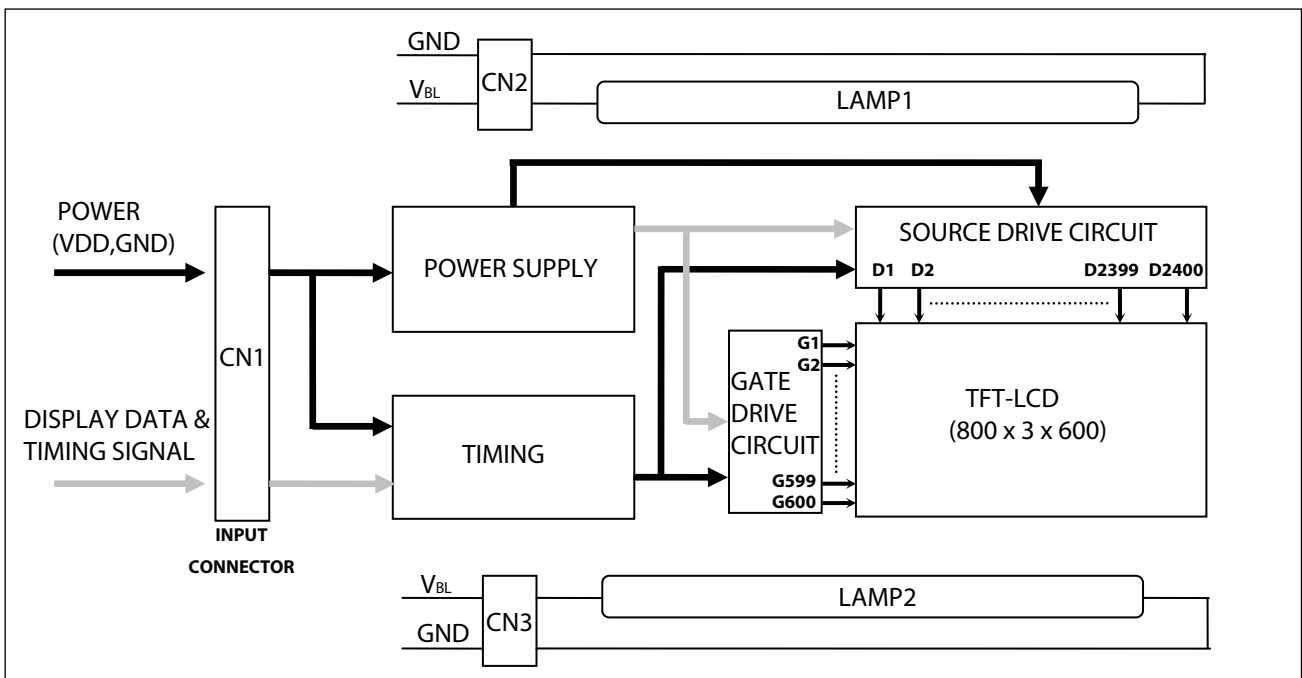
1 Revision History

Date	Rev.No.	Description	Page
Aug. 31.2001	1.0	First Draft (Preliminary)	

2 General Description

The HLD1210A-500 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp (CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 12.1 inches diagonally measured active display area with SVGA resolution (600 vertical by 800 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The HLD1210A-500 has been designed to apply the interface method that enables low power, high speed, low EMI. The HLD1210A-500 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the HLD1210A-500 characteristics provide an excellent flat display for office automation products such as Notebook PC.



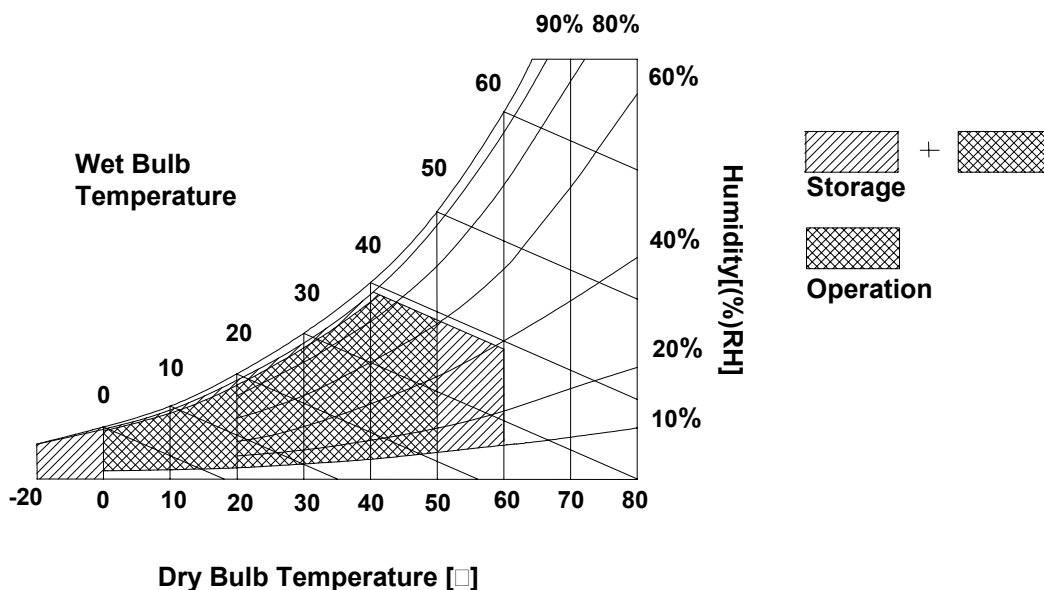
3 General Features

Item	Specification
Active Screen Size	12.1 inches(30.75cm) diagonal
Outline Dimension	280(H) × 218(V) × 11(D) mm (Typ.)
Pixel Pitch	0.3075 mm × 0.3075 mm
Pixel Format	800 horiz. By 600 vert. Pixels RGB strip arrangement
Color Depth	6-bit, 262,144 colors
Luminance, White	500 cd/m2(Typ.)
Power Consumption	7.8 Watt(Typ.)
Weight	675 g (Typ.)
Display Operating Mode	Transmissive mode, normally white
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer

4 Absolute Maximum Ratings

Item	Symbol	Min.	Max	Unit	Notes
Power Supply Voltage for LCD	V _{CC}	- 0.3	4.0	Vdc	at 25 ± 5°C
Operating Temperature (panel surface)	T _{op}	0	+50	°C	1
Storage Temperature (panel surface)	T _{stg}	- 20	+ 60	°C	1
Operating Ambient Humidity	H _{op}	10	90	%RH	1
Storage Humidity	H _{st}	10	90	%RH	1

Note : 1. Temperature and relative humidity range are shown in the figure below.
Wet bulb temperature should be 39°C Max, and no condensation of water.



5 Electrical Specifications

5.1 Electrical Characteristics

The HLD1210A-500 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the CCFL, is typically generated by an inverter. The inverter is an external unit to the LCD.

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
Power Supply Input Voltage for LCD	V _{CC}	3.0	3.3	3.6	Vdc	
Power Supply Input Current for LCD	I _{CC}	-	240	310	mA	1
Power Consumption	P _C	-	0.8	1.0	Watt	1

5.2 Backlight

Item	Symbol	Min.	Typ.	Max	Unit	Remarks	
Lamp Voltage	V _{BL}	540(8.0mA)	580(6.0mA)	665(3.0mA)	Vrms	2	
Lamp Current	I _{BL}	3.0	6.0	8.0	mArms	3	
Lamp Frequency	F _{BL}	30	55	-	kHz	5	
Lamp Starting Voltage	V _S	Ta=25°C	-	-	875	Vrms	4
		Ta=0°C	-	-	1300	Vrms	
Discharge Stabilization Time	T _S	-	-	3	Min	6	
Power Consumption	P _{BL}	-	7.0	8.6	Watt	7	
Lamp Life Time		50.000	-	-	h	8	

Remarks

The design of the inverter must have specifications for the lamp in LCD Assembly.

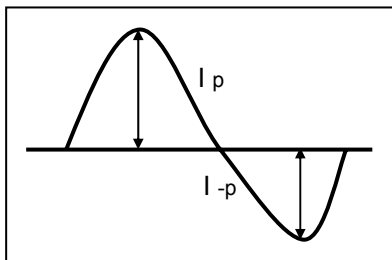
The performance of the Lamp in LCM, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC inverter. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter. When you design or order the inverter, please make sure unwanted lighting caused by the mismatch of the lamp and the inverter(no lighting, flicker, etc) never occurs. When you confirm it, the LCD Assembly should be operated in the same condition as installed in your instrument.

1. VCC=3.3V, 25°C, fV (frame frequency) = 60Hz condition, whereas Mosaic pattern(Typ),full blackpattern(Max) is displayed.
2. The variance of the voltage is ± 10%.
3. The typical operating current is for the typical surface luminance (LWH) in optical characteristics.
4. The voltage above VS should be applied to the lamps for more than 1 second for start-up. Otherwise, the lamps may not be turned on. The used lamp current is the lamp typical current.
5. The output of the inverter must have symmetrical(negative and positive) voltage waveform and symmetrical current waveform.(Unsymmetrical ratio is less than 10%) Please do not use the inverter which has unsymmetrical voltage and unsymmetrical current and spike wave. Lamp frequency may produce interference with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away possible from the horizontal synchronous frequency and from its harmonics in order to prevent interference.
6. Let's define the brightness of the lamp after being lighted for 5 minutes as 100%. Ts is the time required for the brightness of the center of the lamp to be not less than 95%.

7. The lamp power consumption shown above does not include loss of external inverter. The used lamp current is the lamp typical current. (2 Lamp)
8. The life time is determined as the time at which brightness of the lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at $25 \pm 2^\circ\text{C}$.

* Requirements for a system inverter design, which is intended to have a better display performance, a better power efficiency and a more reliable lamp, are following. It shall help increase the lamp lifetime and reduce leakage current.

- a. The asymmetry rate of the inverter waveform should be less than 10%.
 - b. The distortion rate of the waveform should be within $\sqrt{2} \pm 10\%$.
- * Inverter output waveform had better be more similar to ideal sine wave.



* Asymmetry rate:

$$| I_p - I_{-p} | / I_{rms} * 100\%$$

* Distortion rate

$$I_p \text{ (or } I_{-p}) / I_{rms}$$

- * Do not attach a conducting tape to lamp connecting wire.
If the lamp wire attach to a conducting tape, TFT-LCD Module has a low luminance and the inverter has abnormal action. Because leakage current is occurred between lamp wire and conducting tape.

5.3 Interface Connections

This LCD employs three interface connections, a 41 pin connector is used for the module electronics interface and the other connectors are used for the integrated backlight system.

The electronics interface connector is a DF9B-41P-1V manufactured by Hirose or equivalent.

Pin	Symbol	Function	Notes
1	Vss	Ground	See Note 1
2	CLK	Main Clock	
3	Vss	Ground	See Note 1
4	Hsync	Horizontal sync.	
5	Vsync	Vertical sync.	
6	Vss	Ground	See Note 1
7	Vss	Ground	See Note 1
8	Vss	Ground	See Note 1
9	R0	Red Data	Red data least significant bit (LSB)
10	R1	Red Data	
11	R2	Red Data	
12	Vss	Ground	See Note 1
13	R3	Red Data	
14	R4	Red Data	
15	R5	Red Data	Red data most significant bit (MSB)
16	Vss	Ground	See Note 1
17	Vss	Ground	See Note 1
18	Vss	Ground	See Note 1
19	G0	Green Data	Green data least significant bit (LSB)
20	G1	Green Data	
21	G2	Green Data	
22	Vss	Ground	See Note 1
23	G3	Green Data	
24	G4	Green Data	
25	G5	Green Data	Green data most significant bit (MSB)
26	Vss	Ground	See Note 1
27	Vss	Ground	See Note 1
28	Vss	Ground	See Note 1
29	B0	Blue Data	Blue data least significant bit (LSB)
30	B1	Blue Data	
31	B2	Blue Data	
32	Vss	Ground	See Note 1
33	B3	Blue Data	
34	B4	Blue Data	
35	B5	Blue Data	Blue data most significant bit (MSB)
36	Vss	Ground	See Note 1
37	DE	Data Enable signal	
38	L_R	Horizontal display mode Select signal	See Note 3
39	VDD	Power Input	See Note 2
40	VDD	Power Input	See Note 2
41	U-D	Vertical display mode select signal	See Note 4

Notes :

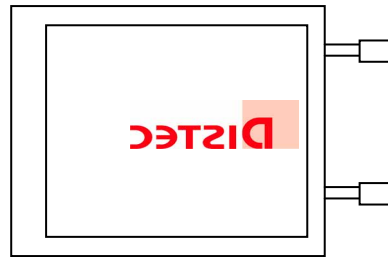
1. All GND (Ground) Pins Should be connected together and the LCD's metal frame.
2. All VDD (Power Input) Pins should be connected together.

3. Horizontal Display Mode



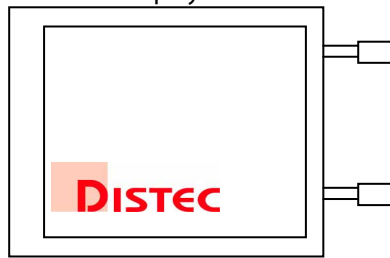
L_R = GND (LOW), U_D = VDD (HIGH)

Display Reverse
↔



L_R = VDD (HIGH), U_D = VDD (HIGH)

4. Vertical Display Mode



L_R = GND (LOW), U_D = VDD (HIGH)

Display Reverse
↔



L_R = GND (LOW), U_D = GND (LOW)

CN2, CN3 (backlight)
Backlight side connector
Inverter side connector:

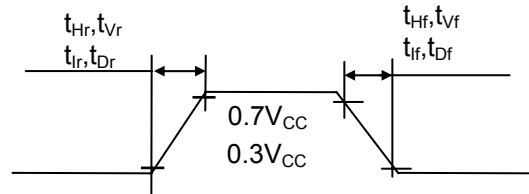
BHR-03VS-1 (JST)
SM02B(8.0)B-BHS-1-TB (JST)

Pin Number	Symbol	Function
1	HV	High Voltage (Pink color)
2	NC	-
3	LV	Low Voltage (white color)

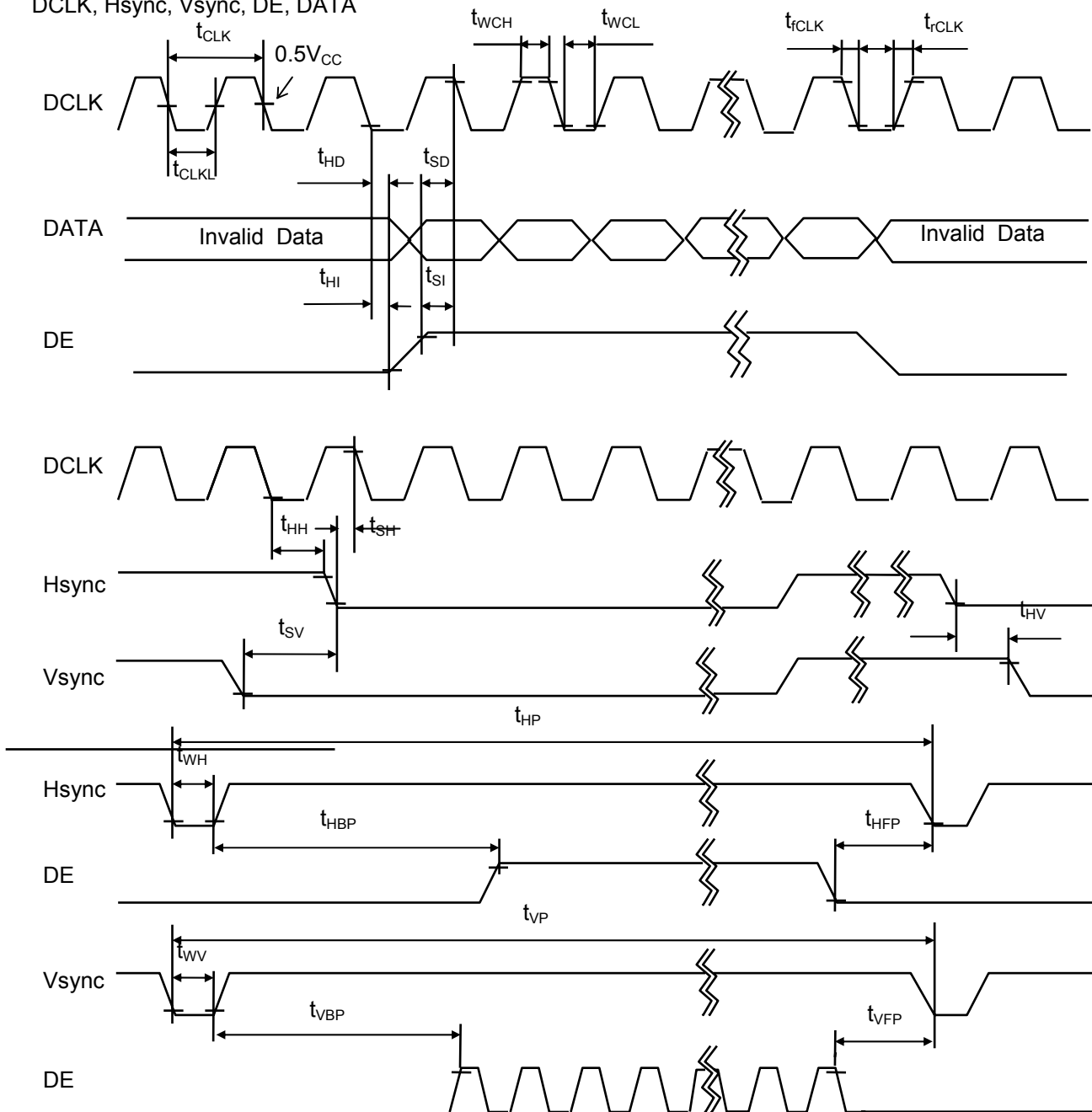
5.4 Signal Timing Specifications

Item	Symbol	Min.	Typ.	Max.	Unit	Note	
DCLK	Frequency	f_{CLK}	-	38.5	40	MHz	
	Width_Low	t_{WCL}	8	-	-	ns	
	Width_High	t_{WCH}	5	-	-		
	Rise Time	t_{rCLK}	-	-	25		
	Fall Time	t_{fCLK}	-	-	25		
Hsync	Setup Time	t_{SH}	3	-	-	ns	For Dclk
	Hold Time	t_{HH}	8	-	-		
	Period	t_{HP}	990	1024	1100	t_{CLK}	
	Width_Active	t_{WH}	12	-	120	ns	
	Rise/Fall Time	t_{Hr}, t_{Hf}	-	-	30		
Vsync	Setup Time	t_{SV}	0	-	-	ns	For Hsync
	Hold Time	t_{HV}	2	-	-		
	Period	t_{VP}	607	625	730	t_{HP}	
	Width_Active	t_{WV}	2	-	24	ns	
	Rise/Fall Time	t_{Vr}, t_{Vf}	-	-	50		
DE	Setup Time	t_{SI}	6	-	-	ns	For Dclk
	Hold Time	t_{HI}	1	-	-		
	Rise/Fall Time	t_{Ir}, t_{If}	-	-	30	ns	
	Horizontal Back Porch	t_{HBP}	12	-	-	t_{CLK}	
	Horizontal Front Porch	t_{HFP}	12	-	-		
	Vertical Back Porch	t_{VBP}	5	-	-	t_{HP}	
	Vertical Front Porch	t_{VFP}	1	-	-		
DATA	Setup Time	t_{SD}	6	-	-	ns	
	Hold Time	t_{HD}	3	-	-		
	Rise/Fall Time	t_{Dr}, t_{Df}	-	-	25	ns	

5.5 Signal Timing Waveforms



DCLK, Hsync, Vsync, DE, DATA

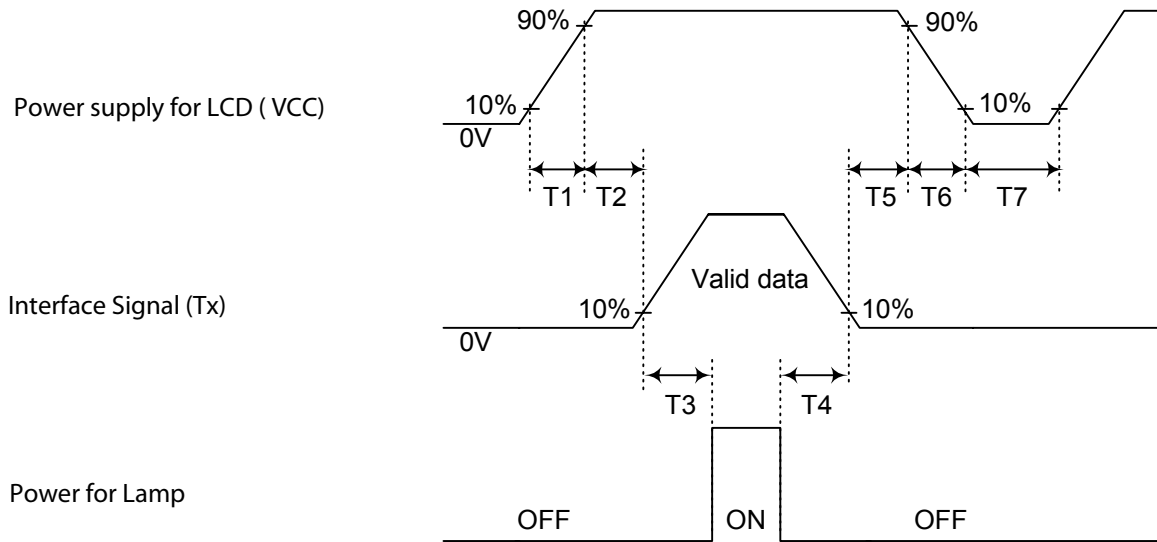


5.6 Color input data reference

The brightness of each primary color (red, green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Color		Input Color Data																	
		RED						GREEN						BLUE					
		MSB			LSB			MSB			LSB			MSB			LSB		
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
					
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
					
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
BLUE	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
					
	BLUE (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	BLUE (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

5.7 Power Sequence



Parameter	Value			Units
	Min.	Typ.	Max.	
T ₁	-	-	10	(ms)
T ₂	0	-	50	(ms)
T ₃	200	-	-	(ms)
T ₄	200	-	-	(ms)
T ₅	0	-	50	(ms)
T ₆	-	-	10	(ms)
T ₇	400	-	-	(ms)

Note:

1. Please avoid floating state of interface signal at invalid period.
2. When the interface signal is invalid, be sure to pull down the power supply for LCD VCC to 0V.
3. Lamp power must be turn on after power supply for LCD and interface signal are valid.

6 Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0°.

FIG. 1 presents additional information concerning the measurement equipment and method.

FIG. 1 Optical Characteristic Measurement Equipment and Method

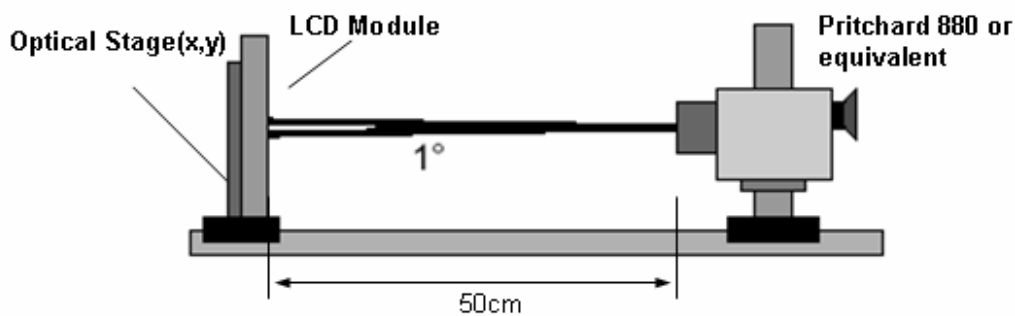


Table 8. OPTICAL CHARACTERISTICS

$T_a=25^\circ\text{C}$, $V_{CC}=3.3\text{V}$, $f_v=60\text{Hz}$, $D_{clk}=38.5\text{MHz}$, $V_{IN}=3.3\text{V}$, $I_L=6.0\text{mA}$

Parameter	Symbol	Values			Units	Notes
		Min	Typ	MAx		
Contrast Ratio	CR	-	200	-		1
Surface Luminance, white	L_{WH}	255	300	-	cd/m ²	2
Luminance Variation	δ_{WHITE}	-	1.25	1.45		3
Response Time						4
Rise Time	Tr_R	-	20	50	ms	
Decay Time	Tr_D	-	35	50	ms	
Color Coordinates						
RED	RX	0.570	0.600	0.630		
	RY	0.313	0.343	0.373		
GREEN	GX	0.304	0.334	0.364		
	GY	0.504	0.534	0.564		
BLUE	BX	0.125	0.155	0.185		
	BY	0.119	0.149	0.179		
WHITE	WX	0.310	0.340	0.370		
	WY	0.316	0.346	0.376		
Viewing Angle						5
x axis, right($\Phi=0^\circ$)	Θ_r	55	60	-	degree	
x axis, left ($\Phi=180^\circ$)	Θ_l	55	60	-	degree	
y axis, up ($\Phi=90^\circ$)	Θ_u	35	40	-	degree	
y axis, down ($\Phi=270^\circ$)	Θ_d	50	55	-	degree	

Note:

1. Contrast Ratio(CR) is defined mathematically as:

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$
2. Surface luminance is the center point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.
3. The variation in surface luminance , The Panel total variation (δ WHITE) is determined by measuring LN at each test position 1 through 5, and then dividing the maximum LN of 5 points luminance by minimum LN of 5 points luminance. For more information see FIG 2.

$$\delta \text{ WHITE} = \frac{\text{Maximum}(L1,L2, \dots L5)}{\text{Minimum}(L1,L2, \dots L5)}$$
4. Response time is the time required for the display to transition from white to black (rise time, TrR) and from black to white(Decay Time, TrD). For additional information see FIG 3.
5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
6. Gray scale specification * fV=60Hz

Gray Level	Luminance [%] (Typ)
L0	0.2
L7	0.8
L15	4.5
L23	11.0
L31	22.0
L39	35.5
L47	52.5
L55	74.0
L63	100

FIG. 2 Luminance

<measuring point for surface luminance & measuring point for luminance variation>

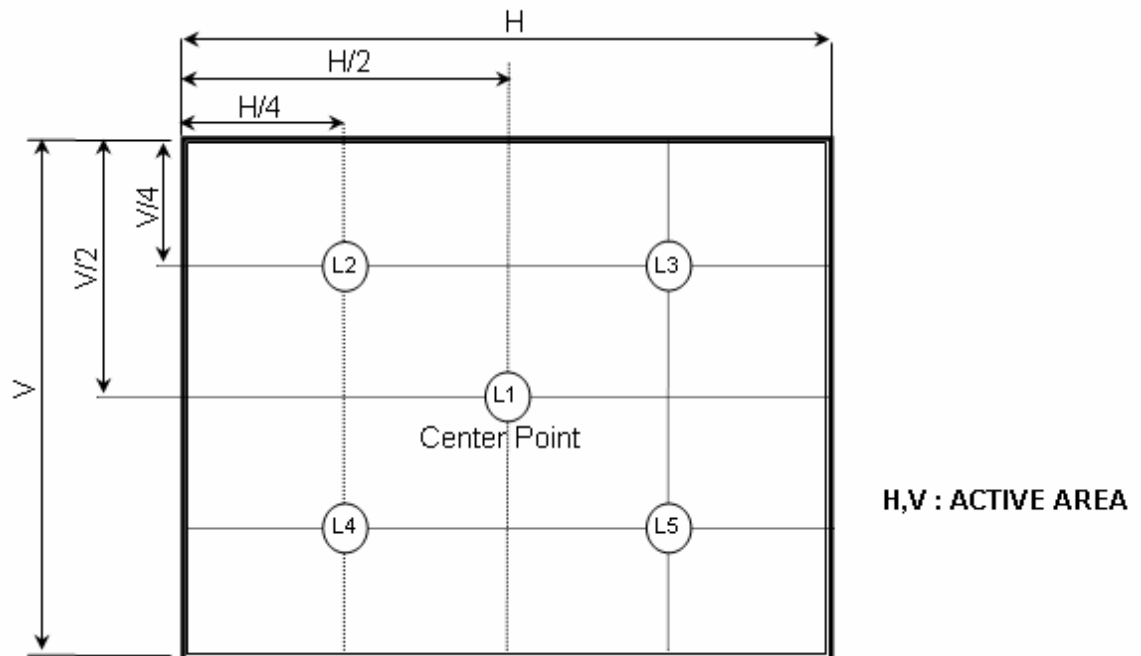


FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

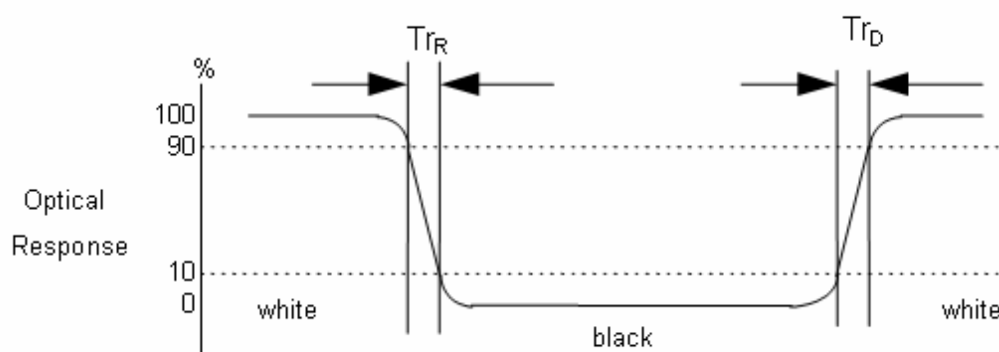
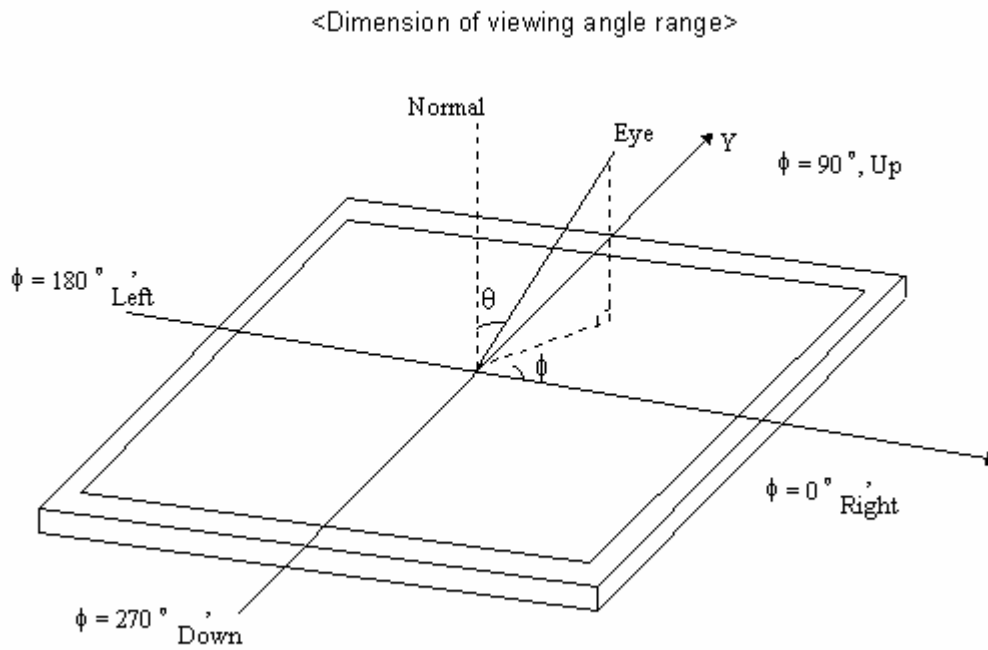


FIG. 4 Viewing angle



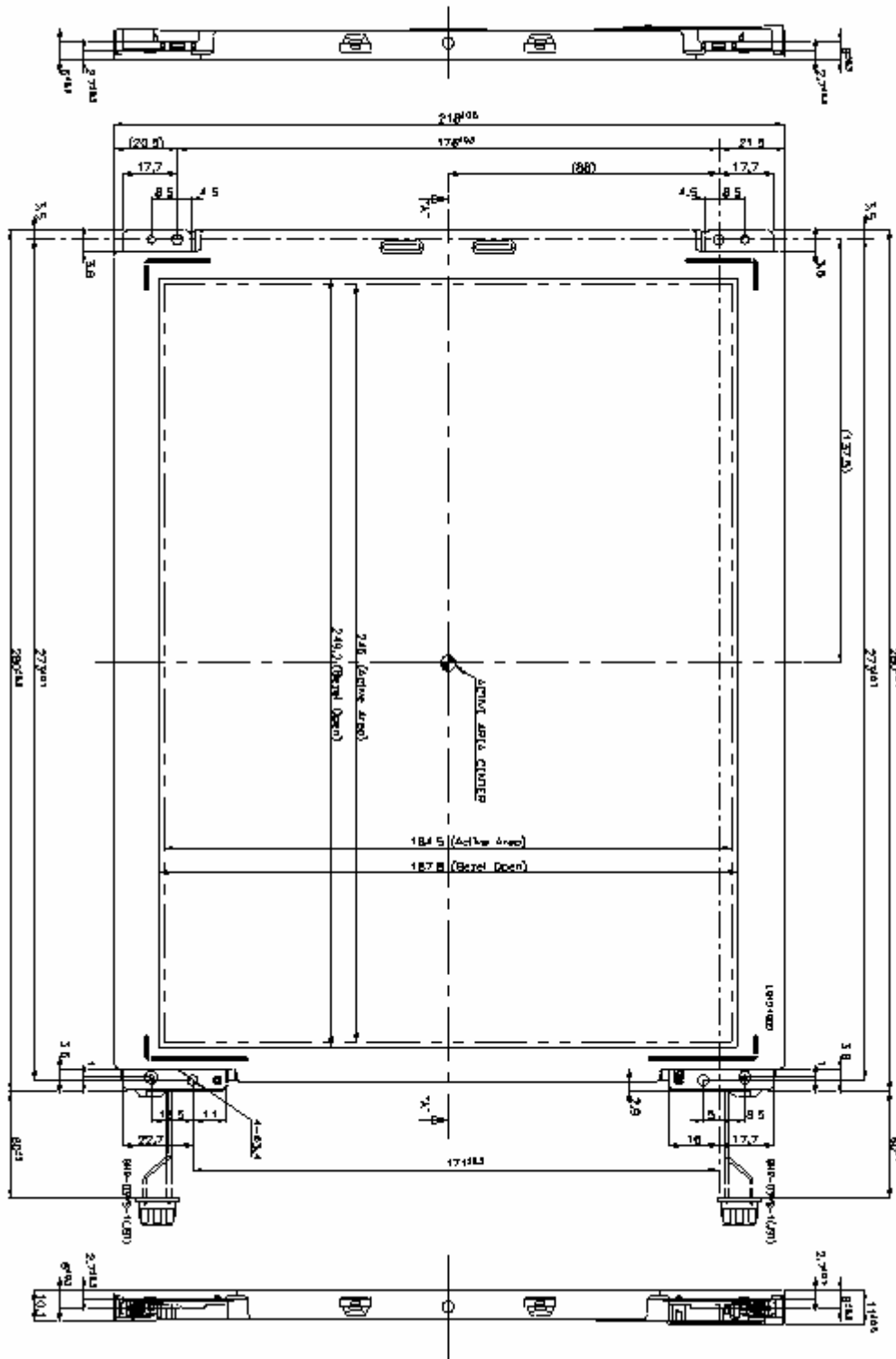
7 Mechanical Characteristics

The contents provide general mechanical characteristics for the model HLD1210A-500. In addition the figures in the next page are detailed mechanical drawing of the LCD.

Outline Dimension	Horizontal	280.0 ± 0.5mm
	Vertical	218.0 ± 0.5mm
	Depth	Typ. 11.0 mm, Max. 11.5mm
Bezel Area	Horizontal	249.0 ± 0.5mm
	Vertical	187.5 ± 0.5mm
Active Display Area	Horizontal	246.0 mm
	Vertical	184.5 mm
Weight	675 (Typ.) 690g (Max.)	
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer	

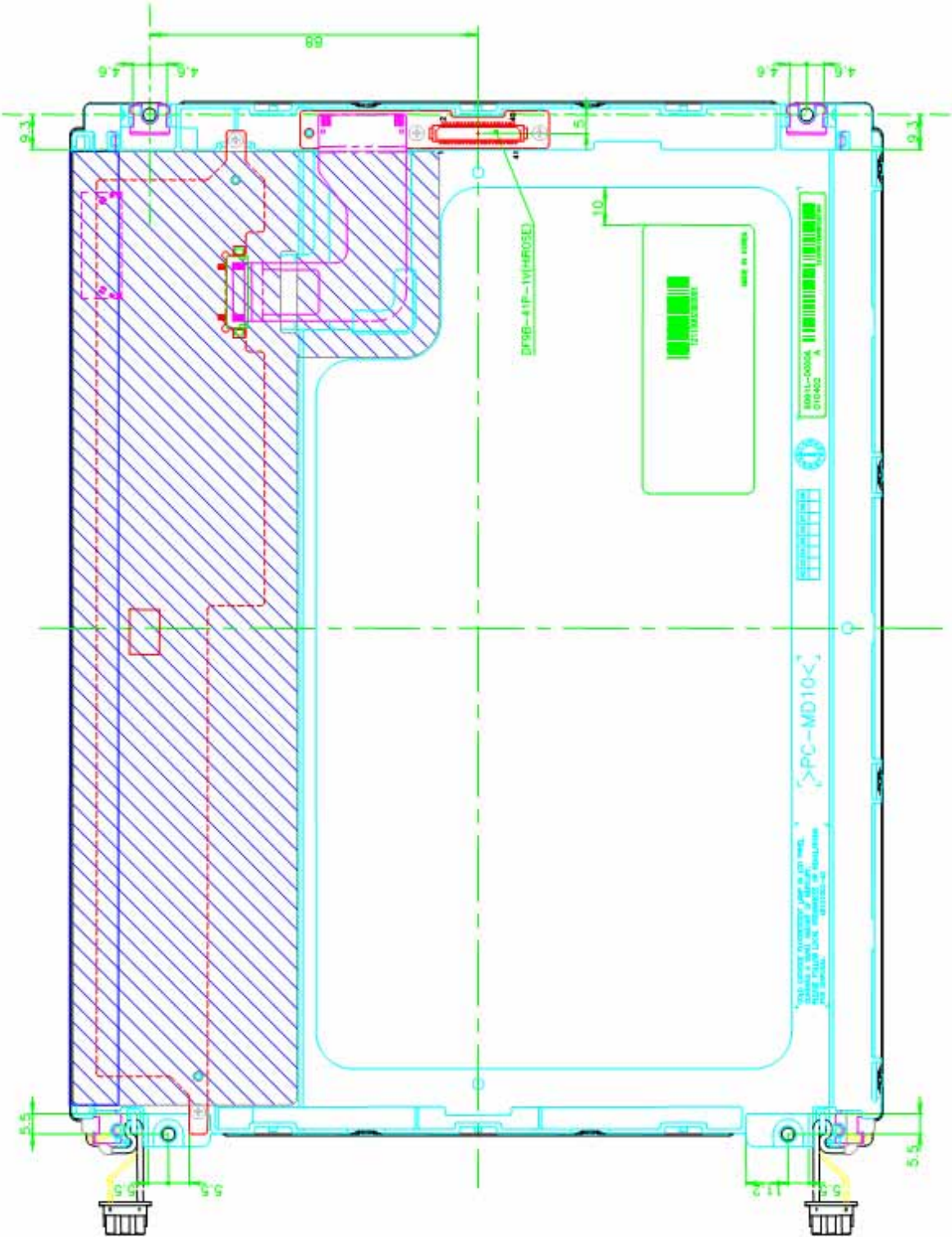
FRONT VIEW

Note) Unit:[mm], General tolerance: $\pm 0.5\text{mm}$



REAR VIEW

Note) Unit:[mm], General tolerance: ±0.5mm



8 Reliability

Environment Test Conditions

No.	Test Item	Conditions
1	High temperature storage test	Ta= 60°C, 240h
2	Low temperature storage test	Ta= -20°C, 240h
3	High temperature operation test	Ta= 50°C, 50%RH, 240h
4	Low temperature operation test	Ta= 0°C, 240h
5	Vibration test (non-operating)	Sine wave, 10 ~ 500 ~ 10Hz, 1.5G, 0.37oct/min 3 axis, 1hour/axis
6	Shock test (non-operating)	Half sine wave, 180G, 2ms one shock of each six faces(i.e. run 180G 6ms for all six faces)
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr

(Result Evaluation Criteria)

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

9 International Standards

9.1 Safety

- a) UL 1950 Third Edition, Underwriters Laboratories, Inc. Jan. 28, 1995.
Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.
- b) CAN/CSA C22.2 No. 950-95 Third Edition, Canadian Standards Association, Jan. 28, 1995.
Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.
- c) EN 60950 : 1992+A1: 1993+A2: 1993+A3: 1995+A4: 1997+A11: 1997
IEC 950 : 1991+A1: 1992+A2: 1993+A3: 1995+A4: 1996
European Committee for Electrotechnical Standardization(CENELEC)
EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

9.2 EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz." American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998

10 Packing

10.1 Designation of Lot Mark

a) Lot Mark

A	B	C	D	E	F	G	H	I	J	K	L	M
---	---	---	---	---	---	---	---	---	---	---	---	---

A,B,C : SIZE

D : YEAR

E : MONTH

F,G : PANEL CODE

H : ASSEMBLY CODE

I,J,K,L,M : SERIAL NO.

Note

1. YEAR

Year	97	98	99	2000	2001	2002	2003	2004	2005	2006	2007
Mark	7	8	9	0	1	2	3	4	5	6	7

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	4	4	5	6	7	8	9	A	B	C

3. Serial No

Serial No.	1 ~ 99,999	100,000 ~
Mark	00001 ~ 99999	A0001 ~ A9999, ----, Z9999

b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module.
This is subject to change without prior notice.

10.2 Packing Form

a) Package quantity in one box : 10 pcs

b) Box Size : 360mm × 322mm × 391mm

11 Precautions

Please pay attention to the following when you use this TFT LCD module.

11.1 Mounting Precautions

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaked with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

11.2 Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :
 $V = \pm 200\text{mV}$ (Over and under shoot voltage)
- (2) Response time depends on the temperature. (In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) In lower temperature response time (required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.

11.3 Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. and don't touch interface pin directly.

11.4 Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

11.5 Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

11.6 Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is ecognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.



Our company network supports you worldwide with offices in Germany, Turkey, Great Britain and the USA. For more information please contact:

