

### N-Channel Enhancement Mode Power MOSFET

# **Description**

The HM2300B uses advanced trench technology to provide excellent  $R_{\rm DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a battery protection or in other switching application.

#### **General Features**

•  $V_{DS} = 20V, I_D = 4.5A$ 

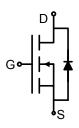
 $R_{DS(ON)}$  < 40m $\Omega$  @  $V_{GS}$ =2.5V

 $R_{DS(ON)}$  < 33m $\Omega$  @  $V_{GS}$ =4.5V

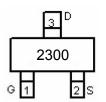
- High power and current handing capability
- Lead free product is acquired
- Surface mount package

# **Application**

- Battery protection
- Load switch
- ●Power management



#### Schematic diagram



Marking and pin assignment



SOT-23 top view

# **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
2300	HM2300B	SOT-23	Ø180mm	8 mm	3000 units

Absolute Maximum Ratings (T<sub>A</sub>=25 ℃unless otherwise noted)

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	20	V	
Gate-Source Voltage		V <sub>G</sub> s	±12	V	
Continuous Drain Current	T <sub>A</sub> =25℃		4.5	^	
Continuous Diam Current	T <sub>A</sub> =70°C	I <sub>D</sub>	3.6	A	
Drain Current-Pulsed (Note 1)		I <sub>DM</sub>	13.5	Α	
Maximum Power Dissipation		P <sub>D</sub>	1.25	W	
Operating Junction and Storage Temperature Range		$T_{J}, T_{STG}$	-55 To 150	$^{\circ}$ C	

#### **Thermal Characteristic**

Thermal Resistance.Junction-to-Ambient (Note 2)	$R_{ hetaJA}$	100	°C/W
Thermal Resistance, surface to 7 molent (Note 2)	· \OJA	100	CIVV

## Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

Parameter Syr		Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	20	22	-	V

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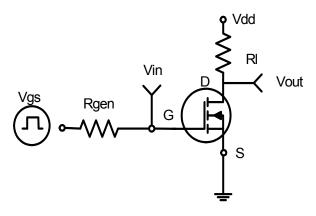
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ =20V, $V_{GS}$ =0V	-	-	1	μΑ
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±12V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)	·					
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250μA	0.5	0.65	1.2	V
D : 0 0 0 1 D : 1	Б	V <sub>GS</sub> =2.5V, I <sub>D</sub> =4.0 A	-	33	40	mΩ
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =4.5A	-	22	33	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =10V,I <sub>D</sub> =4A	-	10	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>	\/ -0\/\/ -0\/	-	500	-	PF
Output Capacitance	Coss	V <sub>DS</sub> =8V,V <sub>GS</sub> =0V, F=1.0MHz	-	300	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.UMHZ	-	140	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	20	40	nS
Turn-on Rise Time	t <sub>r</sub>	V <sub>DD</sub> =10V,I <sub>D</sub> =1A	-	18	40	nS
Turn-Off Delay Time	$t_{d(off)}$	$V_{GS}$ =4.5 $V$ , $R_{GEN}$ =6 $\Omega$	-	60	108	nS
Turn-Off Fall Time	t <sub>f</sub>		-	28	56	nS
Total Gate Charge	Qg		-	10	15	nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =10V,I <sub>D</sub> =3A,V <sub>GS</sub> =4.5V	-	2.3	-	nC
Gate-Drain Charge	$Q_{gd}$		-	2.9	-	nC
Drain-Source Diode Characteristics	•	•				
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =1A	-	-	1.2	V
Diode Forward Current (Note 2)	I <sub>S</sub>		-	-	1	Α

#### Notes:

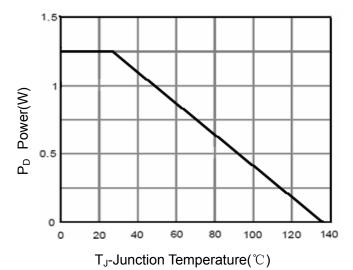
- **1.** Repetitive rating: pulse width limited by maximum junction temperature.
- **2.** Surface mounted on FR4 Board,  $t \le 10$  sec.
- **3.** Pulse test: pulse width  $\leq$  300 $\mu$ s, duty cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production

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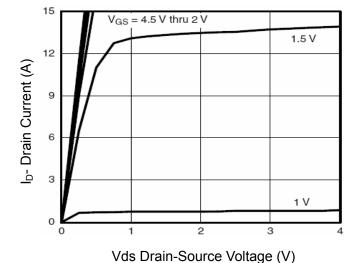
# **Typical Electrical and Thermal Characteristics**



**Figure 1:Switching Test Circuit** 



**Figure 3 Power Dissipation** 



**Figure 5 Output CHARACTERISTICS** 

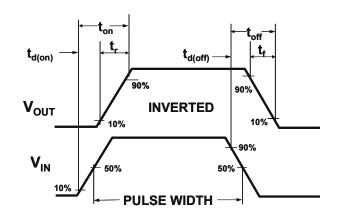
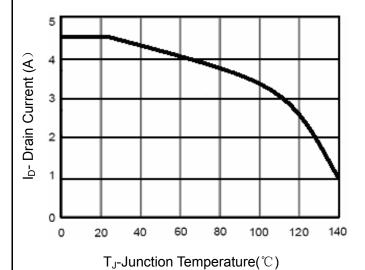


Figure 2:Switching Waveforms



**Figure 4 Drain Current** 

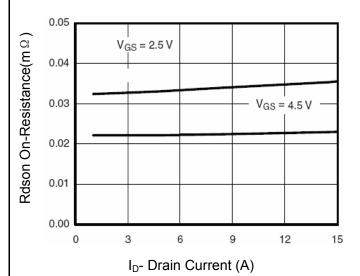
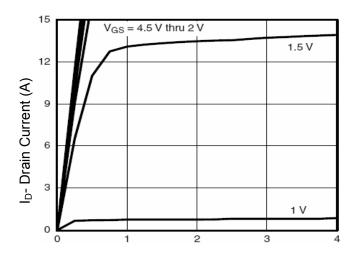


Figure 6 Drain-Source On-Resistance

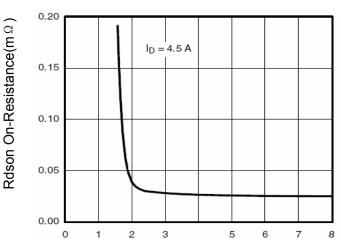
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Vgs Gate-Source Voltage (V)

# Figure 7 Transfer Characteristics



Vgs Gate-Source Voltage (V)

## Figure 9 Rdson vs Vgs

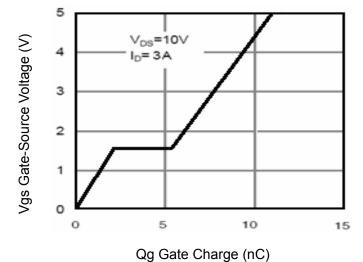


Figure 11 Gate Charge

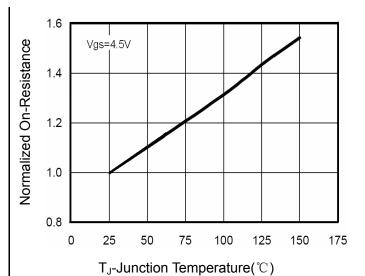


Figure 8 Drain-Source On-Resistance

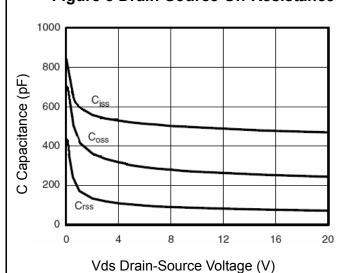
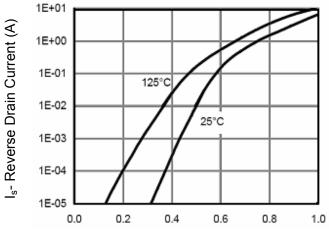


Figure 10 Capacitance vs Vds

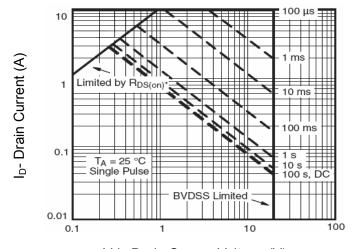


Vsd Source-Drain Voltage (V)

Figure 12 Source- Drain Diode Forward

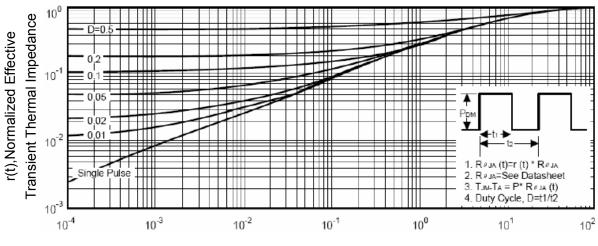
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Vds Drain-Source Voltage (V)

Figure 13 Safe Operation Area

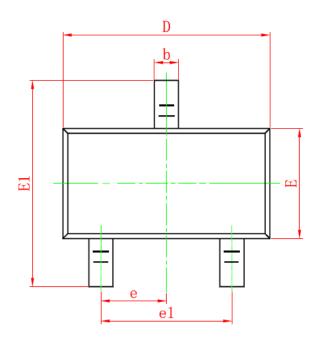


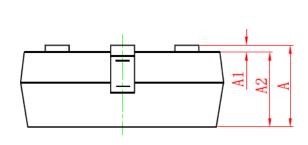
Square Wave Pluse Duration(sec)

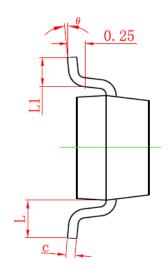
**Figure 14 Normalized Maximum Transient Thermal Impedance** 

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# **SOT-23 Package Information**







Symbol	Dimensions in Millimeters		
Symbol	MIN.	MAX.	
Α	0.900	1.150	
<b>A</b> 1	0.000	0.100	
A2	0.900	1.050	
b	0.300	0.500	
С	0.080	0.150	
D	2.800	3.000	
E	1.200	1.400	
E1	2.250	2.550	
е	0.950TYP		
e1	1.800	2.000	
L	0.550REF		
L1	0.300	0.500	
θ	0°	8°	

#### **Notes**

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- 5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

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