

N-Channel Enhancement Mode Power MOSFET

DESCRIPTION

The HM2310 uses advanced trench technology to provide excellent $R_{\rm DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

GENERAL FEATURES

V_{DS} =60V,I_D =3A

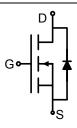
 $R_{DS(ON)}$ <105m Ω @ V_{GS} =10V

 $R_{DS(ON)}$ < 125m Ω @ V_{GS} =4.5V

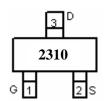
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

Application

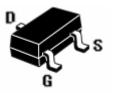
- Battery Switch
- DC/DC Converter



Schematic diagram



Marking and pin Assignment



SOT-23 -3L top view

Package Marking And Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
2310	HM2310	SOT-23-3L	Ø180mm	8 mm	3000 units

Absolute Maximum Ratings (TA=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	60	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	3	Α
Drain Current-Pulsed (Note 1)	I _{DM}	10	Α
Maximum Power Dissipation	P _D	1.7	W
Operating Junction and Storage Temperature Range	T_{J} , T_{STG}	-55 To 150	$^{\circ}$ C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	R _{0JA}	73.5	°C/W
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Electrical Characteristics (TA=25℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	60	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V,V _{GS} =0V	-	-	1	μA

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Page 1 v1.0



Cata Pady Lagkago Current		\/ =+20\/\/ =0\/			±100	nA	
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	_	-	±100	IIA	
On Characteristics (Note 3)							
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1	1.4	3	V	
Drain-Source On-State Resistance	R _{DS(ON)}	V_{GS} =10V, I_D =3A	-	-	105	mΩ	
Diain-Source On-State Resistance		V _{GS} =4.5V, I _D =3A	-	-	125	mΩ	
Forward Transconductance	g FS	S V _{DS} =15V,I _D =2A		-	-	S	
Dynamic Characteristics (Note4)							
Input Capacitance	C _{lss}	V _{DS} =30V,V _{GS} =0V,	-	247	-	PF	
Output Capacitance	Coss	V _{DS} -30V,V _{GS} -0V, F=1.0MHz	-	34	-	PF	
Reverse Transfer Capacitance	C _{rss}	F-1.0MHZ	-	19.5	-	PF	
Switching Characteristics (Note 4)							
Turn-on Delay Time	$t_{d(on)}$		-	6	-	nS	
Turn-on Rise Time	t _r	V_{DD} =30V, I_{D} =1.5A	-	15	-	nS	
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{GEN} =1 Ω	-	15	-	nS	
Turn-Off Fall Time	t _f		-	10	-	nS	
Total Gate Charge	Q_g	\/ -20\/ -24	-	6	-	nC	
Gate-Source Charge	Q_{gs}	$V_{DS}=30V,I_{D}=3A,$ $V_{GS}=4.5V$	-	1	-	nC	
Gate-Drain Charge	Q _{gd}	V GS-4.5 V	-	1.3	-	nC	
Drain-Source Diode Characteristics							
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =3A	-	-	1.2	V	
Diode Forward Current (Note 2)	Is		-	-	3	Α	

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- **3.** Pulse Test: Pulse Width ≤ 300μ s, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

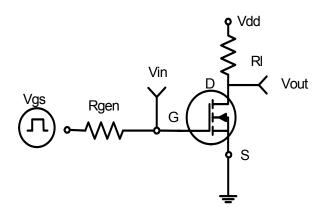


Figure 1:Switching Test Circuit

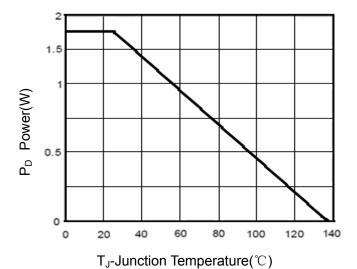


Figure 3 Power Dissipation

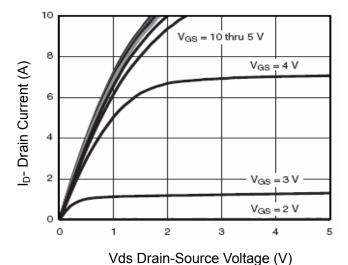


Figure 5 Output CHARACTERISTICS

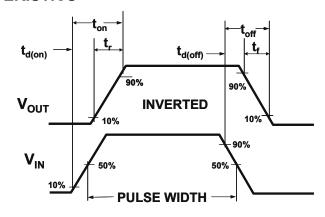


Figure 2:Switching Waveforms

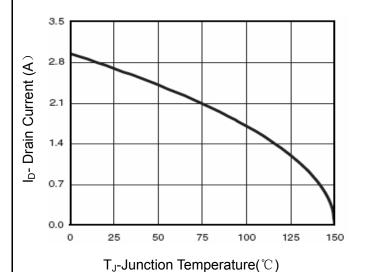


Figure 4 Drain Current

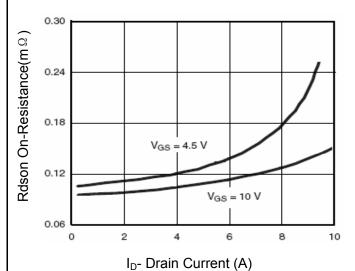
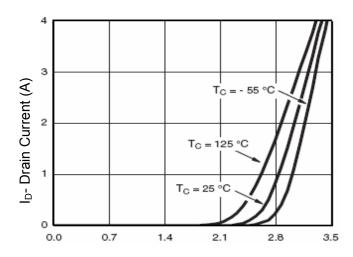


Figure 6 Drain-Source On-Resistance

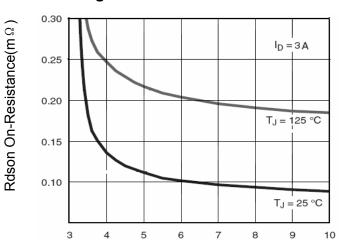
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Page 3 v1.0



Vgs Gate-Source Voltage (V)

Figure 7 Transfer Characteristics



Vgs Gate-Source Voltage (V)

Figure 9 Rdson vs Vgs

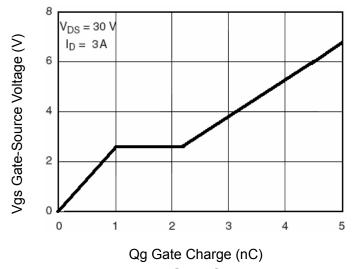
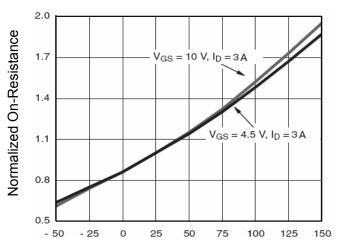
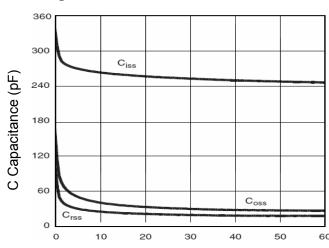


Figure 11 Gate Charge



T_J-Junction Temperature(°C)

Figure 8 Drain-Source On-Resistance



Vds Drain-Source Voltage (V)

Figure 10 Capacitance vs Vds

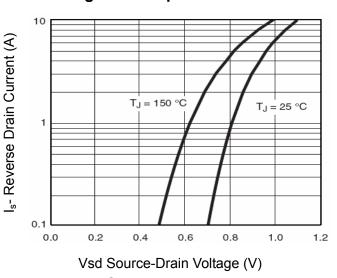


Figure 12 Source- Drain Diode Forward

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Page 4 v1.0

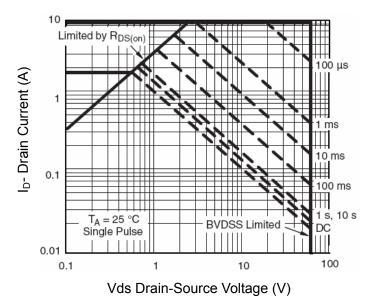


Figure 13 Safe Operation Area

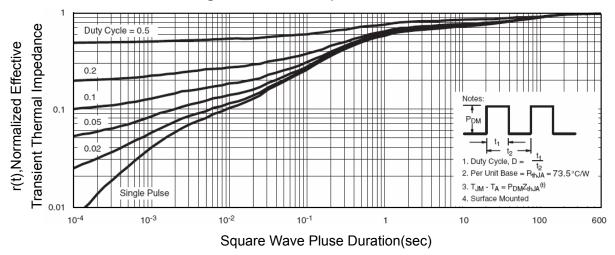
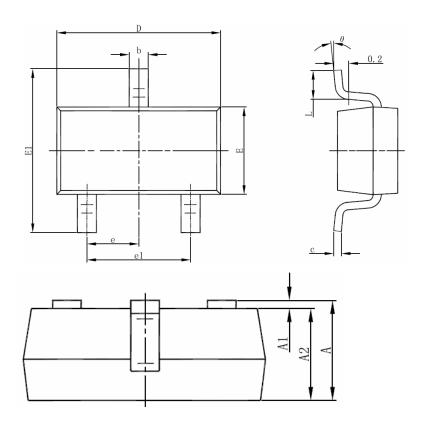


Figure 14 Normalized Maximum Transient Thermal Impedance

SOT-23-3L PACKAGE INFORMATION



Country of	Dimensions In	Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
С	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
E	1.500	1.700	0.059	0.067	
E1	2.650	2.950	0.104	0.116	
е	0.950	(BSC)	0.037(BSC)		
e1	1.800	2.000	0.071	0.079	
Ĺ	0.300	0.600	0.012	0.024	
θ	0°	8°	0°	8°	

NOTES

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- $5. \ Controlling \ dimension \ is \ millimeter, \ converted \ inch \ dimensions \ are \ not \ necessarily \ exact.$

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