N-Channel Enhancement Mode Power MOSFET

DESCRIPTION

The HM3400DR uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

GENERAL FEATURES

- V_{DS} = 30V,I_D = 8È€A
 - $R_{DS(ON)} < 59m\Omega @ V_{GS}=2.5V$
 - $R_{DS(ON)} < 45m\Omega @ V_{GS}=4.5V$
 - $R_{DS(ON)} < 41m\Omega @ V_{GS} = 10V$
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

Application

- •PWM applications
- Load switch
- Power management

Fin Drain Bruce Drain Bruce DFN2X2-6L bottom view

Package Marking And Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
HM3400DR	HM3400DR	DFN2X2-6L	Ø180mm	8 mm	3000 units

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	30	V
Gate-Source Voltage	Vgs	±12	V
Drain Current-Continuous	I _D	₩₩₩₩₩₩₿È€	A
Drain Current-Pulsed (Note 1)	I _{DM}	30	A
Maximum Power Dissipation	P₀ /////////////////////////////		W
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	R _{0JA}	1.0	°C/W
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Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	30	33	-	V

				1		
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =30V, V_{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V_{GS} =±12V, V_{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	0.7	0.9	1.4	V
		V_{GS} =2.5V, I_{D} =4A	-	45	59	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =2.9A	-	34	45	mΩ
		V _{GS} =10V, I _D =2.9A	-	31	41	mΩ
Forward Transconductance	g fs	V_{DS} =5V,I _D =2.9A	10	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}	(-1E)(1)(-0)(-0)(-1)(-1)(-1)(-1)(-1)(-1)(-1)(-1)(-1)(-1	-	623	-	PF
Output Capacitance	Coss	V _{DS} =15V,V _{GS} =0V, F=1.0MHz	-	99	-	PF
Reverse Transfer Capacitance	C _{rss}		-	77	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	3.3	-	nS
Turn-on Rise Time	tr	V _{DD} =15V,I _D =2.9A	-	4.8	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10V, R_{GEN} =3 Ω	-	26	-	nS
Turn-Off Fall Time	t _f		-	4	-	nS
Total Gate Charge	Qg		-	9.5	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} =15V,I _D =ÌÈ€A,	-	1.5	-	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =4.5V	-	3	-	nC
Drain-Source Diode Characteristics	•		- I		1	
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =2.9A	-	0.75	1.2	V
Diode Forward Current (Note 2)	I _S		-	-	2.9	А

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, $t \le 10$ sec.

3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

4. Guaranteed by design, not subject to production

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

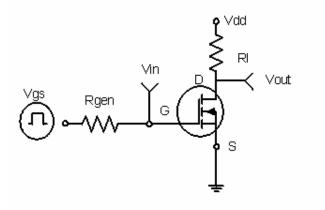


Figure 1:Switching Test Circuit

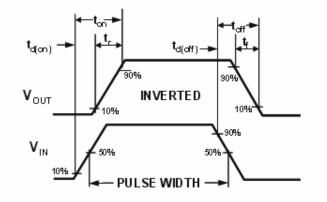
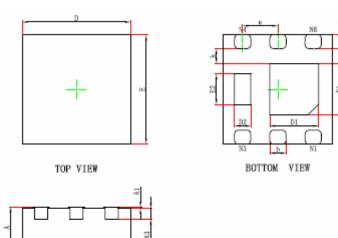


Figure 2:Switching Waveforms

DFN2X2-6L Package Information



SIDE VIEW

Symbol	Dimensions Ir	n Millimeters	Dimensions In Inches		
Cymbol	Min.	Max.	Min.	Max.	
A	0.700	0.800	0.028	0.031	
A1	0.000	0.050	0.000	0.002	
A3	0.203	REF.	0.008	REF.	
D	1.924	2.076	0.076	0.082	
E	1.924	2.076	0.076	0.082	
D1	0.800	1.000	0.031	0.039	
E1	0.850	1.050	0.033	0.041	
D2	0.200	0.400	0.008	0.016	
E2	0.460	0.660	0.018	0.026	
k	0.200MIN.		00MIN. 0.008MIN.		
b	0.250	0.350	0.010	0.014	
е	0.650TYP.		0.026TYP.		
L	0.174	0.326	0.007	0.013	

Notes

1. All dimensions are in millimeters.

2. Tolerance ±0.10mm (4 mil) unless otherwise specified

3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.

4. Dimension L is measured in gauge plane.

5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

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