

P-Channel Enhancement Mode Power MOSFET

DESCRIPTION

The HM3407B uses advanced trench technology to provide excellent $R_{DS(ON)}$, This device is suitable for use as a load switch or in PWM applications.

GENERAL FEATURES

• $V_{DS} = -30V, I_{D} = -4.1A$

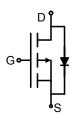
 $R_{DS(ON)}$ < 95m Ω @ V_{GS} =-4.5V

 $R_{DS(ON)}$ < 65m Ω @ V_{GS} =-10V

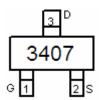
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

Application

- PWM applications
- Load switch
- Power management



Schematic diagram



Marking and pin Assignment



SOT-23 top view

Package Marking And Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
3407	HM3407B	SOT-23	Ø180mm	8 mm	3000 units

Absolute Maximum Ratings (TA=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	-30	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	-4.1	Α
Drain Current-Pulsed (Note 1)	I _{DM}	-20	Α
Maximum Power Dissipation	P _D	1.4	W
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	℃

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ heta JA}$	90	°C/W
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Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250μA	-30	-33	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-24V,V _{GS} =0V	-	-	-1	μA



Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)		1				I
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} ,I _D =-250μA	-1	-1.5	-3	V
Dunin Course On Chata Decistance	R _{DS(ON)}	V _{GS} =-10V, I _D =-4.1A	-	55	65	mΩ
Drain-Source On-State Resistance		V _{GS} =-4.5V, I _D =-4A	-	75	95	mΩ
Forward Transconductance	9 FS	V _{DS} =-5V,I _D =-4.1A	5.5	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}	V _{DS} =-15V,V _{GS} =0V, F=1.0MHz	-	700	-	PF
Output Capacitance	Coss		-	120	-	PF
Reverse Transfer Capacitance	C _{rss}	F-1.UIVITZ	-	75	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	9	-	nS
Turn-on Rise Time	t _r	V_{DD} =-15V,R _L =3.6 Ω	-	5	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-10V, R_{GEN} =3 Ω	-	28	-	nS
Turn-Off Fall Time	t _f		-	13.5	-	nS
Total Gate Charge	Qg		-	14	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} =-15V,I _D =-4A,V _{GS} =-10V	-	3.1	-	nC
Gate-Drain Charge	Q_{gd}		-	3.	-	nC
Drain-Source Diode Characteristics	•	•				
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-1A	-	-	-1.2	V

Notes:

- $\textbf{1.} \ \textbf{Repetitive Rating: Pulse width limited by maximum junction temperature.}$
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

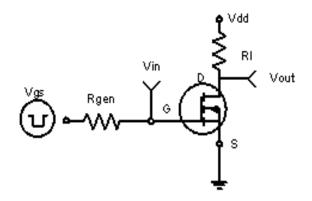
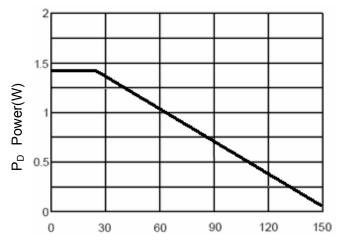


Figure 1:Switching Test Circuit



T_J-Junction Temperature(°C)

Figure 3 Power Dissipation

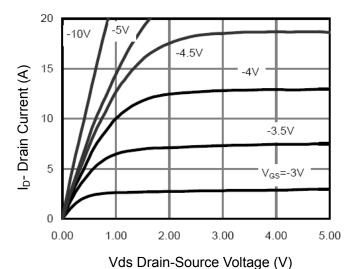


Figure 5 Output CHARACTERISTICS

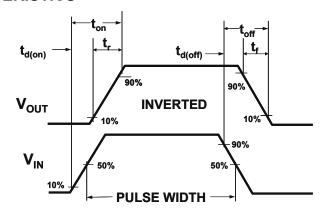
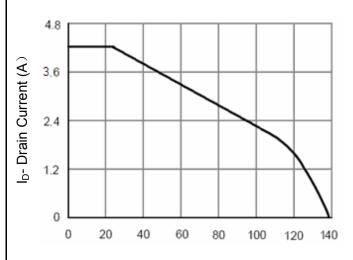


Figure 2:Switching Waveforms



 T_J -Junction Temperature($^{\circ}$ C)

Figure 4 Drain Current

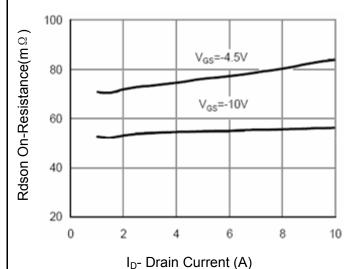


Figure 6 Drain-Source On-Resistance

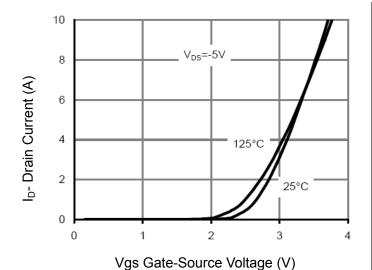
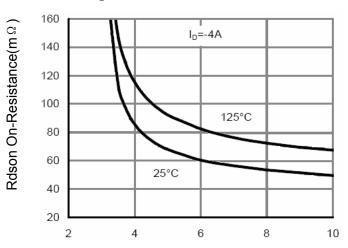


Figure 7 Transfer Characteristics



Vgs Gate-Source Voltage (V)

Figure 9 Rdson vs Vgs

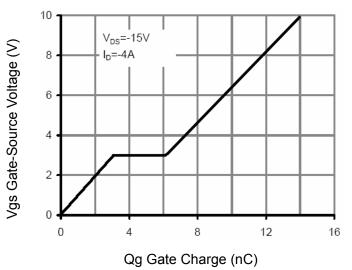


Figure 11 Gate Charge

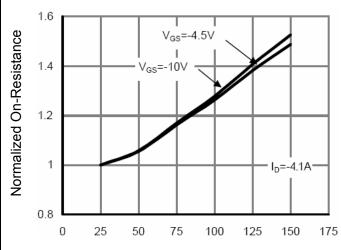


Figure 8 Drain-Source On-Resistance

T_J-Junction Temperature(°C)

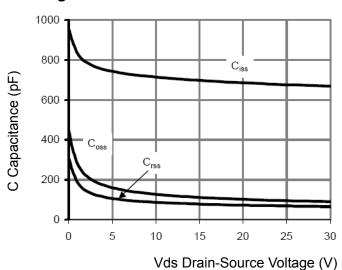


Figure 10 Capacitance vs Vds

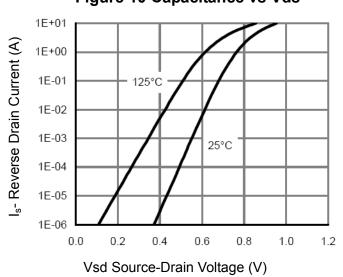


Figure 12 Source- Drain Diode Forward

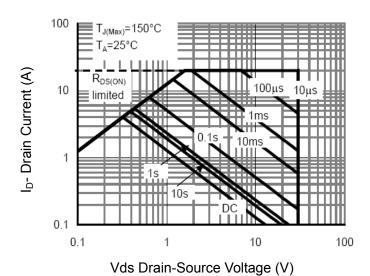


Figure 13 Safe Operation Area

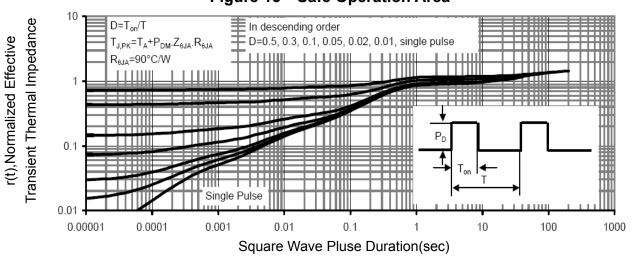
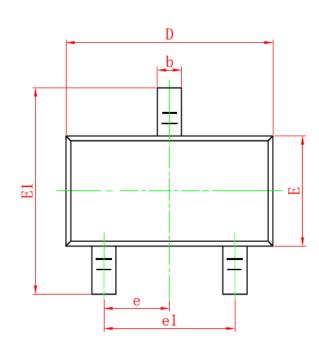


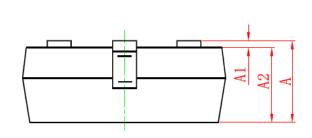
Figure 14 Normalized Maximum Transient Thermal Impedance

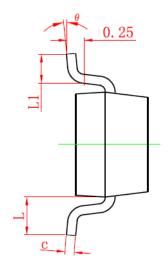


SOT-23 PACKAGE INFORMATION









Symbol	Dimensions in Millimeters			
Symbol	MIN.	MAX.		
Α	0.900	1.150		
A 1	0.000	0.100		
A2	0.900	1.050		
b	0.300	0.500		
С	0.080	0.150		
D	2.800	3.000		
E	1.200	1.400		
E1	2.250	2.550		
е	0.950TYP			
e1	1.800	2.000		
L	0.550REF			
L1	0.300	0.500		
θ	0°	8°		

NOTES

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- $5. \ Controlling \ dimension \ is \ millimeter, \ converted \ inch \ dimensions \ are \ not \ necessarily \ exact.$

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