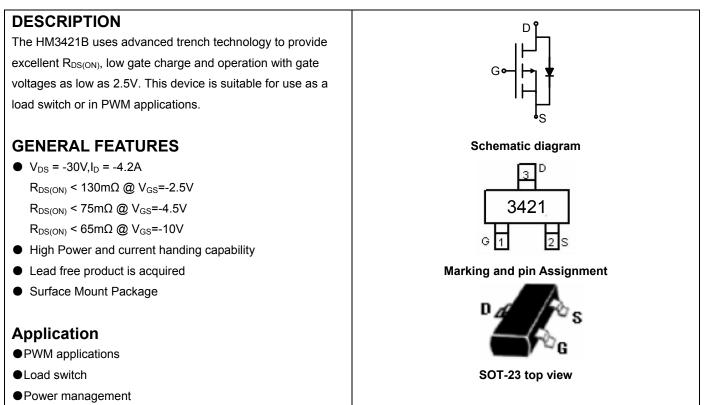
# P-Channel Enhancement Mode Power MOSFET



### Package Marking And Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
3421	HM3421B	SOT-23	Ø180mm	8 mm	3000 units

### Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	-30	V
Gate-Source Voltage	Vgs	±12	V
Drain Current-Continuous	Ι <sub>D</sub>	-4.2	A
Drain Current-Pulsed (Note 1)	I <sub>DM</sub>	-30	A
Maximum Power Dissipation	PD	1.2	W
Operating Junction and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55 To 150	°C

### **Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note 2)	R <sub>0JA</sub>	104	°C <b>/W</b>
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### Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250µA	-30		-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-24V,V <sub>GS</sub> =0V	-	-	-1	μA

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# HM3421B

Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}$ =±12V, $V_{DS}$ =0V	-	-	±100	nA
On Characteristics (Note 3)	·	·				
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =-250µA	-0.7	-1	-1.3	V
		V <sub>GS</sub> =-10V, I <sub>D</sub> =-4.2A	-	50	55	mΩ
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-4A	-	64	72	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-1A		95	120	mΩ
Forward Transconductance	<b>g</b> fs	V <sub>DS</sub> =-5V,I <sub>D</sub> =-4.2A	-	10	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>	V <sub>DS</sub> =-15V.V <sub>GS</sub> =0V.	-	950	-	PF
Output Capacitance	C <sub>oss</sub>	F=1.0MHz	-	115	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F = 1.0101112	-	75	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	7	-	nS
Turn-on Rise Time	tr	V <sub>DD</sub> =-15V,I <sub>D</sub> =-3.2A	-	3	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =-10V,R <sub>GEN</sub> =6 $\Omega$	-	30	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	12	-	nS
Total Gate Charge	Qg		-	9.5	-	nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =-15V,I <sub>D</sub> =-4A,V <sub>GS</sub> =-4.5V	-	2	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	3	-	nC
Drain-Source Diode Characteristics	·					
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =-1A	-	-	-1.2	V

### Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

**2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.

**3.** Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2%.

4. Guaranteed by design, not subject to production

# **TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS**

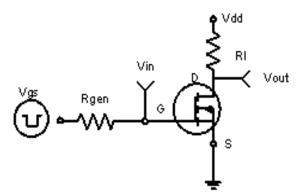
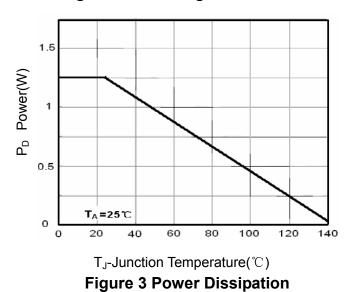
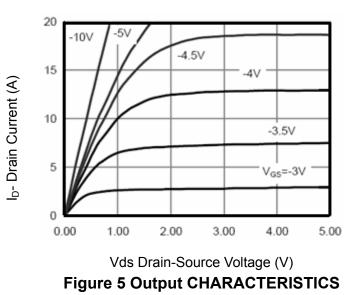
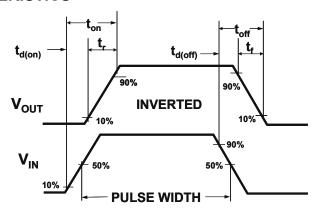


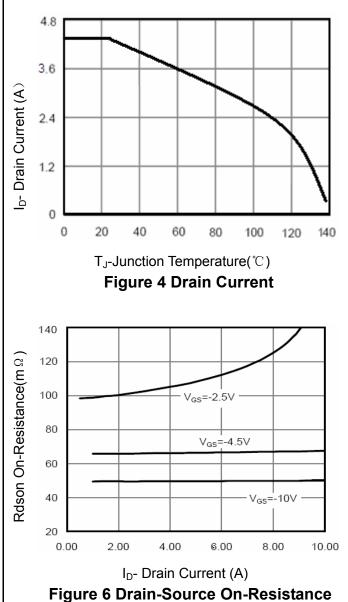
Figure 1:Switching Test Circuit



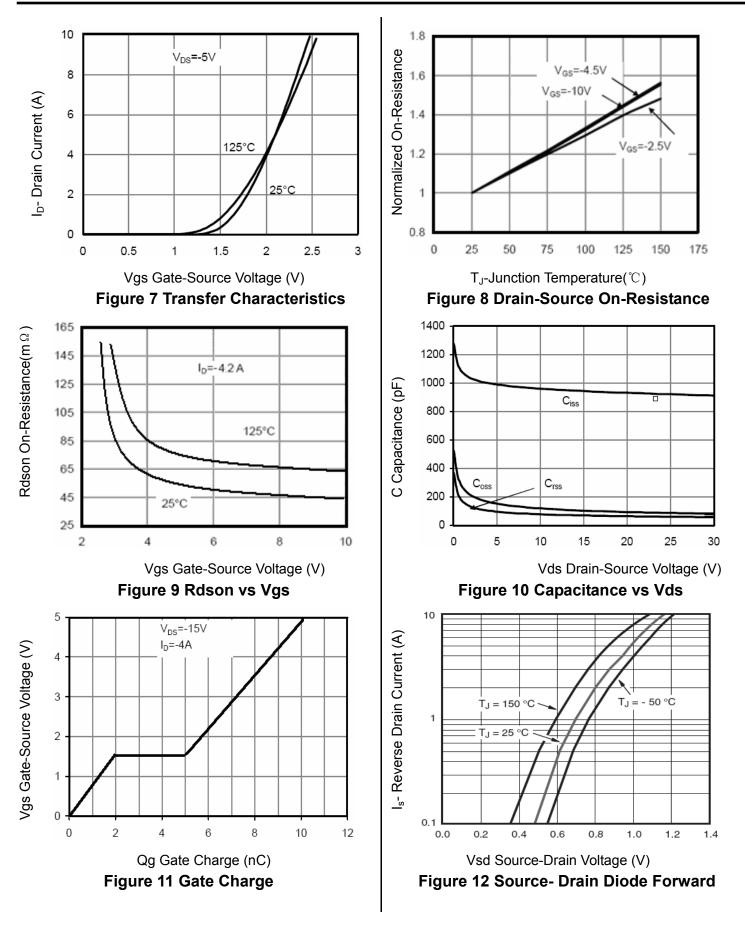








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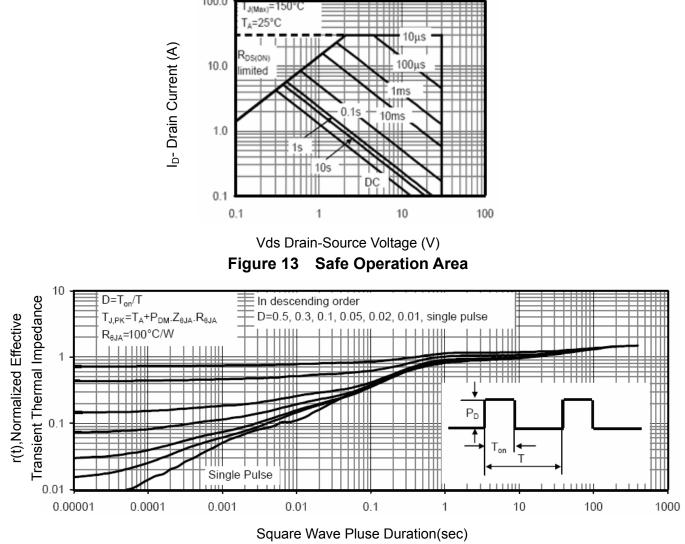
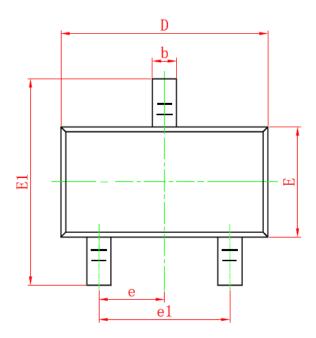
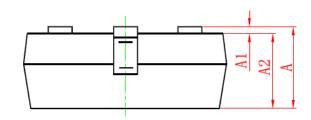


Figure 14 Normalized Maximum Transient Thermal Impedance

# **SOT-23 PACKAGE INFORMATION**





**Dimensions in Millimeters (UNIT:mm)** 

	<u>0. 25</u>
<u>[1]</u>	
	,

Symbol	Dimensions in Millimeters				
	MIN.	MAX.			
Α	0.900	1.150			
A1	0.000	0.100			
A2	0.900	1.050			
b	0.300	0.500			
С	0.080	0.150			
D	2.800	3.000			
Е	1.200	1.400			
E1	2.250	2.550			
е	0.950TYP				
e1	1.800	2.000			
L	0.550REF				
L1	0.300	0.500			
θ	<b>0°</b>	8°			

# NOTES

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- 5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

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