



# Silicon N-Channel Power MOSFET



## HPA700R320PC-G

### General Description:

HPA700R320PC-G, the silicon N-channel Enhanced MOSFETs, is obtained by the super junction technology which reduces the conduction loss, improve switching performance and enhance the avalanche energy. The transistor can be used in various power switching circuit for system miniaturization and higher efficiency. The package type is TO-220F, which accords with the RoHS standard.

### Features:

- | Fast Switching
- | Low Gate Charge
- | Low Reverse transfer capacitances
- | 100% Single Pulse avalanche energy Test
- | Halogen Free

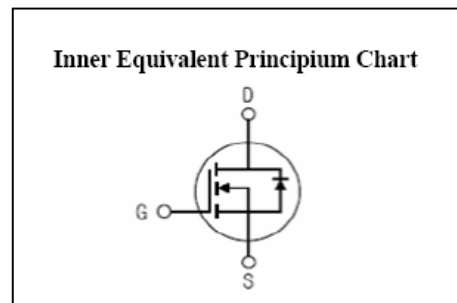
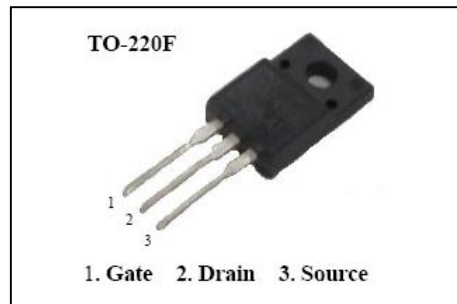
### Applications:

Power switch circuit of adaptor ,PC and LED power.

**Absolute** ( $T_j = 25^\circ\text{C}$  unless otherwise specified):

Symbol	Parameter	Rating	Units
$V_{DSS}$	Drain-to-Source Voltage( $V_{GS}=0V$ )	700	V
$I_D^{a1}$	Continuous Drain Current $T_C = 25^\circ\text{C}$	15	A
$I_{DM}^{a2}$	Pulsed Drain Current $T_C = 25^\circ\text{C}$	45	A
$V_{GSS}$	Gate-to-Source Voltage	$\pm 30$	V
$E_{AS}^{a3}$	Single Pulse Avalanche Energy	245	mJ
$dv/dt^{a4}$	Peak Diode Recovery $dv/dt$	15	V/ns
$dv/dt$	MOSFET $dv/dt$ ruggedness	50	V/ns
$di_f/dt$	Maximum diode communication speed	500	A/us
$P_D$	Power Dissipation( $T_C=25^\circ\text{C}$ )	29	W
$T_j, T_{stg}$	Operating and Storage Temperature Range	$-55 \dots +150$	$^\circ\text{C}$
$T_L$	Maximum Temperature for Soldering	300	$^\circ\text{C}$

$V_{DSS}(T_{jmax})$	750	V
$I_D$	15	A
$P_D(T_C=25^\circ\text{C})$	29	W
$R_{DS(ON)Typ}$	0.28	$\Omega$
$E_{oss@400V}$	2.4	$\mu\text{J}$



**Electrical Characteristics** (T<sub>j</sub>= 25°C unless otherwise specified):

<b>OFF Characteristics</b>						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
V <sub>DSS</sub>	Drain to Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	700	--	--	V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	Bvdss Temperature Coefficient	ID=250uA	--	0.67	--	V/°C
I <sub>DSS</sub>	Drain to Source Leakage Current	V <sub>DS</sub> = 700V, V <sub>GS</sub> = 0V, T <sub>j</sub> = 25°C	--	--	1	μA
		V <sub>DS</sub> =560V, V <sub>GS</sub> = 0V, T <sub>j</sub> = 125°C	--	--	100	
I <sub>GSS(F)</sub>	Gate to Source Forward Leakage	V <sub>GS</sub> =+30V V <sub>DS</sub> = 0V,	--	--	100	nA
I <sub>GSS(R)</sub>	Gate to Source Reverse Leakage	V <sub>GS</sub> =-30V V <sub>DS</sub> = 0V,	--	--	-100	nA

<b>ON Characteristics</b>						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
R <sub>DS(ON)</sub>	Drain-to-Source On-Resistance	V <sub>GS</sub> =10V,I <sub>D</sub> =5.2A	--	0.28	0.32	Ω
V <sub>GS(TH)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	2.0	--	4.0	V
Pulse width tp ≤ 300μs, δ ≤ 2%						

<b>Dynamic Characteristics</b>						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
R <sub>g</sub>	Gate resistance	f = 1.0MHz	--	6.4	--	Ω
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V V <sub>DS</sub> = 100V f = 1.0MHz	--	1048	--	pF
C <sub>oss</sub>	Output Capacitance		--	47	--	
C <sub>rss</sub>	Reverse Transfer Capacitance		--	2.2	--	

<b>Resistive Switching Characteristics</b>						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
t <sub>d(ON)</sub>	Turn-on Delay Time	I <sub>D</sub> =6.5A V <sub>DD</sub> = 400V V <sub>GS</sub> = 13V R <sub>G</sub> =3.4 Ω	--	13	--	ns
t <sub>r</sub>	Rise Time		--	5.7	--	
t <sub>d(OFF)</sub>	Turn-Off Delay Time		--	52	--	
t <sub>f</sub>	Fall Time		--	8.8	--	
Q <sub>g</sub>	Total Gate Charge	I <sub>D</sub> =6.5A V <sub>DD</sub> =400V V <sub>GS</sub> = 10V	--	27	--	nC
Q <sub>gs</sub>	Gate to Source Charge		--	4.7	--	
Q <sub>gd</sub>	Gate to Drain (“Miller”)Charge		--	12	--	
V <sub>plateau</sub>	Gate Plateau Voltage		--	4.8	--	



Source-Drain Diode Characteristics						
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
$I_S$	Continuous Source Current (Body Diode)	$T_c=25^\circ\text{C}$	--	--	15	A
$I_{SM}$	Maximum Pulsed Current (Body Diode)		--	--	45	A
$V_{SD}$	Diode Forward Voltage	$I_S=7.5\text{A}, V_{GS}=0\text{V}$	--	--	1.2	V
$T_{rr}$	Reverse Recovery Time	$I_S=6.5\text{A}, T_j = 25^\circ\text{C}$ $dI_F/dt=100\text{A}/\mu\text{s},$ $V_{GS}=0\text{V}$	--	248	--	ns
$Q_{rr}$	Reverse Recovery Charge		--	2700	--	nC
$I_{rrm}$	Reverse Recovery Current		--	22	--	A

Thermal Resistance

Symbol	Parameter	Max.	Units
$R_{\theta JC}$	Junction-to-Case	4.29	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Junction-to-Ambient	62.5	$^\circ\text{C}/\text{W}$

- a<sup>1</sup>: Limited by Tjmax Maximum duty cycle D=0.75
- a<sup>2</sup>: Repetitive rating; pulse width limited by maximum junction temperature
- a<sup>3</sup>: L=20.0mH, Rg=25 Ω, Vdd=50V, Start Tj=25°C
- a<sup>4</sup>: Identical low side and high side switch with identical Rg

## Characteristics Curve:

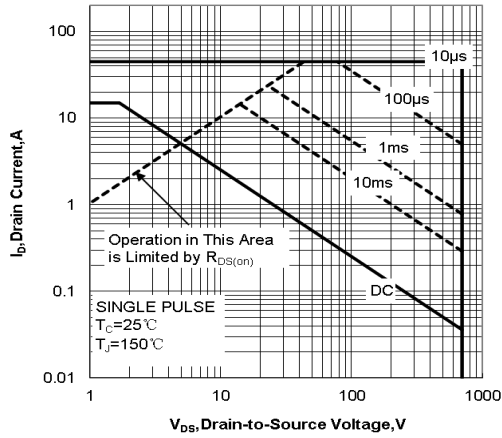


Figure.1 Maximum Forward Bias Safe Operating Area

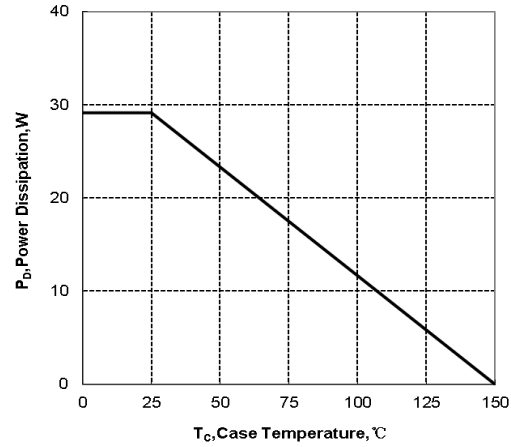


Figure.2 Maximum Power Dissipation vs Case Temperature

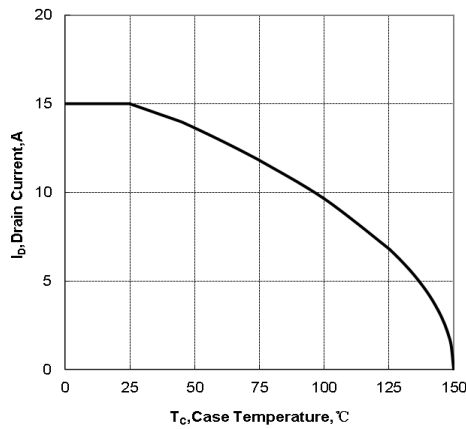


Figure.3 Maximum Continuous Drain Current vs Case Temperature

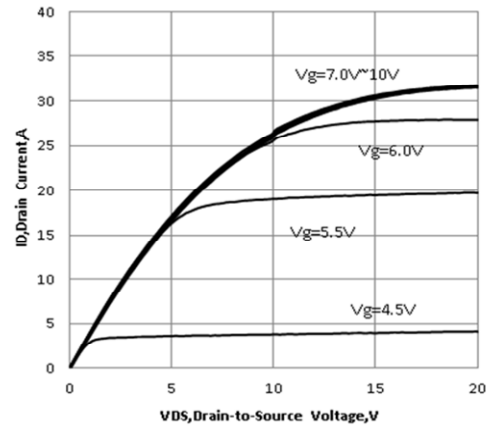


Figure.4 Typical Output Characteristics

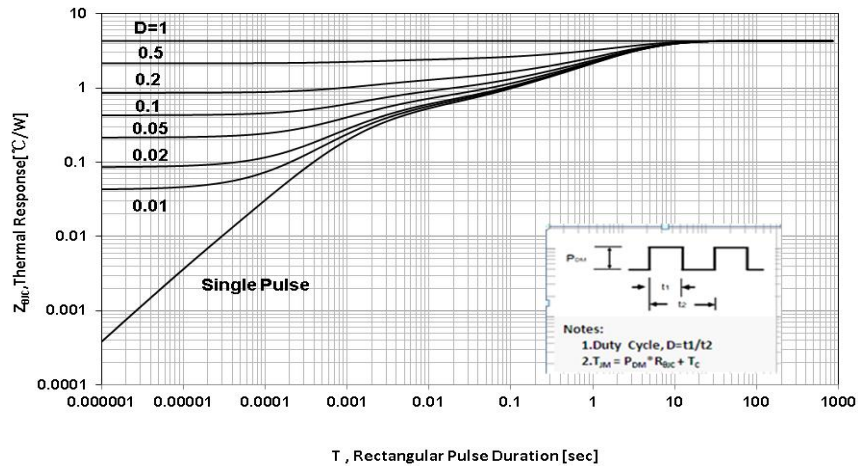


Figure.5 Maximum Effective Thermal Impedance, Junction to Case

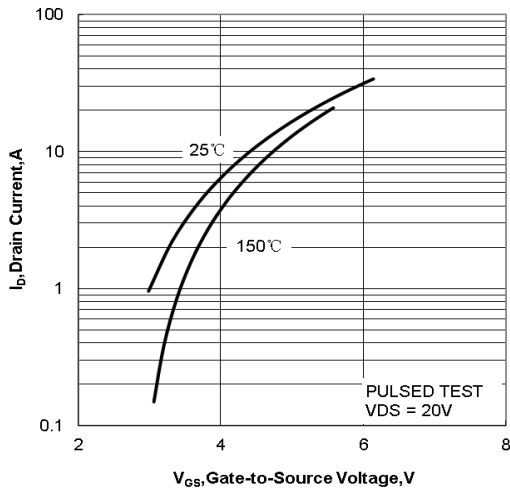


Figure.6 Typical Transfer Characteristics

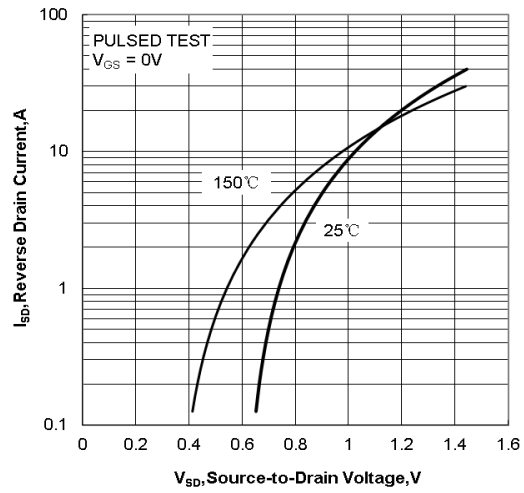


Figure.7 Typical Body Diode Transfer Characteristics

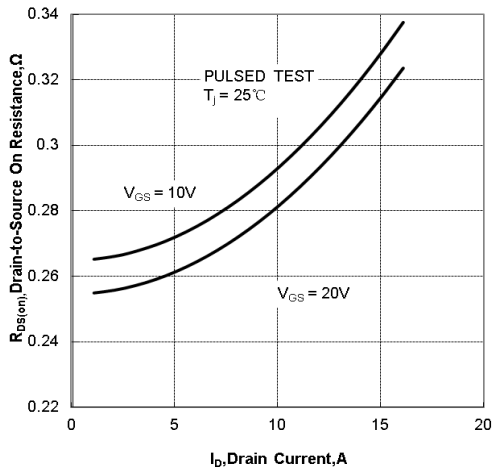


Figure.8 Typical Drain to Source ON Resistance vs Drain Current

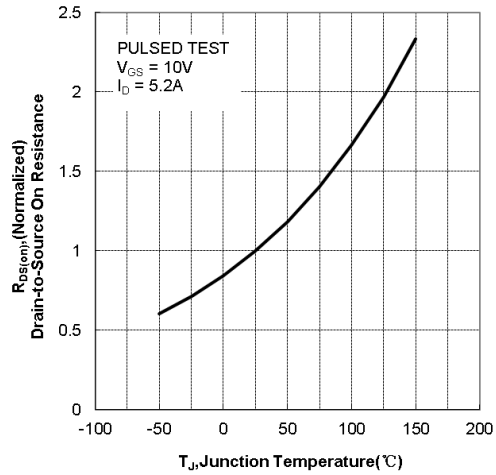


Figure.9 Typical Drain to Source on Resistance vs Junction Temperature

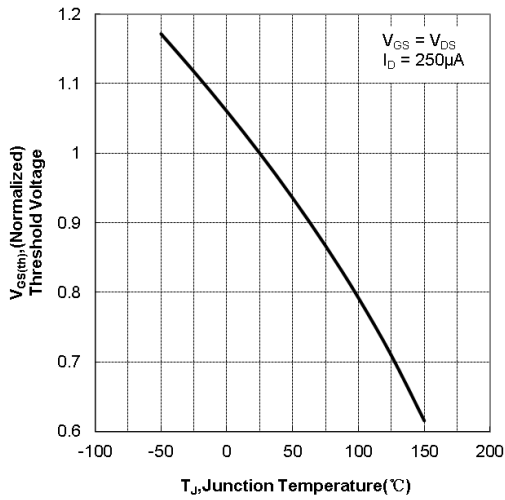


Figure.10 Typical Theshold Voltage vs Junction Temperatur

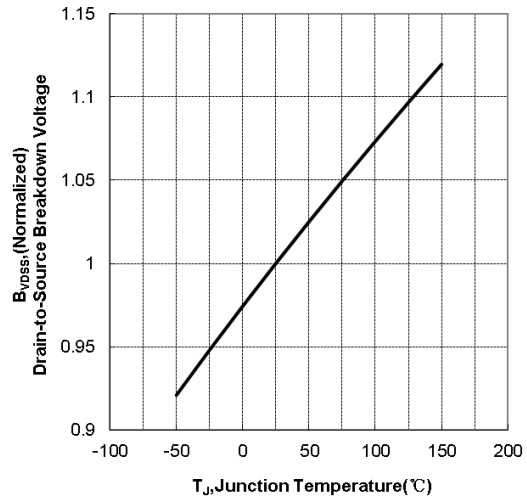


Figure 11 Typical Breakdown Voltage vs Junction Temperature

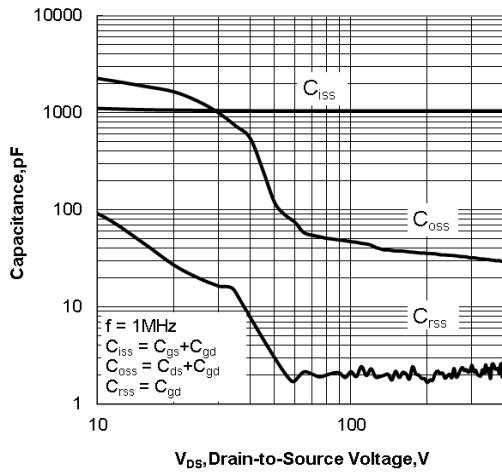


Figure.12 Typical Capacitance vs Drain to Source Voltage

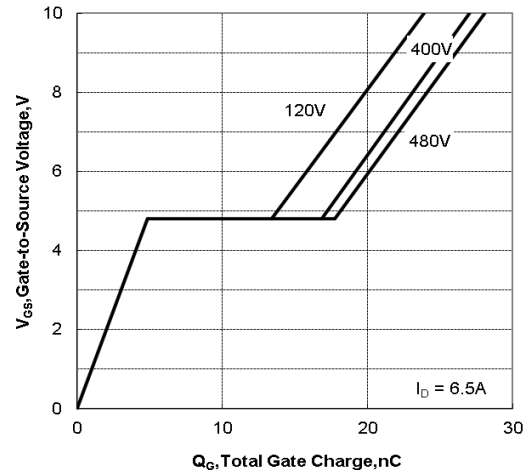


Figure.13 Typical Gate Charge vs Gate to Source Voltage

**Test Circuit and Waveform**



Figure 14. Gate Charge Test Circuit



Figure 15. Gate Charge Waveforms

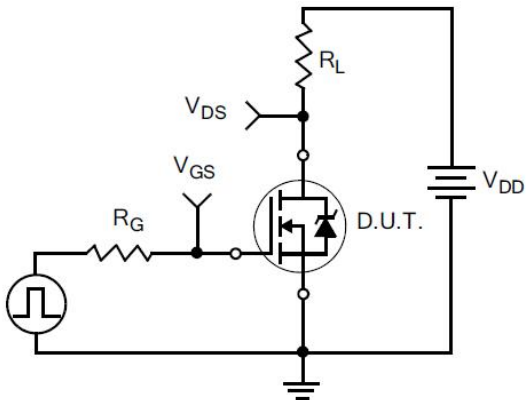


Figure 16. Resistive Switching Test Circuit

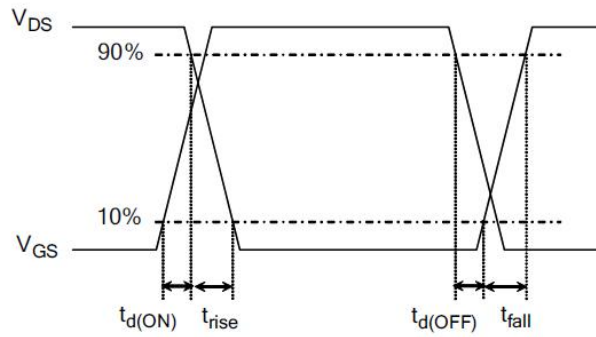


Figure 17. Resistive Switching Waveforms

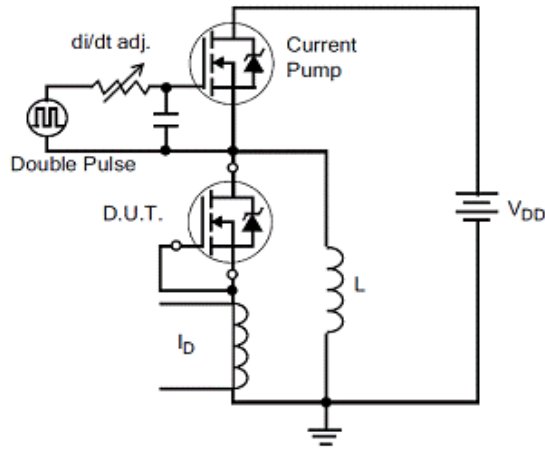


Figure 18. Diode Reverse Recovery Test Circuit

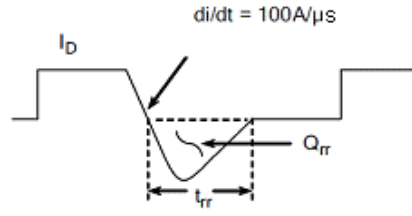


Figure 19. Diode Reverse Recovery Waveform

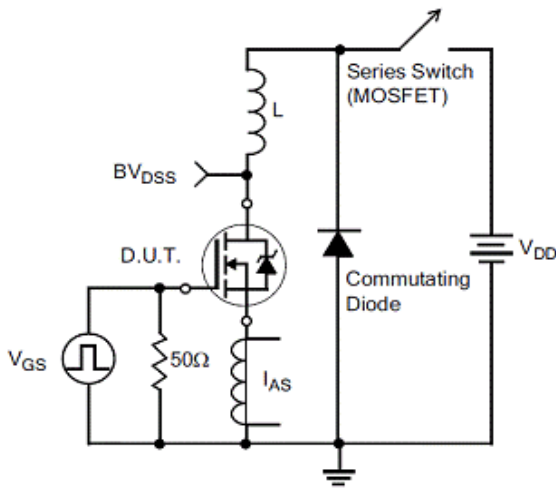


Figure20.Unclamped Inductive Switching Test Circuit

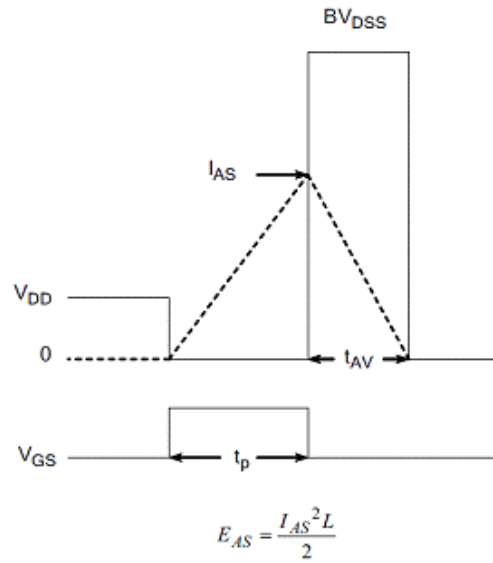
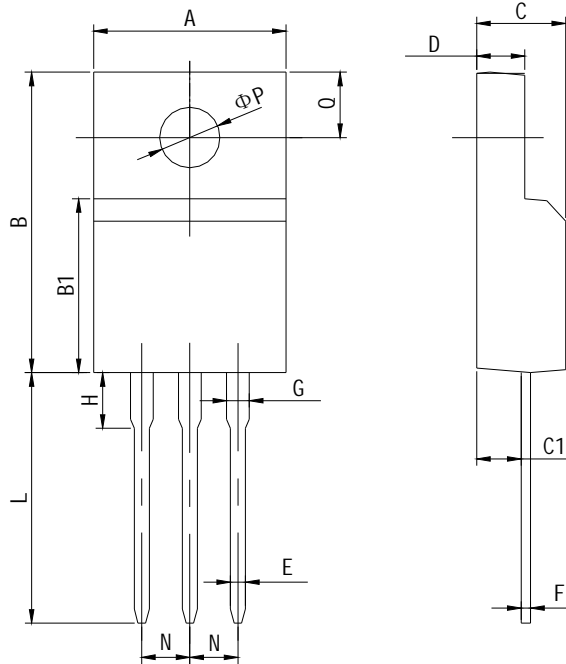


Figure21.Unclamped Inductive Switching Waveform



Package Information



Items	Values(mm)	
	MIN	MAX
A	9.60	10.4
B	15.4	16.2
B1	8.90	9.50
C	4.30	4.90
C1	2.10	3.00
D	2.40	3.00
E	0.60	1.00
F	0.30	0.60
G	1.12	1.42
H	3.40	3.80
L*	12.0	14.0
N	2.34	2.74
Q	3.15	3.55
$\phi P$	2.90	3.30

\*adjustable

TO-220F Package

**The name and content of poisonous and harmful material in products**

	Hazardous Substance									
	Pb	Hg	Cd	Cr(VI)	PBB	PBDE	DBP	DEHP	DBP	BBP
Limit	≤0.1%	≤0.1%	≤ 0.01%	≤0.1%	≤0.1%	≤0.1%	≤0.1%	≤0.1%	≤0.1%	≤0.1%
Lead Frame	○	○	○	○	○	○	○	○	○	○
Molding	○	○	○	○	○	○	○	○	○	○
Chip	○	○	○	○	○	○	○	○	○	○
Wire Bonding	○	○	○	○	○	○	○	○	○	○
Solder	×	○	○	○	○	○	○	○	○	○
Note	○: Means the hazardous material is under the criterion of 2011/65/EU. ×: Means the hazardous material exceeds the criterion of 2011/65/EU. The plumbum element of solder exist in products presently, but within the allowed range of Eurogroup's RoHS.									

**Warnings**

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. It is suggested to be used under 80 percent of the maximum ratings of the device.
2. When installing the heat sink, please pay attention to the torsional moment and the smoothness of the heatsink.
3. VDMOSFET is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. This publication is made by HuaJing Microelectronics and subject to regular change without notice.

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