

**Radiation Hardened
CMOS Dual DPST Analog Switch**

Intersil's Satellite Applications Flow™ (SAF) devices are fully tested and guaranteed to 100kRAD Total Dose. These QML Class T devices are processed to a standard flow intended to meet the cost and shorter lead-time needs of large volume satellite manufacturers, while maintaining a high level of reliability.

The HS-302RH-T analog switch is a monolithic device fabricated using Radiation Hardened CMOS technology and the Intersil dielectric isolation process for latch-up free operation. Improved total dose hardness is obtained by layout (thin oxide tabs extending to a channel stop) and processing (hardened gate oxide). These switches offer low-resistance switching performance for analog voltages up to the supply rails. "ON" resistance is low and stays reasonably constant over the full range of operating voltage and current. "ON" resistance also stays reasonably constant when exposed to radiation, being typically 30Ω pre-rad and 35Ω post 100kRAD(Si). This device provide break-before-make switching.

Specifications

Specifications for Rad Hard QML devices are controlled by the Defense Supply Center in Columbus (DSCC). The SMD numbers listed below must be used when ordering.

Detailed Electrical Specifications for the HS-302RH-T are contained in SMD 5962-95812. A "hot-link" is provided from our website for downloading.

www.intersil.com/spacedefense/newsafclasst.asp

Intersil's Quality Management Plan (QM Plan), listing all Class T screening operations, is also available on our website.

www.intersil.com/spacedefense/newsafclasst.asp

Ordering Information

ORDERING NUMBER	PART NUMBER	TEMP. RANGE (°C)
5962R9581201TCC	HS1-302RH-T	-55 to 125
5962R9581201TXC	HS9-302RH-T	-55 to 125

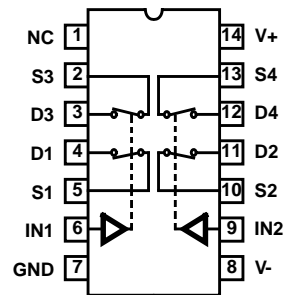
NOTE: **Minimum order quantity for -T is 150 units through distribution, or 450 units direct.**

Features

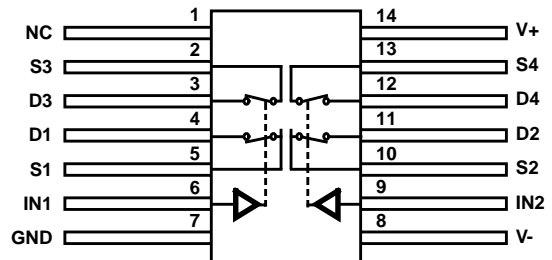
- QML Class T, Per MIL-PRF-38535
- Radiation Performance
 - Gamma Dose (γ) 1 x 10⁵ RAD(Si)
- No Latch-Up, Dielectrically Isolated Device Islands
- Pin for Pin Compatible with Intersil HI-302 Series Analog Switches
- Analog Signal Range 15V
- Low Leakage 100nA (Max, Post Rad)
- Low R_{ON} 60Ω (Max, Post Rad)
- Low Operating Power 100μA (Max, Post Rad)

Pinouts

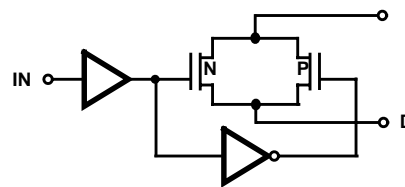
**HS1-302RH-T (SBDIP), CDIP2-T14
TOP VIEW**



**HS9-302RH-T (FLATPACK), CDFP3-F14
TOP VIEW**



Functional Diagram



TRUTH TABLE

LOGIC	ALL SWITCHES
0	OFF
1	ON

HS-302RH-T

Die Characteristics

DIE DIMENSIONS:

(2130 μm x 1930 μm x 279 μm \pm 25.4 μm)
84 x 76 x 11mils \pm 1mil

METALLIZATION:

Type: Al
Thickness: 12.5k \AA \pm 2k \AA

SUBSTRATE POTENTIAL:

Unbiased (DI)

BACKSIDE FINISH:

Gold

PASSIVATION:

Type: Silox (SiO_2)
Thickness: 8k \AA \pm 1k \AA

WORST CASE CURRENT DENSITY:

$< 2.0\text{e}5 \text{ A/cm}^2$

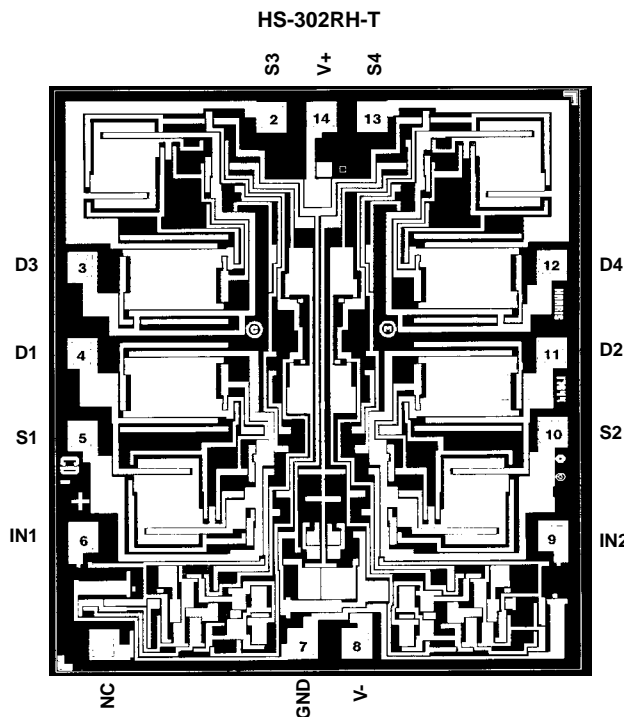
TRANSISTOR COUNT:

76

PROCESS:

Metal Gate CMOS, Dielectric Isolation

Metallization Mask Layout



All Intersil semiconductor products are manufactured, assembled and tested under **ISO9000** quality systems certification.

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