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|----------------|--|----------|--------|
| Document Title | HSD013BPF1-D00 Preliminary Specification | Page No. | 1 / 34 |
| Document No.   | DC110-002194                             | Revision | 1.3    |



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|----------------|--|----------|--------|
| Document Title | HSD013BPF1-D00 Preliminary Specification | Page No. | 2 / 34 |
| Document No.   | DC110-002194                             | Revision | 1.3    |

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|      |              | R         | ecord of Revisions   |
|------|--------------|-----------|--|
| Rev. | Date         | Sub-Model | Description of change  |
| 1.0  | May.17,2016  | D00       | Preliminary specification was first issued.  |
| 1.1  | May. 27,2016 |           | Update 6.3 Chip Size(P20)  |
| 1.2  | Jun.,13,2016 |           | Update 6.2 Chip Cut Mark Position  |
| 1.3  | Sep.,02,2016 |           | Update 6.3 Chip Size(P20)<br>Update 6.2 Chip Cut Mark Position<br>Update 3.0 ELECTRICAL SPECIFICATIONS |
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| Document Title | HSD013BPF1-D00 Preliminary Specification | Page No. | 4 / 34 |
|----------------|--|----------|--------|
| Document No.   | DC110-002194                             | Revision | 1.3    |

#### 1.0 GENERAL DESCRIPTION

## 1.1 Introduction

HannStar Display model HSD013BPF1-D is a color active matrix thin film transistor (TFT) liquid crystal display without polarizer. This model is composed of amorphous silicon TFT as a switching device. It is a transmissive type display operating in the normally black mode.

This TFT LCD has a 1.28-inch diagonally measured active display area with 240 x 240 dot (240 horizontal by 240 vertical pixel) resolution. Each pixel is divided into Red, Green, Blue dots which are arranged in vertical stripes.

## 1.2 Applications

#### 1.3 **General Information**

|   | Red, Green, Blue dots which are a | irranged in vertical stripes.      |        |
|---|-----------------------------------|------------------------------------|--------|
| 2 | Applications                      |                                    |        |
|   | Smart Watch applications          |                                    | Va     |
| 3 | General Information               | ·····                              |        |
|   | Item                              | Specification                      | Unit   |
|   | Glass Dimension                   | 35.1(H) x 37.83(V) x 0.4(T) (Typ.) | mm     |
|   | Display Area                      | Ф32.4                              | mm     |
|   | Number of Pixel                   | 240 RGB(H) x 240(V)                | pixels |
|   | Pixel Pitch                       | 0.135H) x 0.135(V)                 | mm     |
|   | Pixel Arrangement                 | RGB Vertical stripe                |        |
|   | Display Mode                      | Normally black                     |        |
|   | Display Color                     | 262K                               |        |
|   |                                   |                                    |        |



| Document Title | HSD013BPF1-D00 Preliminary Specification | Page No. | 5 / 34 |
|----------------|--|----------|--------|
| Document No.   | DC110-002194                             | Revision | 1.3    |

#### 2.0 **ABSOLUTE MAXIMUM RATINGS**

(The following are maximum values which, if exceeded, may cause operation or damage to the unit.)

| Item                       | Symbol          | Min. | Max. | Unit   | Note          |
|----------------------------|-----------------|------|------|--------|---------------|
| LC Operating Voltage       | VOP             |      | 5.2  | V      | *1,*2         |
| Operating Temperature      | T <sub>OP</sub> | -20  | 70   | °C     |               |
| Storage Temperature        | T <sub>ST</sub> | -30  | 80   | D°     | , <u>\$</u> 0 |
| Operating Ambient Humidity | H <sub>OP</sub> | 10   | *4   | RH     | *3            |
| Storage Humidity           | H <sub>ST</sub> | 10   | *4   | RH     | *3            |
| Note:<br>*1. At 25+5℃      |                 |      |      | Clouic |               |

### Note:

- \*1. At 25±5℃
- \*2. Due to the characteristics of LC Material, the Liquid Crystal driving voltage varies with environmental temperature. 10
- \*3. Non-condensation.
- \*4. Temp.≤ 60°C,90%RH Max.
  - Temp. >  $60^{\circ}$ C, Absolute humidity shall be less than 90%RH.

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| Document Title | HSD013BPF1-D00 Preliminary Specification | Page No. | 6 / 34 |
|----------------|--|----------|--------|
| Document No.   | DC110-002194                             | Revision | 1.3    |

#### 3.0 **ELECTRICAL SPECIFICATIONS**

| Item                          | Symbol      | Min. | Тур. | Max. | Unit | Note  |
|-------------------------------|-------------|------|------|------|------|-------|
| TFT Gate ON Voltage           | VGH         |      | 15   |      | V    | *1,*2 |
| TFT Gate OFF Voltage          | VGL         |      | -12  |      | V    |       |
| TFT Common Voltage            | Vcom        | -2   |      | 0    | V    |       |
| Data (RGB signal) Voltage     | Vsig        | -5.1 |      | 5.1  | V    |       |
| Note:                         |             |      |      |      |      |       |
| *1. VGH is TFT Gate operati   | ing Voltage |      |      |      |      | 010   |
| *2. VGL is TFT Gate operation | ng Voltage. |      |      |      |      | A     |

## Note:

\*2. VGL is TFT Gate operating Voltage.

The storage structure of this model is C<sub>ST</sub>(Storage on Common) y Cro y Cro Supply & Purchase

\*3. Vcom must be adjusted to optimize display quality Cross talk, Contrast Ratio and etc.

\*4. Frame rate suggestion value: 105 Hz

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|----------------|--|----------|--------|
| Document Title | HSD013BPF1-D00 Preliminary Specification | Page No. | 7 / 34 |
| Document No.   | DC110-002194                             | Revision | 1.3    |

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| Pad No. | Pad Name         | Pad No. | Pad Name |   |
|---------|------------------|---------|----------|---|
| 1       | GND              | 31      | TE       |   |
| 2       | NULL(metal open) | 32      | DB[17]   |   |
| 3       | VCOM             | 33      | DB[16]   |   |
| 4       | VCOM             | 34      | DB[15]   |   |
| 5       | VCOM             | 35      | DB[14]   |   |
| 6       | VCOM/VCOM_R      | 36      | DB[13]   |   |
| 7       | VCOM/VCOM_R      | 37      | DB[12]   |   |
| 8       | VCOM/VCOM_R      | 38      | DB[11]   |   |
| 9       | AGND             | 39      | DB[10]   |   |
| 10      | AGND             | 40      | DB[9]    |   |
| 11      | AGND             | 41      | DB[8]    |   |
| 12      | VGH/VGL(*Note1)  | 42      | DB[7]    |   |
| 13      | VGH/VGL(*Note1)  | 43      | DB[6]    | _ |
| 14      | VGH/VGL(*Note1)  | 44      | DB[5]    |   |
| 15      | VPP              | 45      | DB[4]    |   |
| 16      | VPP              | 46      | DB[3]    |   |
| 17      | VCC/VCORE        | 47      | DB[2]    |   |
| 18      | VCC/VCORE        | 48      | DB[1]    |   |
| 19      | VCC/VCORE        | 49      | DB[0]    |   |
| 20      | VCC/VCORE        | 50      | SDA      |   |
| 21      | VCC/VCORE        | 51      | DOTCLK   |   |
| 22      | VCC/VCORE        | 52      | ENABLE   |   |
| 23      | VDDI/IOVCC       | 53      | HSYNC    |   |
| 24      | VDDI/IOVCC       | 54      | VSYNC    |   |
| 25      | VDDI/IOVCC       | 55      | RDX      |   |
| 26      | VDDI/IOVCC       | 56      | WRX      |   |
| 27      | VDDI_LED/DUMMY   | 57      | DCX      |   |
| 28      | LED_EN           | 58      | CSX      |   |
| 29      | LED_PWM          | 59      | RESX     |   |
| 30      | SDO              | 60      | IM[0]    |   |

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|----------------|--|----------|--------|
| Document Title | HSD013BPF1-D00 Preliminary Specification | Page No. | 8 / 34 |
| Document No.   | DC110-002194                             | Revision | 1.3    |
|                |  |          |        |
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|         | FPC OLB                 |             | FPC OLB   |
|---------|-------------------------|-------------|---|
| Pad No. | Pad Name                | Pad No.     | Pad Name  |
| 61      | IM[1]                   | 91          | DUMMY/VGH(*Note2)   |
| 62      | IM[2]                   | 92          | DUMMY/VGH(*Note2)   |
| 63      | IM[3]                   | 93          | VCOM/VCOM_L   |
| 64      | EXTC                    | 94          | VCOM/VCOM_L   |
| 65      | DGND                    | 95          | VCOM/VCOM_L   |
| 66      | DGND                    | 96          | DUMMY/VGH(*Note2) DUMMY/VGH(*Note2) VCOM/VCOM_L VCOM/VCOM_L VCOM/VCOM_L VCOM VCOM VCOM NULL(metal open) GND |
| 67      | DGND                    | 97          | VCOM  |
| 68      | AGND                    | 98          | VCOM  |
| 69      | AGND                    | 99          | NULL(metal open)  |
| 70      | AGND                    | 100         | GND   |
| 71      | AGND/CGND               |             |   |
| 72      | AGND/CGND               |             | 60  |
| 73      | AGND/CGND               |             | wa.   |
| 74      | AGND/VGS                |             |   |
| 75      | AGND/VGS                |             |   |
| 76      | VDD/VCI                 |             |   |
| 77      | VDD/VCI                 |             | 8   |
| 78      | VDD/VCI                 |             |   |
| 79      | VDD/VCI                 |             |   |
| 80      | VAP/VREG1OUT            |             |   |
| 81      | VAP/VREG1OUT            | 5           |   |
| 82      | VAN/VREG2OUT            |             |   |
| 83      | AVCL/DDVDL              |             |   |
| 84      | AVCL/DDVDL              |             |   |
| 85      | AVCL/DDVDL              |             |   |
| 86      | AVDD/DDVDH              |             |   |
| 87      | AVDD/DDVDH              |             |   |
| 88      | AVDD/DDVDH              |             |   |
| 89      | VGL                     |             |   |
| 90      | VGL                     |             |   |
| *       | Note1: ST7789V & ST7789 | H2 pin12~14 | → VGH   |
|         | ILI9340L pin12~14       | 4 →VGL      |   |
|         | p                       | . ,         |   |
|         |                         |             |   |
| *       | Note2: ST7789V & ST7789 | -           | iz → DUMMY  |
|         | ILI9340L pin91 &        | 92 →VGH     |   |



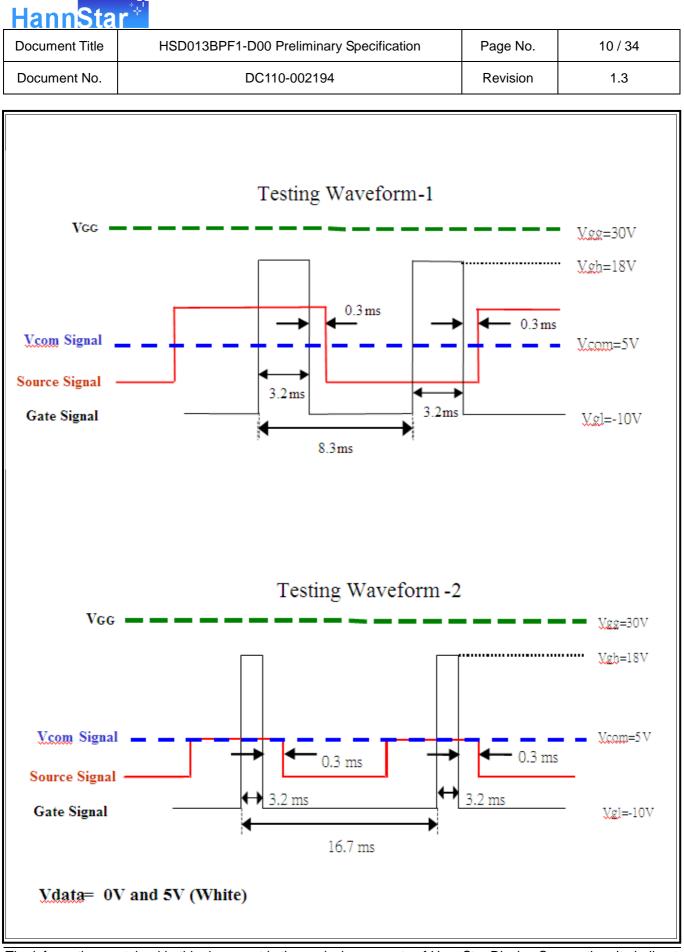
| Document Title | HSD013BPF1-D00 Preliminary Specification | Page No. | 9 / 34 |
|----------------|--|----------|--------|
| Document No.   | DC110-002194                             | Revision | 1.3    |

# 3.2 TFT Design Rules

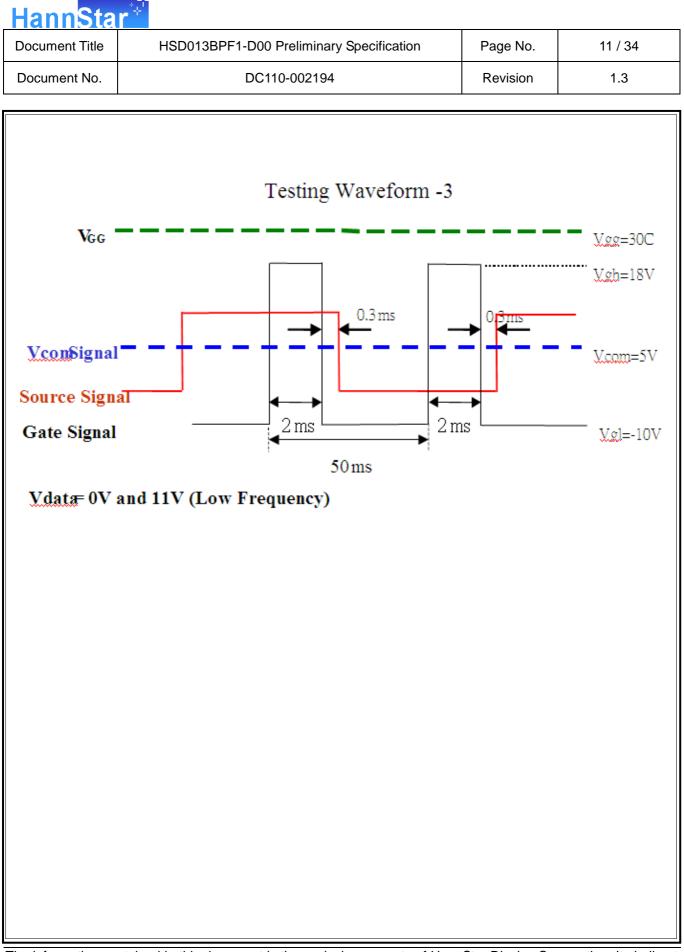
| lte                   | m              | Specification               | unit  |
|-----------------------|----------------|-----------------------------|-------|
|                       | Chip size      | 15155 x 698                 | um    |
| ST7789H2              | Pad number     | 1278                        |       |
|                       | Pin assignment | Based on the ST7789H2 Spec. |       |
| 3.3 Cell Test Light O | n Waveform     |                             | ation |

## 3.3 Cell Test Light On Waveform

| Display | Vdata  | Pattern |
|---------|--|---------|
| Black   | TSR = 0V  and  11V $TSG = 0V  and  11V$ $TSB = 0V  and  11V$ |         |
| Gray    | TSR = 0V  and  6V $TSG = 0V  and  6V$ $TSB = 0V  and  6V$    |         |
| Red     | TSR =5V and $6V$<br>TSG = 0V and $11V$<br>TSB = 0V and $11V$ |         |
| Green   | TSR = 0V  and  11V $TSG = 5V  and  6V$ $TSB = 0V  and  11V$  |         |
| Blue    | TSR = 0V  and  11V $TSG = 0V  and  11V$ $TSB = 5V  and  6V$  |         |



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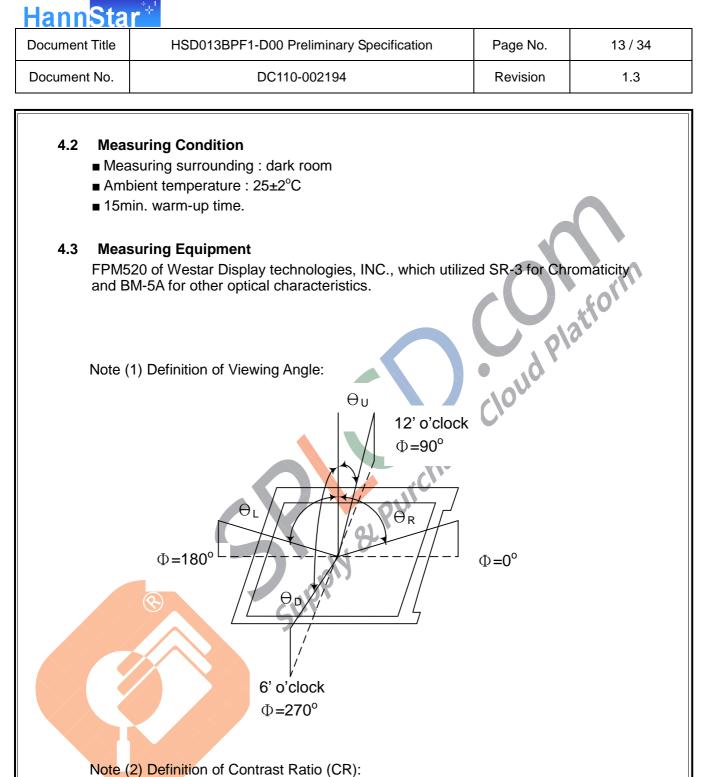
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| Document Title | HSD013BPF1-D00 Preliminary Specification | Page No. | 12 / 34 |
| Document No.   | DC110-002194                             | Revision | 1.3     |

# 4.0 OPTICAL CHARACTERISTICS

# 4.1 Optical Specification

| Item                              |               | Symbol                         | Condition                  | Min. | Тур.    | Max. | Unit | Note                     |
|-----------------------------------|---------------|--------------------------------|----------------------------|------|---------|------|------|--------------------------|
| Transmittance<br>(with Polarizer) |               | T(%)                           | —                          |      | 4.7     | _    | %    | Normal POL               |
| Transmittance<br>(without Polari  | zer)          | T(%)                           | —                          |      | 15.32   | _    | %    | m                        |
| Contrast Ratio                    |               | CR                             | Θ=0                        | 640  | 800     |      |      | (1)(2)                   |
| Response                          | Time          | T <sub>R</sub> +T <sub>F</sub> | Normal<br>viewing<br>angle | _    | 30      | 35   | msec | (1)(3)                   |
| Color Gamut                       |               | S(%)                           |                            |      | 60      | _    | %    |                          |
|                                   | \A/I=`(       | W <sub>x</sub>                 |                            | TBD  | (0.308) | TBD  | -    |                          |
|                                   | White         | Wy                             |                            | TBD  | (0.331) | TBD  |      |                          |
|                                   | 6             | Rx                             |                            | TBD  | 0.641   | TBD  |      |                          |
| Color                             | Red           | Ry                             |                            | TBD  | 0.337   | TBD  |      | (1)(4)                   |
| Chromaticity<br>(CIE1931)         | Green<br>Blue | Gx                             |                            | TBD  | 0.274   | TBD  |      | CF glass                 |
| · · · ·                           |               | Gy                             |                            | TBD  | 0.560   | TBD  |      |                          |
|                                   |               | Bx                             |                            | TBD  | 0.141   | TBD  |      |                          |
|                                   |               | By                             | 101                        | TBD  | 0.113   | TBD  |      |                          |
|                                   | Hor.          | ΘL                             | 50                         | _    | 80      | —    |      | Viewing Angle            |
|                                   |               | Θ <sub>R</sub>                 | CD: 10                     | _    | 80      | —    |      | base on using<br>Normal  |
| Viewing Angle                     |               | Θυ                             | CR>10                      | _    | 80      | —    |      | Polarizer ,<br>Reference |
|                                   | Ver.          | ΘD                             |                            | —    | 80      | _    |      | Only                     |
| Optima View Direction             |               |                                | ALL                        |      |         |      |      | (5)                      |
|                                   |               |                                |                            |      |         |      |      |                          |

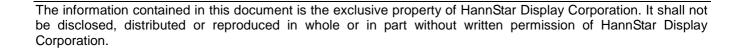


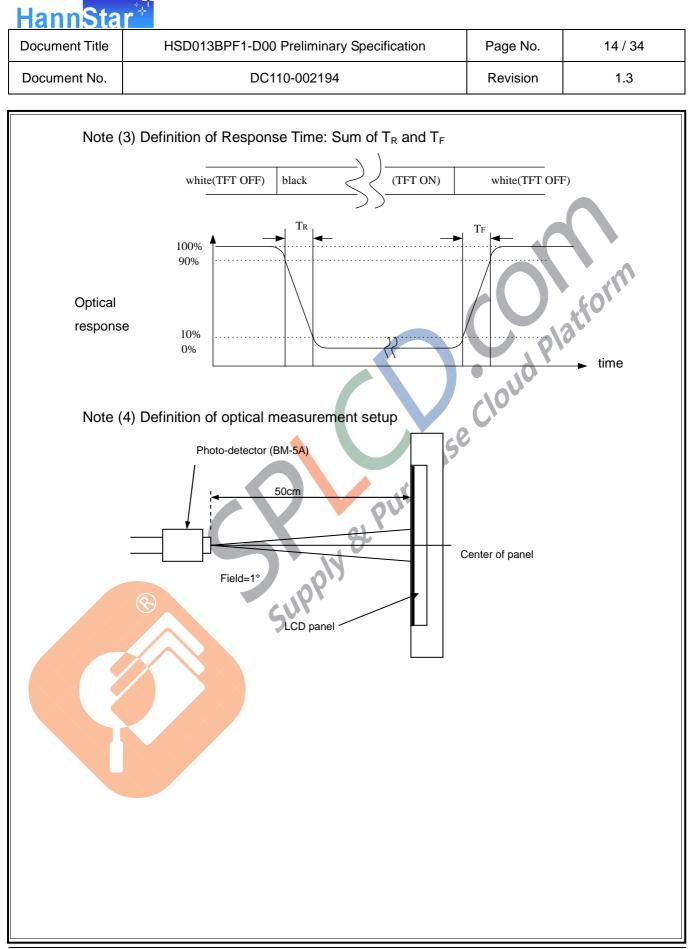
measured at the center point of panel

Luminance with all pixels white

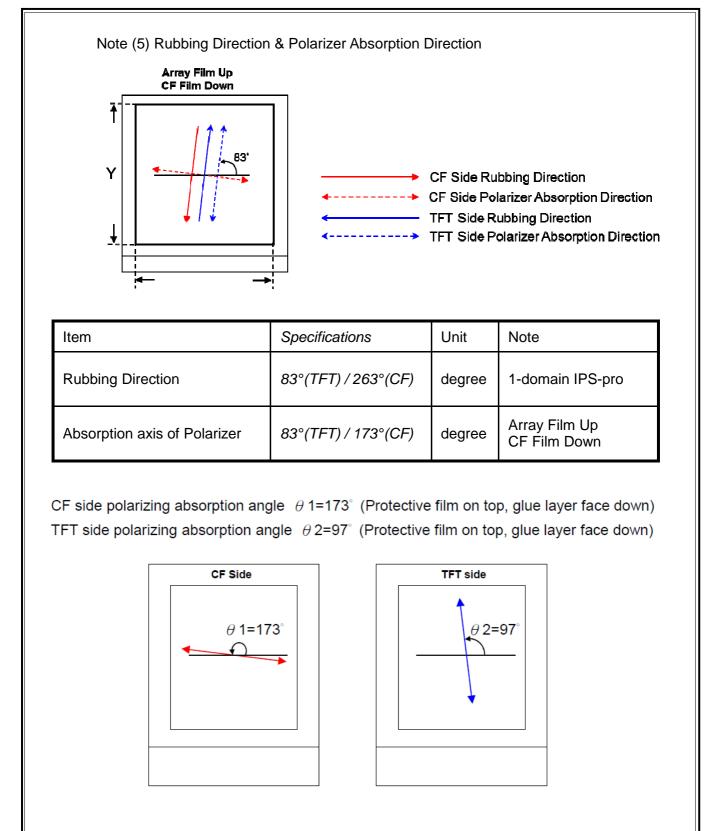
CR = --

Luminance with all pixels black

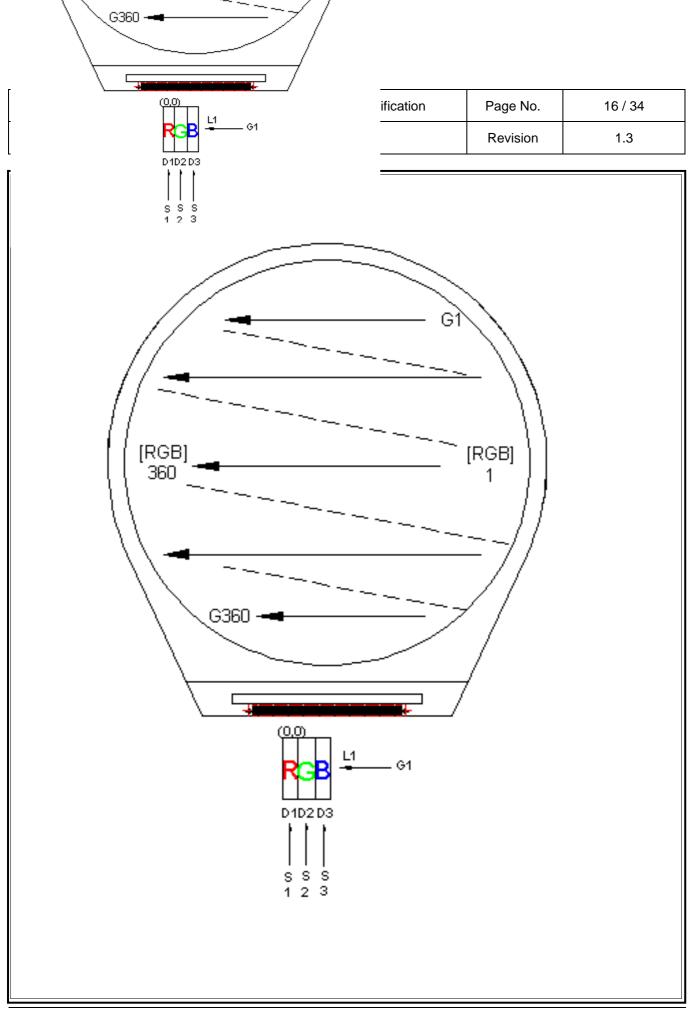




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| Document No.    | DC110-002194                             | Revision | 1.3     |



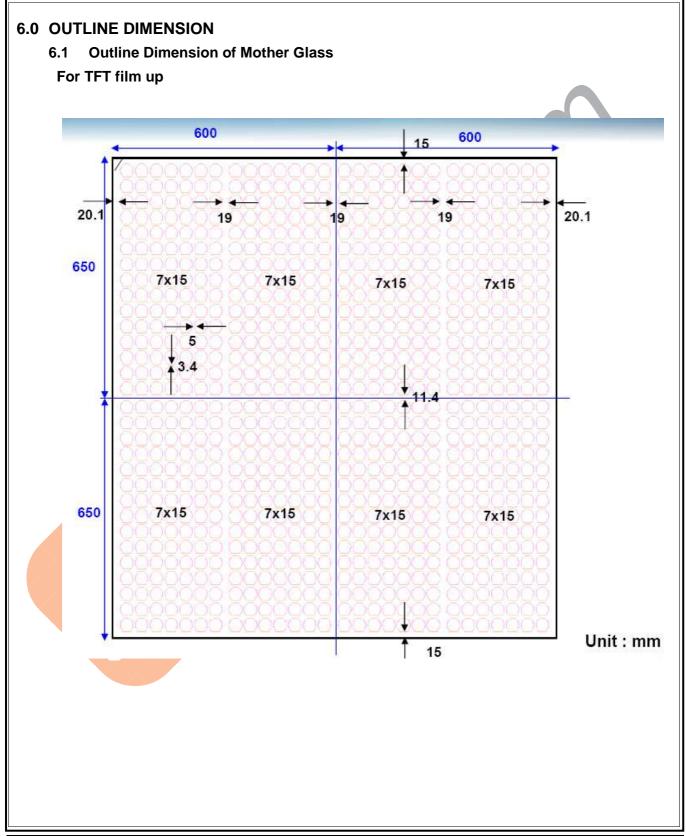
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| Document Title | HSD013BPF1-D00 Preliminary Specification | Page No. | 17 / 34 |
|----------------|--|----------|---------|
| Document No.   | DC110-002194                             | Revision | 1.3     |



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| Docum         | ent Title  | HSD013BPF1-D00 Preliminary Specification Page No. |     |         |         |    |         | lo.   | 18     | / 34  |          |       |
| Docum         | ient No.   |   |     | DC110   | -002194 |    |         | R     | evisio | on    | 1        | .3    |
|               | 6.2 Chip Cut Mark Position<br>Sub A/B/C/D Array Film Up<br>600 600 |   |     |         |         |    |         |       |        |       |          |       |
|               |  |   |     | 2       | * *     | 3  |         |       | 4      | *     | $ \cap $ |       |
| 650           | 000  | Su  | b A | A       |         |    | Sut     | bВ    |        |       | tion     | 0     |
|               | ↓ ★ 5  |   |     | 6       | * *     | 7  |         |       | 8      | *     |          |       |
|               | <b>†</b> * 9   |   |     | 10      | * * 1   | 1  |         |       | 12     | *     |          |       |
| 650           | 200  | Su  | b ( | C       |         |    | Sub     | D     |        |       |          |       |
|               | ★13  |   |     | 14      | * * 1   | 5  |         |       | 16     | *     |          |       |
| Array film up |  |   |     |         |         |    |         |       |        |       |          |       |
| No            | X  | Y   | No  | Х       | Y       | No | Х       | Y     | No     | х     |          | Y     |
|               | -580000  | 636250  | 5   | -580000 | 4450    | 9  | -580000 | -4450 | 13     | -5800 |          | 36250 |
| 2             | -9400  | 636250  | 6   | -9400   | 4450    | 10 | -9400   | -4450 | 14     | -940  | 00 -63   | 36250 |

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unit : um

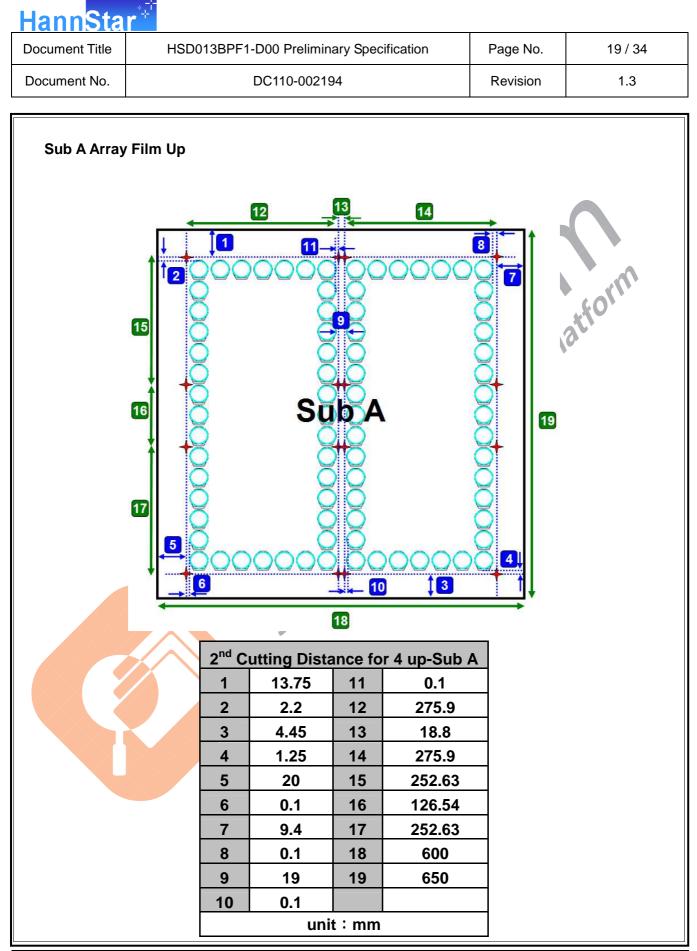
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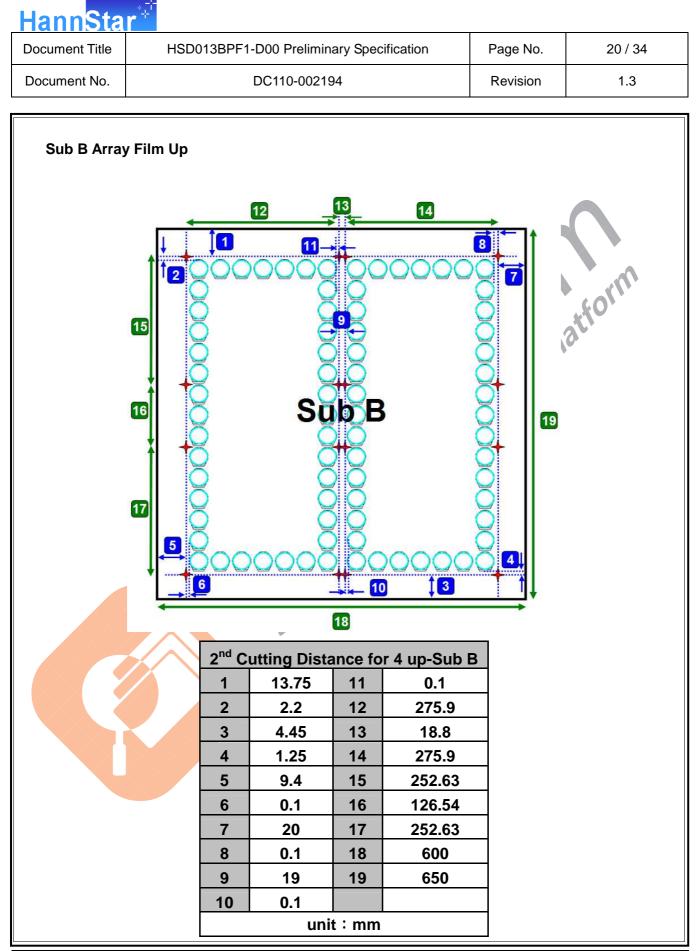
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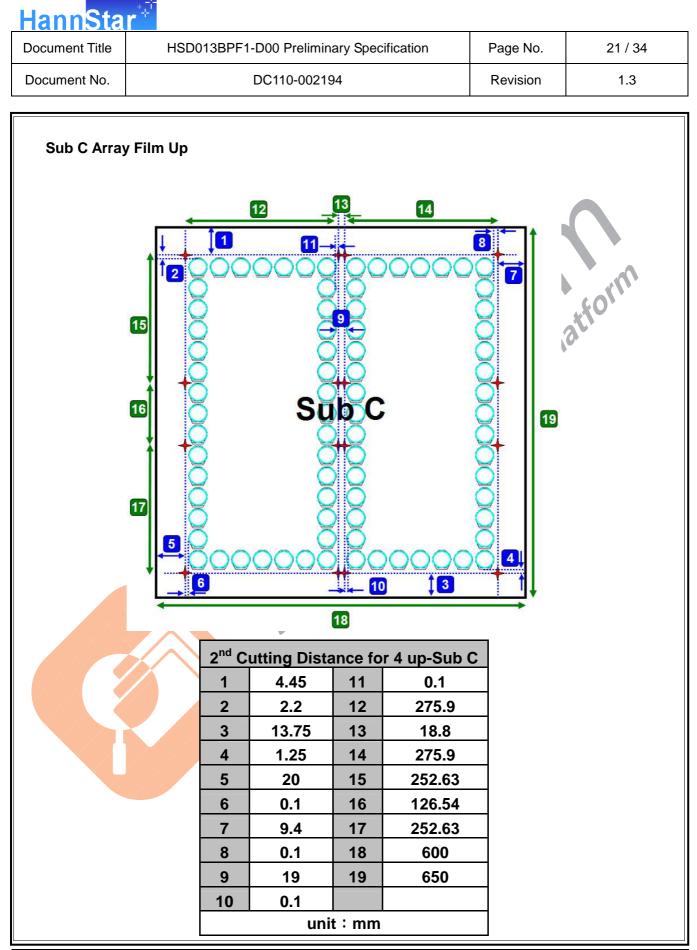
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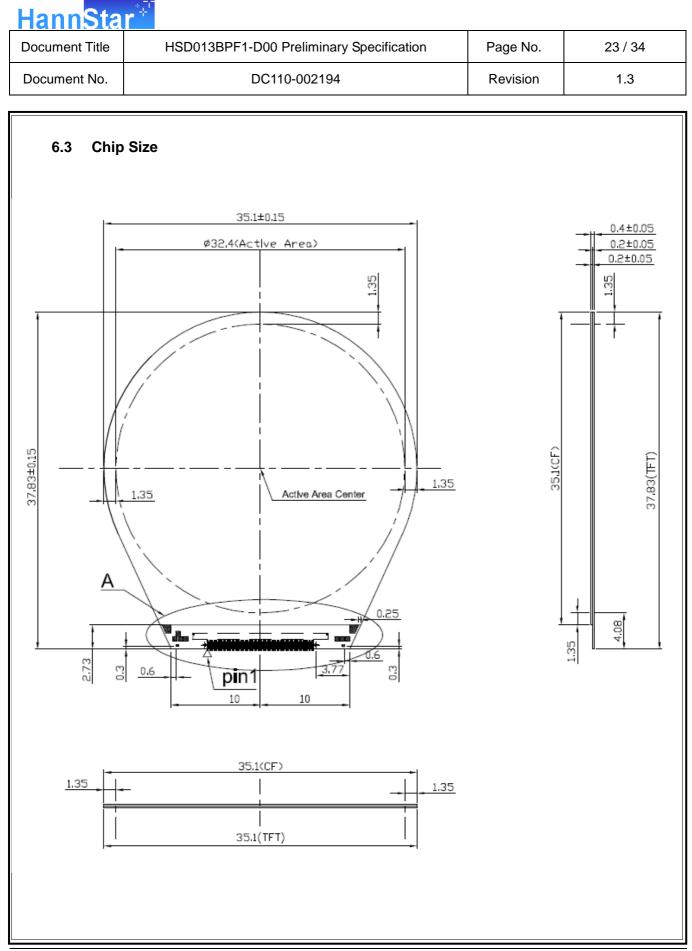


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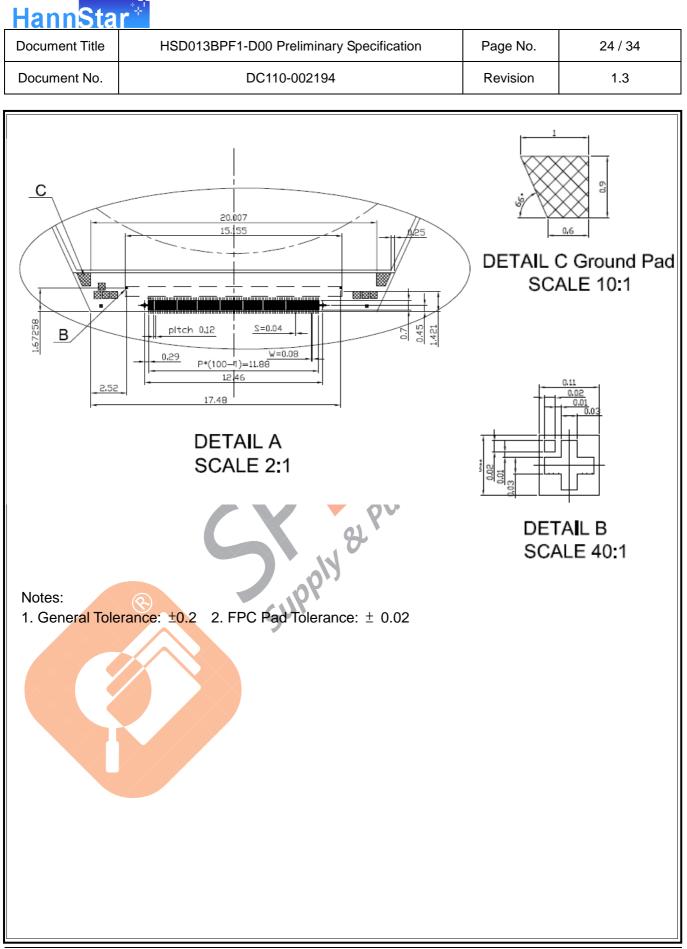
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| Document Title | HSD013BPF1 | -D00 Prelimin     | ary Spec | ification     | Page No. | 22 / 34 |
| Document No.   |            | DC110-00219       | 94       |               | Revision | 1.3     |
| Sub D Array    | Film Up    |                   |          |               | 8        |         |
|                |            | 0000              |          |               |          |         |
|                |            |                   |          | r 4 up-Sub D  |          |         |
|                | 1 2        | 4.45              | 11       | 0.1           | -        |         |
|                | 3          | 2.2<br>13.75      | 12<br>13 | 275.9<br>18.8 | -        |         |
|                | 4          | 1.25              | 13       | 275.9         | -        |         |
|                | 5          | 9.4               | 14       | 252.63        | -        |         |
|                | 6          | <u>9.4</u><br>0.1 | 16       | 126.54        | -        |         |
|                | 7          | 20                | 17       | 252.63        | -        |         |
|                | 8          | 0.1               | 18       | 600           | -        |         |
|                | 9          | 19                | 19       | 650           | -        |         |
|                | 10         | 0.1               |          |               | -        |         |
|                |            |                   | t∶mm     |               |          |         |

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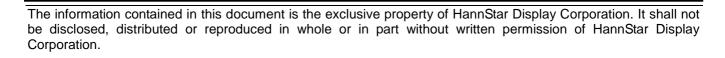
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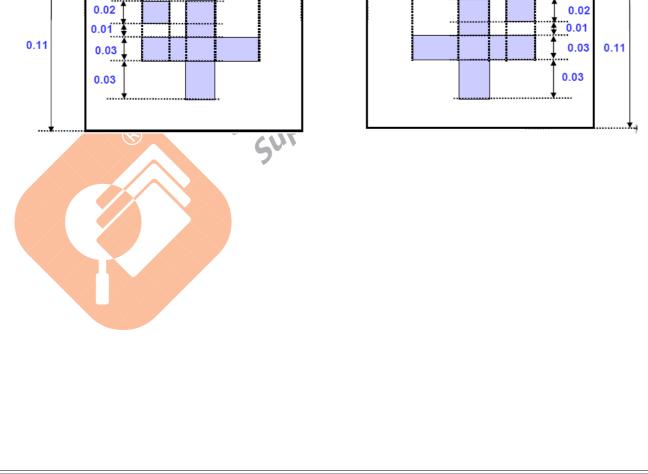
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| Document Title | HSD013BPF                             | 1-D00 Preliminary Specification | Page No. | 25 / 34                    |
| Document No.   |                                       | DC110-002194                    | Revision | 1.3                        |
| 6.4 Driv       | er IC Block Positi<br>1.67258<br>2.52 | 15.155                          | C .      | <br>0.698<br><br>Unit : mm |

0.11

0.03

0.03





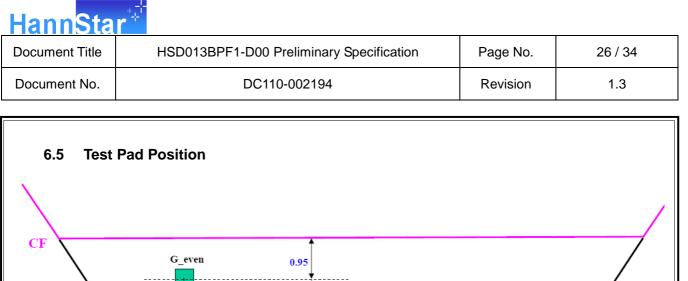


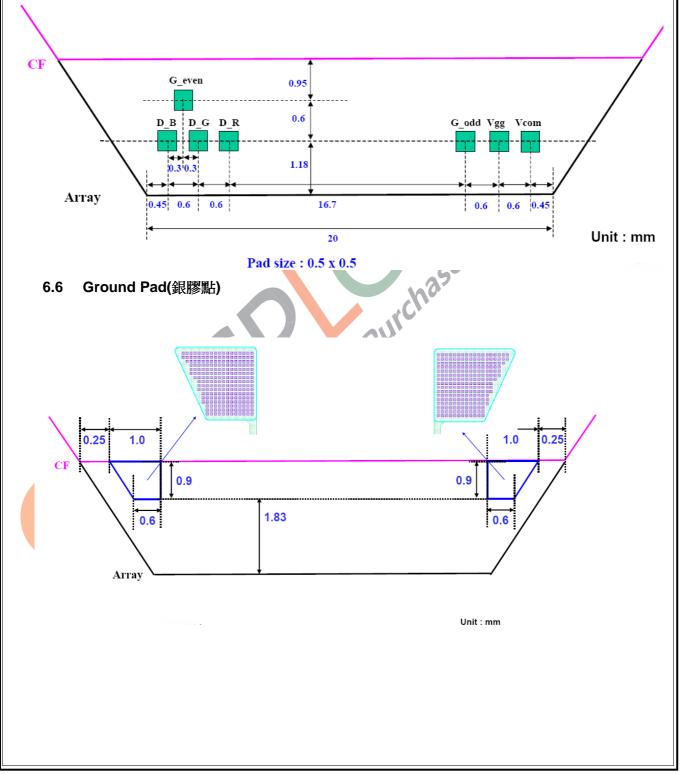
Array edge

0.11

0.03

0.03





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|----------------|--|----------|---------|
| Document Title | HSD013BPF1-D00 Preliminary Specification | Page No. | 27 / 34 |
| Document No.   | DC110-002194                             | Revision | 1.3     |

# 7.0 RELIABILITY TEST ITEMS

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| No. | ltem  | Conditions              | Remark |
|-----|---|-------------------------|--------|
| 1   | High Temperature Storage                          | Ta=+80°C, 240hrs        |        |
| 2   | Low Temperature Storage                           | Ta=-30°C, 240hrs        |        |
| 3   | High Temperature Operation                        | Ta=+70°C, 240hrs        |        |
| 4   | Low Temperature Operation                         | Ta=-20°C, 240hrs        | n,     |
| 5   | High Temperature and High<br>Humidity (Operating) | Ta=+60°C, 90%RH, 240hrs | 401    |
|     |   |                         | 0      |

Note: (1) All tests above are practiced at module type.

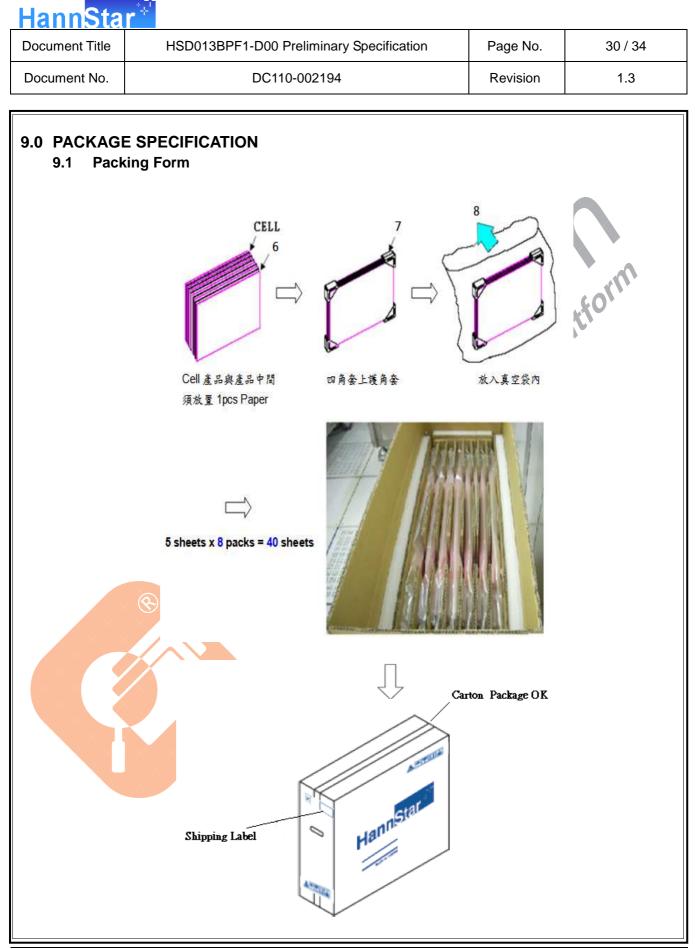
cosmeti clo supply supply (2) There is no display function NG issue occurred, all the cosmetic specification is judged before the reliability stress.



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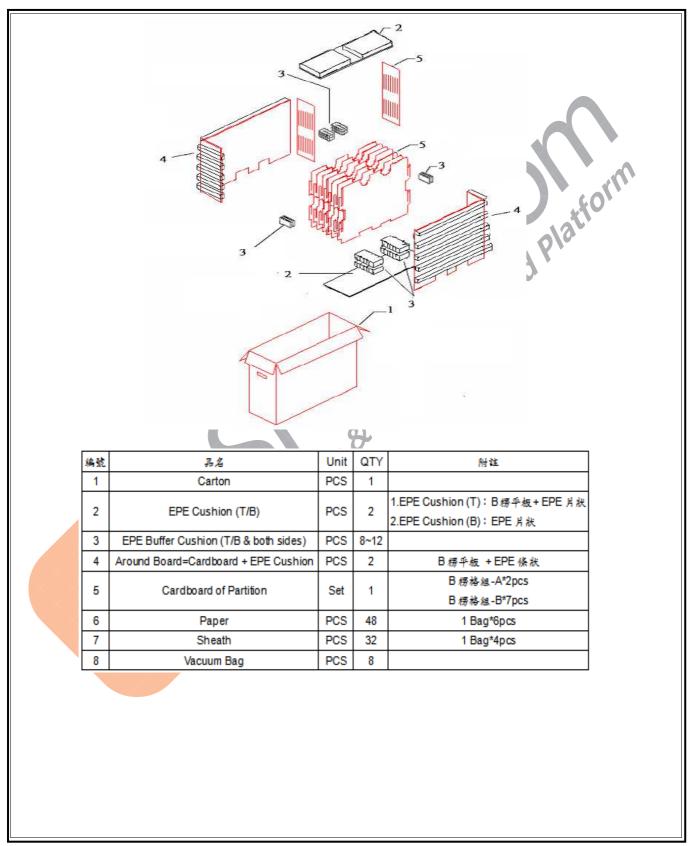
| Document Title   | ent Title HSD013BPF1-D00 Preliminary Specification Page No. 29 / 34       |          |            |     |        |        |      |      | / 34    |      |      |      |
|--|---|----------|------------|-----|--------|--------|------|------|---------|------|------|------|
| Document No.   | DC110-002194  |          |            |     |        |        |      | R    | evision | n    | 1    | .3   |
| (2) Pr   | (2) Production date   |          |            |     |        |        |      |      |         |      |      |      |
| (3) La   | bel ID: seri  | 1        |            |     |        |        |      | (0)  | (10)    |      |      |      |
|  | (1) (2)   | (3)      | (4)        | ) ( | (5) (6 | 6) (7) | (8)  | (9)  | (10)    | )    |      |      |
|  | Code (1),(2) : Out source code<br>Code (3) : Grade (D)<br>Code (4) : Year |          |            |     |        |        |      |      |         |      |      |      |
| Year   | 2016  | 2017     | 7 20       | 18  | 2019   | 2020   | 2021 | 2022 | 2 20    | 23 2 | 2024 | 2025 |
| Mark   | 6<br>Code (5)   | 7<br>• M | ہ<br>1onth | 8   | 9      | 0      | 1    | 2    |         | 3    | 4    | 5    |
| Month  |   |          | Mar.       | Apr | . May  | . Jun. | Jul. | Aug. | Sep.    | Oct  | Nov. | Dec. |
| Mark   | 1   | 2        | 3          | 4   | 5      | 6      | 7    | 8    | 9       | А    | В    | С    |
| Month Jan. Feb. Mar. Apr. May. Jun. Jul. Aug. Sep. Oct Nov. Dec. |   |          |            |     |        |        |      |      |         |      |      |      |

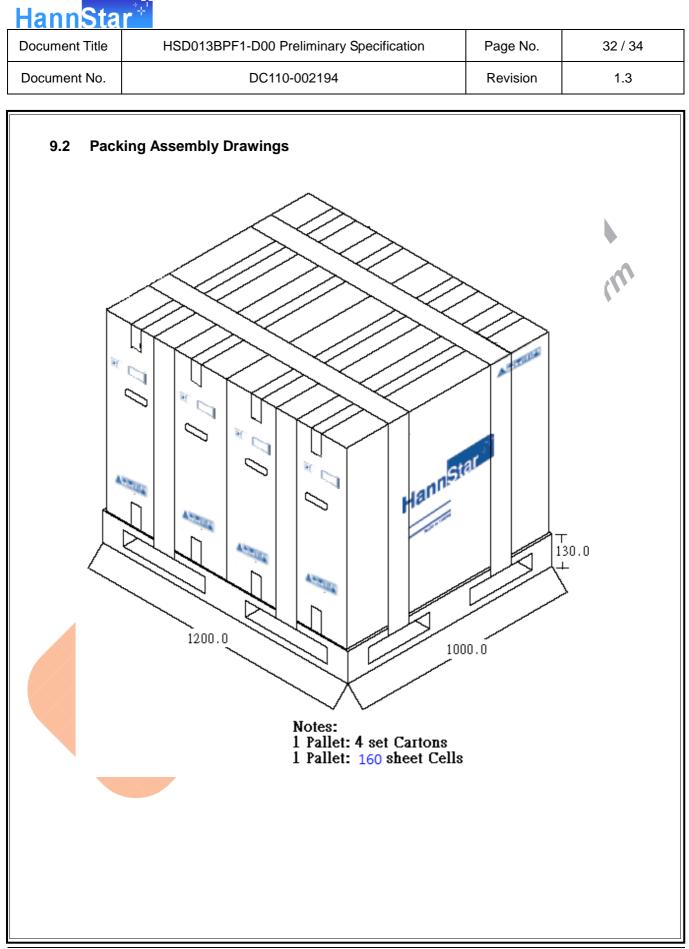


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|----------------|--|----------|---------|
| Document Title | HSD013BPF1-D00 Preliminary Specification | Page No. | 31 / 34 |
| Document No.   | DC110-002194                             | Revision | 1.3     |

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| Document Title | HSD013BPF1-D00 Preliminary Specification | Page No. | 33 / 34 |
|----------------|--|----------|---------|
| Document No.   | DC110-002194                             | Revision | 1.3     |

## **10.0GENERAL PRECAUTION**

### 10.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

### 10.2 Disassembling or Modification

Do not disassemble or modify the LCD. It may damage sensitive parts inside LCD, and may cause scratches or dust on the display. HannStar does not warrant the LCD, if customers disassemble or modify the module.

## 10.3 Breakage of LCD Panel

- 10.3.1 If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.
- 10.3.2 If liquid crystal contacts mouth or eyes, rinse out with water immediately.
- 10.3.4 If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.
- 10.3.4 Handle carefully with chips of glass that may cause injury, when the glass is broken.

## 10.4 Absolute Maximum Ratings and Power Protection Circuit

- 10.4.1 Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD may be damaged.
- **10.4.2** Please do not leave LCD in the environment of high humidity and high temperature for a long time.
- 10.4.3 It's recommended to employ protection circuit for power supply.

## 10.5 Operation

- 10.5.1 Do not touch, push or rub the polarizer with anything harder than HB pencil lead If the LCD attaches a polarizer.
- **10.5.2** Use fingerstalls of soft gloves in order to keep clean display quality, when persons handle the LCD for incoming inspection or assembly.
- 10.5.3 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.
- 10.5.4 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may causes deformation or color fading.
- 10.5.5 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzine or other adequate solvent.

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| Document Title | HSD013BPF1-D00 Preliminary Specification | Page No. | 34 / 34 |
|----------------|--|----------|---------|
| Document No.   | DC110-002194                             | Revision | 1.3     |

## **10.6 Static Electricity**

- 10.6.1 Protection film must remove very slowly from the surface of LCD to prevent from electrostatic occurrence if the LCD attaches a polarizer.
- 10.6.2 Because TFT-LCD panel is very weak to electrostatic discharge, please be careful with electrostatic discharge.

Persons who handle the LCD should be grounded through adequate methods.

## 10.7 Strong Light Exposure

The LCD shall not be exposed under strong light such as direct sunlight. Otherwise display characteristics may be changed. plat

## 10.8 Disposal

Disposal When disposing LCD, obey the local environmental regulations. CLOUP C