HannStar Display Corp.

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Hannstar Product Information

Model: HSD190MGW1

- A00

Note: 1. Please contact HannStar Display Corp. before designing your product based on this module specification.

2. The information contained herein is presented merely to indicate the characteristics and performance of our products. No responsibility is assumed by HannStar for any intellectual property claims or other problems that may result from application based on the module described herein.

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	Depart of Devisions								
Record of Revisions Rev. Updated No. Date Description of change									
1.0	_	Oct.26.2005	HSD190MGW1-A product information was first issued.						

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1.0 GENERAL DESCRIPTIONS

1.1 Introduction

HannStar Display model **HSD190MGW1-A** is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, the voltage reference, common voltage, DC-DC converter, column, and row driver circuit. This TFT LCD has a 19-inch diagonally measured active display area with WXGA+ resolution (900 vertical by 1440 horizontal pixel array).

1.2 Features

- 19" WXGA+ TN(Twisted Nematic)t mode TFT LCD panel
- High speed response time
- 4 CCFLs Backlight system
- Supported WXGA+ (V:900 lines, H:1440 pixels) resolution
- Supported to 75Hz refresh rate
- LCD Timing Controller
- RoHS compatible

1.3 General information

General information					
Item	Sp	Unit			
Outline dimension	428×278×18.5 (typ.))	mm		
Display area	408.24 (H) x255.15	(V)	mm		
Number of Pixel	1440(H) x 900(V)		Pixels		
Pixel pitch	0.2835(H) x 0.2385	(V)	mm		
Pixel arrangement	RGB Vertical stripe				
Display color	16.2M (6-bits+FRC)				
Display mode	Normally white				
Surface treatment	Antiglare, Hard-Coating (3H)				
Weight	(2400)	G			
Back-light	4-CCFLs, Top & bot				
Input signal	2-ch LVDS				
Down concurrention	Logic system	TBD	W		
Power consumption	B/L system	TBD	W		
Optimum viewing direction	6 o'clock				

1.4 Applications

- Desktop and Multi-function monitors
- Display terminals for AV applications
- Monitors for industrial applications

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1.5 Mechanical Information

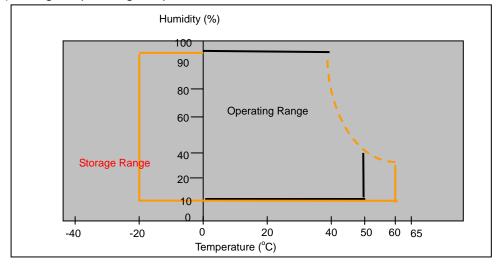
Ite	em	Min.	Тур.	Max.	Unit
	Horizontal(H)		428.0		mm
Module Size	Vertical(V)	ŀ	278.0		mm
	Depth(D)		18.5		mm
Weight (with	out inverter)	-	(2400)		g
Torque of customer screw hole				3.0	Kgf*Cm

2.0 ABSOLUTE MAXIMUM RATINGS

2.1 Absolute Rating of Environment

Item	Symbol	Min.	Max.	Unit	Note
Storage temperature	T _{STG}	-20	60	°C	
Operating temperature	T _{OPR}	0	50	°C	(1)
Vibration(non-operating)	V_{NOP}		1.5	G	(2)
Shock(non-operating)	S _{NOP}		50	G	(3)
Storage humidity	H _{STG}	10	90	%RH	(3)
Operating humidity	H _{OP}	10	80	%RH	(4)
Low pressure(operating)	P _{LOP}	697		HPa	(5)
Low pressure(non-operating)	P _{LNOP}	116		HPa	(6)

Note (1)Storage /Operating temperature



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- (2) 10-300Hz sine wave, X,Y,Z each directions, 30 min/cycle.
- (3) 11ms, ±X, ±Y, ±Z direction, one time each. For this shock test, It is necessary to fill the silicon rubber between the shock jig as buffer.
- (4) Max wet bulb temp. =39°C
- (5) 2 hrs. (10000 feet)
- (6) 24hrs. (50000 feet)

2.2 Electrical Absolute Rating:

2.2.1 TFT LCD Module:

Item	Symbol	Min.	Max.	Unit.	Note
Power supply Voltage	VDD	-0.3	6.0	V(DC)	(1)(2)

2.2.2 Back Light Unit:

Item	Symbol	Min.	Max.	Unit	Note
Lamp current	ΙL	3.0	8.5	mA	(1)(2)(3)
Lamp frequency	f_L	45	80	KHz	(1)(2)(3)

Note: (1) Permanent damage may occur to the LCD module if beyond this specification.

Functional operation should be restricted to the conditions described under

Normal Operating Conditions.

- (2) To exceed 7.5mA, life time accelerate drop down and if to exceed 8.5 mA has safety problem. If current lower than 3.5 mA, CCFL would be unstable or damaged.
- (3) Within Ta=25±2°C

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3.0 OPTICAL CHARACTERISTICS

3.1 Optical specification

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note
Contrast	Contrast				700			(1)(2)
Response time	Rising	TR			5		meac	(1)(2)
Response time	Falling	TF			(Tr+Tf)		msec	(1)(3)
White luminance (center of screen)		Y _L	⊖=0°		300		cd/m ²	(1)(4) (IL=7.5mA)
	Red	Rx	φ=0°	TBD	TBD	TBD		
	Neu	Ry	Normal	TBD	TBD	TBD		
_	Gree	Gx	viewing angle	TBD	TBD	TBD		
Color chromaticity	n	Gy	arigie	TBD	TBD	TBD		(1)(1)
(CIE1931)	Blue	Вх		TBD	TBD	TBD		(1)(4)
	Diue	Ву		TBD	TBD	TBD		
	White	Wx		TBD	0.310	TBD		
	vviile	Wy]	TBD	0.330	TBD		
	Hor.	θL			(75)			
Viewing angle	1101.	Θ_{R}	CR>10		(75)	-		
viewing angle	Ver.	Өн	CIV>10		(70)	1		
	V C I .	Θ_{L}			(65)	1		
	Hor.	θL			(80)			
Viewing angle	Hol.	Θ_{R}	CR>5		(80)			
	Ver.	Өн	UN>0		(80)	-		
	vei.	Θ_{L}	_		(70)	1		
Brightness uniformity		B _{UNI}	⊖=0° φ=0°		75		%	(6)

3.2 Measuring Condition

■ Measuring surrounding: dark room

■ Lamp current I_{BL}: (7.5)±0.1mA, lamp freq. F_L= 50KHz, Inverter: TBD

■ V_{DD1} =5.0V, f_V =60Hz, f_{DCLK} =53.25MHz

■ Surrounding temperature : 25±2°C

■ 30min. Warm-up time.

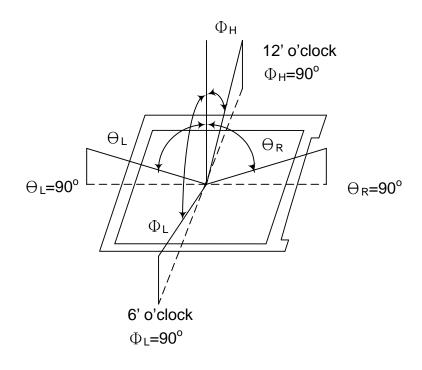
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3.3 Measuring Equipment

- MD-3000 of Otsuka Electric Corp., which utilized MCPD-7000 for Chromaticity and BM-5A for other optical characteristics.
- Measuring spot size: 10~12mm

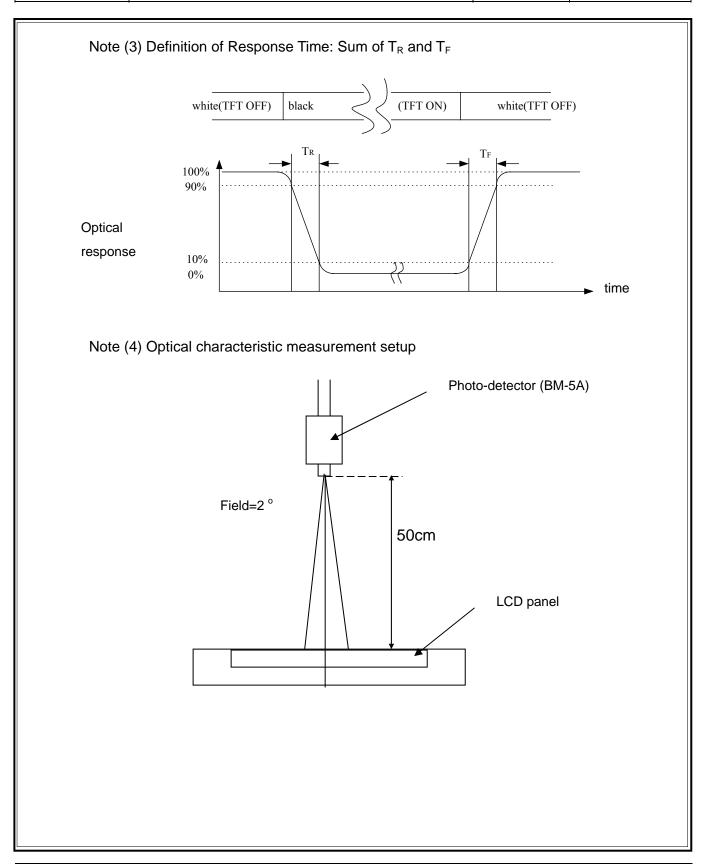
Note (1) Definition of Viewing Angle:



Note (2) Definition of Contrast Ratio(CR) : measured at the center point of panel

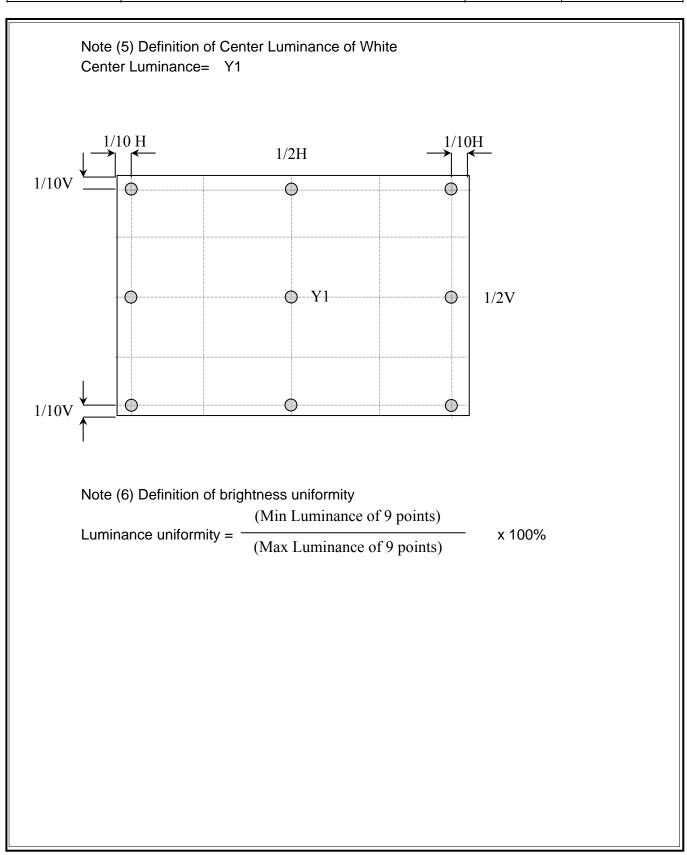
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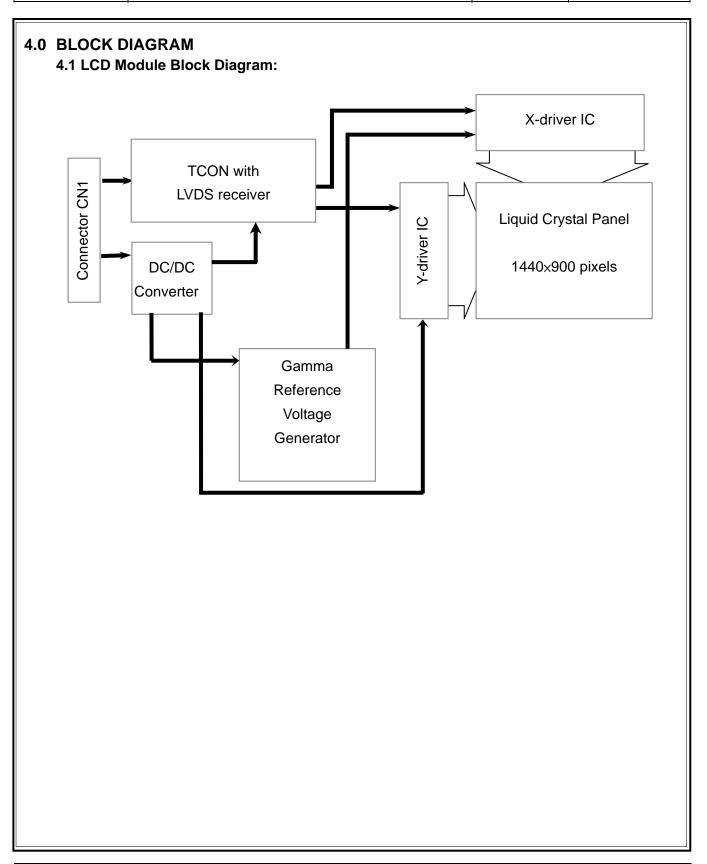
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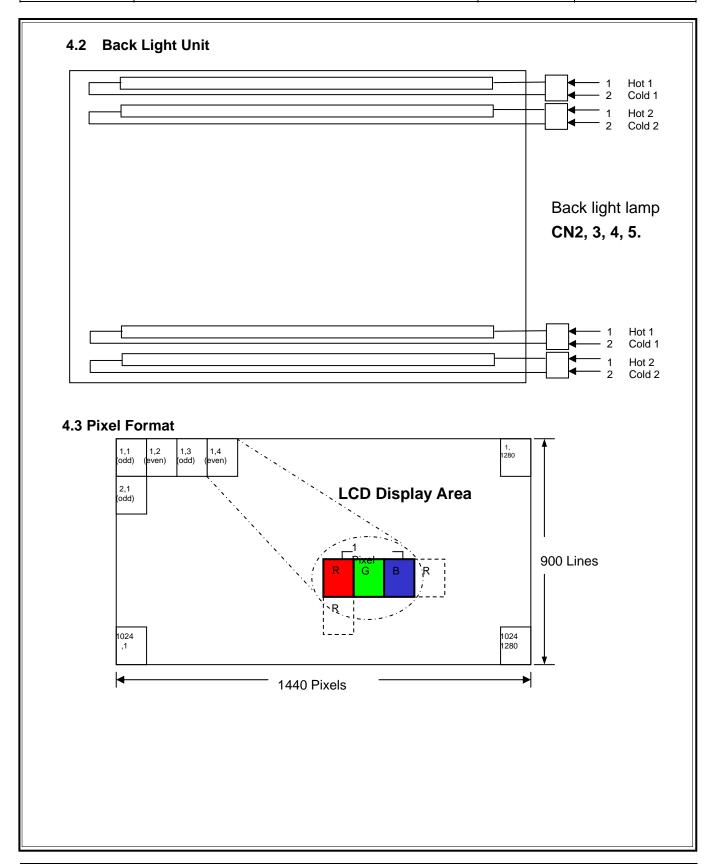
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		MSB LSB MSB LSB MSB LSB	Gray scale
	Display	R7 R6 R5 R4 R3 R2 R1 R0 G7 G6 G5 G4 G3 G2 G1 G0 B7 B6 B5 B4 B3 B2 B1 B0	Level
	Black		-
	Blue		-
	Green		-
Basic	Light Blue		-
color	Red	H H H H H H H H L L L L L L L L L L L L	-
	Purple	H	-
	Yellow	H H H H H H H H H H H H H H L L L L L L	-
	White	<u>нннннннннннннннннн</u>	-
	Black		L0
			L1
	Dark		L2
Gray scale	↑	: : :	L3…L251
of Red		H H H H H H L L L L L L L L L L L L L L	L255
	l ight		L255
	Light		L255
	Red	H H H H H H H L L L L L L L L L L L L L	Red L255
		<u> </u>	
	Black		L0
	Б.		L1
	Dark		L2
Gray scale	Î	: : :	L3…L251
of Green	↓		L255
	Light	<u> </u>	L255
		<u> </u>	L255
	Green	<u> </u>	Green L255
	Black		L0
		<u> </u>	L1
	Dark		L2
Gray scale	↑	: : :	L3…L251
of Blue	\downarrow		L255
	Light		L255
			L255
	Blue		Blue L255
	Black		LO
			L1
Gray scale of White & Black	Dark		L2
	↑	: : : : : : :	L3···L251
		H H H H H L L H H H H H H L L H H H H H	L252
	l icht		L252
	Light	<u> </u>	
	\A/I-:4-	<u> </u>	L254
	White	<u>нннннннннннннннннннн</u>	vvnite L255

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5.0 I/O CONNECTION PIN ASSIGNMENT

5.1 Interface Connector (30-pins) (JAE: FI-XB30SSRL-HF16 or equivalent)

Pin No.	Signal	Description
1	RinO0-	Receiver Signal (-)
2	RinO0+	Receiver Signal (+)
3	RinO1-	Receiver Signal (-)
4	RinO1+	Receiver Signal (+)
5	RinO2-	Receiver Signal (-)
6	RinO2+	Receiver Signal (+)
7	VSS	Ground
8	RinOC-	Clock Signal (-)
9	RinOC+	Clock Signal (+)
10	RinO3-	Receiver Signal (-)
11	RinO3+	Receiver Signal (+)
12	RinE0-	Receiver Signal (-)
13	RinE0+	Receiver Signal (+)
14	VSS	Receiver Signal (+)
15	RinE1-	Receiver Signal (-)
16	RinE1+	Receiver Signal (+)
17	VSS	Ground
18	RinE2-	Receiver Signal (-)
19	RinE2+	Receiver Signal (+)
20	RinEC-	Clock Signal (-)
21	RinEC+	Clock Signal (+)
22	RinE3-	Receiver Signal (-)
23	RinE3+	Receiver Signal (+)
24	VSS	Ground
25	NC	NC
26	NC	NC
27	NC	NC
28	VDD+5V	Power Supply, 5V (Typical)
29	VDD+5V	Power Supply, 5V (Typical)
30	VDD+5V	Power Supply, 5V (Typical)

5.2 Back Light Unit (CCFL) Connectors:

CN2, 3, 4, 5: CCFL Power Source (Yeonho 35001H5-02 or equivalent)

Pin No.	Symbol	Color	Function
1	Hot1	Pink	CCFL power supply (High voltage)
2	Cold1	White	Ground

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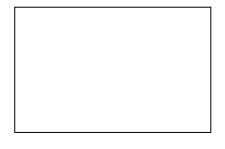
6.0 ELECTRICAL CHARACTERISTICS

6.1 TFT LCD Module:

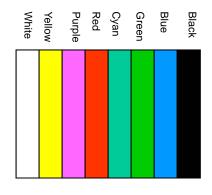
Item	Symbol	Min.	Тур.	Max.	Unit	Note
Voltage of power supply	V_{DD}	4.5	5.0	5.5	V	
Current of power supply	I _{DD1}	TBD	TBD	TBD	mA	(1)
Current of power supply	I _{DD2}	TBD	TBD	TBD	mA	(1)
Vsync frequency	f_{\vee}	60	60	75	Hz	(2)
Hsync frequency	f _H	55.5	59.9	75	KHz	
Frequency	f _{DCLK}	44.375	53.25	68.375	MHz	
Input rush current	I _{RUSH}			3.0	Α	(3)

Note (1)

(a)White:

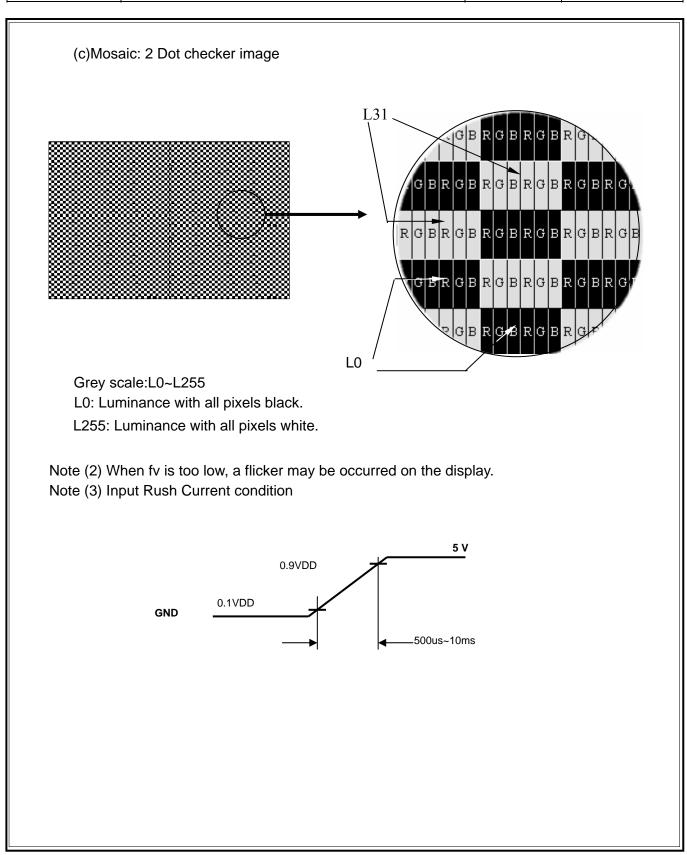


(b).V-Color:



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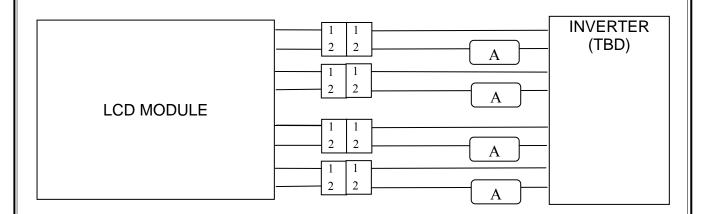
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6.2 Back-Light Unit

The back-light system is an direct-lighting type with 4 CCFLs (Cold Cathode Fluorescent Lamp). The characteristics of the lamp are shown in the following tables.

Item	Symbol	Min.	Тур.	Max.	Unit	Note
Lamp current	IL	3.0	7.5	8.0	mA(rms)	(1)
Lamp voltage	VL	610	680	750	V(rms)	I _L =7.5mA
Frequency	fL	40	50	80	KHz	(2)
Operating Lifetime	Hr	50,000			Hour	6.5mA(3)
Operating Lifetime	Hr	40,000		50,000	Hour	7.5mA(3)
Startup voltage	\/a	1450			V(rms)	at 25°C
Startup voltage	Vs	1700			V (11115)	at 0°C



Note (1)

Lamp current is measured with current meter for high frequency as shown below. Specified values are for a single lamp. To exceed 7.5.0mA, life time accelerate drop down and if to exceed 9.0 mA has safety problem. If current lower than 3.5 mA, CCFL would be unstable or damaged.

Note (2)

Lamp frequency may produce interference with horizontal synchronous frequency and this may cause ripple noise on the display. Therefore lamp frequency shall be kept away from the horizontal synchronous frequency and its harmonics as far as possible in order to avoid interference.

Note (3)

Lamp life time (Hr) can be defined as the time in which it continues to operate under the condition: Ta=25±3°C, Typical IL value indicated in the above table and fL=48 kHz until the brightness becomes less than 50%

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Note (4)

CCFL inverter should be able to provide a voltage over specified value (Vs) in the above table. Lamp units need at least Vs value shown above to ignition.

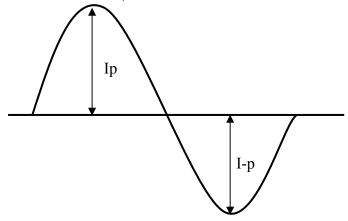
Note (5)

The voltage over specified value (Vs) should be applied to the lamp more than 1 second after startup. Otherwise, the lamp may not be turned on. The used lamp current is the lamp typical current.

Note (6)

The output voltage waveform and current waveform of the inverter must be symmetrical (Unsymmetrical ratio is less than 10%). Please do not use the inverter which has unsymmetrical voltage and current waveform, and spike waveform. The inverter design which can provide the best optical performance, power efficiency, and lamp life should under the following conditions.

- a. The asymmetry rate of the inverter waveform should be less than 10%.
- b. The distortion tae of the waveform should be within $\sqrt{2\pm10\%}$.
- c. The inverter output waveform should be better similar to the ideal sine wave.



Asymmetry rate = $|I_p-I_{-p}| / I_{rms} \times 100\%$ Distortion rate = I_p (or I_{-p}) / I_{rms}

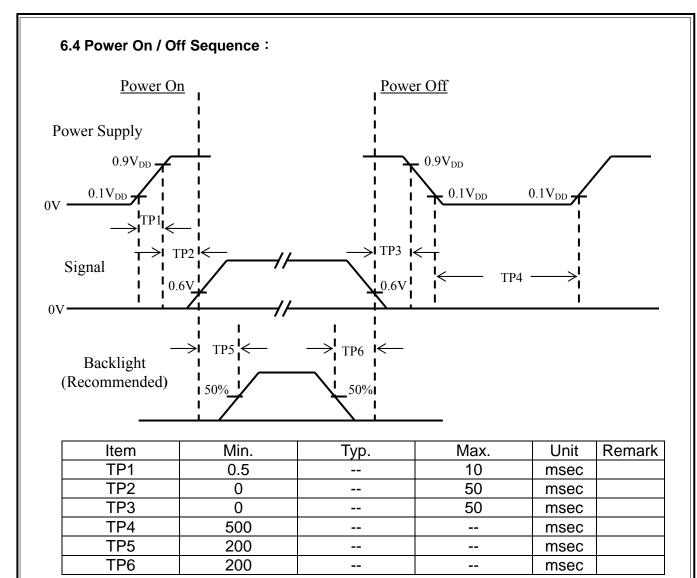
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Item	Symbol	Min.	Тур.	Max.	Unit
Frame Rate		60	60	75	Hz
Frame Period	t1	926	934	942	line
Vertical Display Time	t2	900	900	900	line
Vertical Blanking Time	t3	26	34	42	line
1 Line Scanning Time	t4	800	952	968	clock
Horizontal Display Time	e t5	720	720	720	clock
Horizontal Blanking Tin	ne t6	80	232	248	clock
Clock Rate	t7	44.375	53.25	68.375	MHz
ICLK t4	_)))))	
DE 14	<i>-</i> 》				
t4		X X X X	X,900		
0-5 0-5 0-5 0-5 0-5 orizontal		X X X X X X X X X X	x,900		
orizontal		X,898 X,899	x,900		
orizontal CLK CLK CE TEN TOTAL TOTAL		X,898 X,899	x,900		
0E 14 X,1 X X,2 X X,3 X 0-5 0-5 0-5 0-5	1	X,898 X,899	X,900		
0E 14 X,1 X X,2 X X,3 X Orizontal OE 10-5 0-5 0-5 0-5 1 3 5 6	1	X,898 X,899 t4	X,900		
0E 14 X,1 X X,2 X X,3 X Orizontal OE 10-5 0-5 0-5 0-5 1 3 5 6	1	X,898 X,899 t4	X,900		

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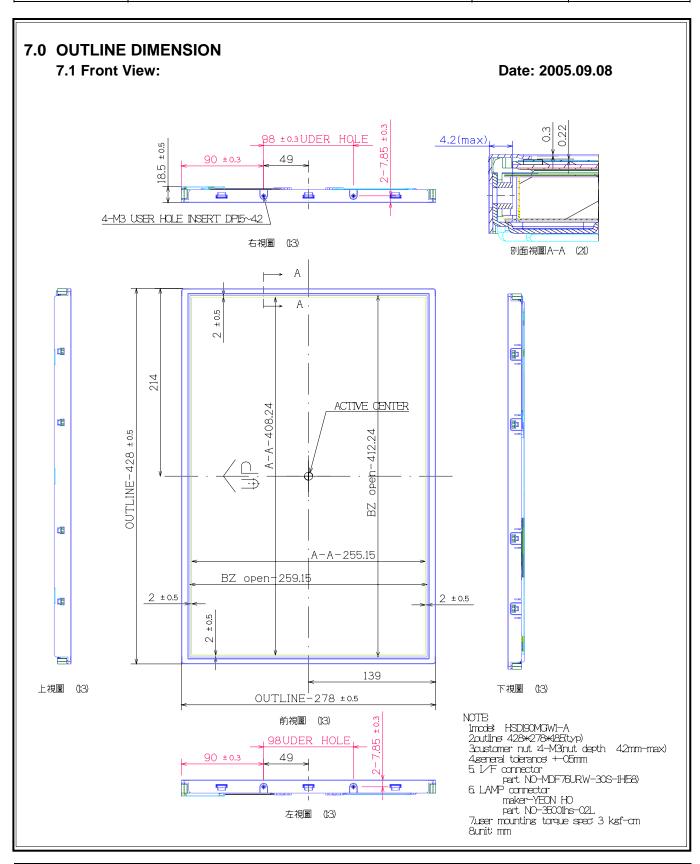
Note: (1) The supply voltage of the external system for the module input should be the same as the definition of V_{DD}.

- (2) Apply the lamp volatge within the LCD operation range. When the back-light turns on before the LCD operation or the LCD truns off before the back-light turns off, the display may momentarily become white.
- (3) In case of VDD = off level, please keep the level of input signal on the low or keep a high impedance.
- (4) TP4 should be measured after the module has been fully discharged between power off and on period.
- (5) Interface signal shall not be kept at high impedance when the power is on.

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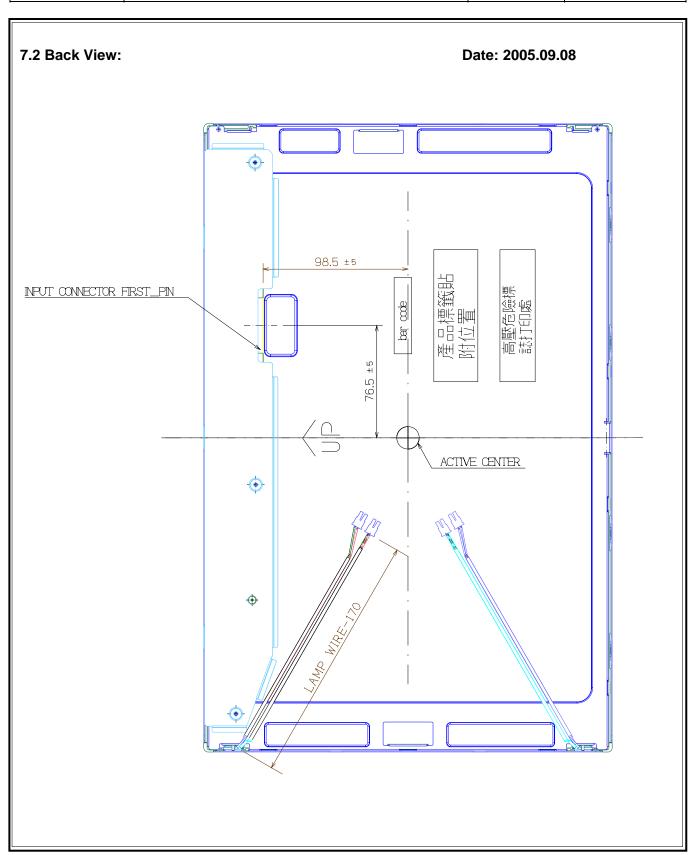
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8.0 LOT MARK

8.1 Lot Mark



code 1,2,3,4,5,6: HannStar internal flow control code.

code 7: production location. code 8: production year. code 9: production month.

code 10,11,12,13,14,15: serial number.

Note (1) Production Year

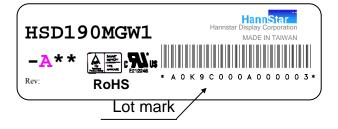
Year	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008
Mark	9	0	1	2	3	4	5	6	7	8

Note (2) Production Month

Month	Jan.	Feb.	Mar.	Apr.	Мау.	Jun.	Jul.	Aug.	Sep.	Oct	Nov.	Dec.
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

8.2 Location of Lot Mark

- (1) The label is attached to the backside of the LCD module.
- (2) This is subject to change without prior notice.



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9.0 PACKAGE SPECIFICATION 9.1 Packing form (1) package quantity in one carton: 8pieces. (2) carton size:544±3 mmx308±3 mmx^H406±3 mm. (3) for domestic transportation only. 9.2 Packing assembly drawings(TBD) 9.3 Pallet transportation specification(TBD)

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10.0 GENERAL PRECAUTION

10.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

10.2 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. HannStar does not warrant the module, if customers disassemble or modify the module.

10.3 Breakage of LCD Panel

- 10.3.1 If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.
- 10.3.2 If liquid crystal contacts mouth or eyes, rinse out with water immediately.
- 10.3.3 If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.
- 10.3.4 Handle carefully with chips of glass that may cause injury, when the glass is broken.

10.4 Electric Shock

- 10.4.1 Disconnect power supply before handling LCD module.
- 10.4.2 Do not pull or fold the CCFL cable.
- 10.4.3 Do not touch the parts inside LCD modules and the fluorescent lamp's connector or cables in order to prevent electric shock.

10.5 Absolute Maximum Ratings and Power Protection Circuit

- 10.5.1 Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.
- 10.5.2 Please do not leave LCD module in the environment of high humidity and high temperature for a long time.
- 10.5.3 It's recommended employing protection circuit for power supply.

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10.6 Operation

- 10.6.1 Do not touch, push or rub the polarizer with anything harder than HB pencil lead. Use fingerstalls of soft gloves in order to keep clean display quality, when Persons handle the LCD module for incoming inspection or assembly.
- 10.6.2 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.
- 10.6.3 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may causes deformation or color fading.
- 10.6.4 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzine or other adequate solvent.

10.7 Mechanism

Please mount LCD module by using mounting holes arranged in four corners tightly.

10.8 Static Electricity

- 10.8.1 Protection film must remove very slowly from the surface of LCD module to prevent
 - from electrostatic occurrence.
- 10.8.2 Because LCD module uses CMOS-IC on circuit board and TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge.
- 10.8.3 Persons who handle the module should be grounded through adequate methods.

10.9 Strong Light Exposure

The module shall not be exposed under strong light such as direct sunlight. Otherwise, display characteristics may be changed.

10.10 Disposal

When disposing LCD module, obey the local environmental regulations.