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To :

Date: May., 05, 2009

# **Formal Specification**

Model: HSD250MUW1

- A\*\*

#### Note:

- 1.Please contact Hannstar Display Corp. before designing your product based on this module specification.
- 2. The information contained herein is presented merely to indicate the characteristics and performance of our products. No responsibility is assumed by Hannstar for any intellectual property claims or other problems that may result from application based on the module described herein.
- 3. The mark " \*\* " of Model means sub-model code.

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			Record of Revisions
Rev.	Updated No.	Date	Description of change
1.0	Updated No. A00		
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#### 1.0 GENERAL DESCRIPTIONS

#### 1.1 Introduction

HannStar Display model HSD250MUW1-A is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, the voltage reference, common voltage, DC-DC converter, column, and row driver circuit. This TFT LCD has a 25-inch diagonally measured active display area with WUXGA resolution (1920 vertical by 1080 horizontal pixel array).

#### 1.2 Features

- 25" WUXGA mode TFT LCD pane for monitor application
- High speed response time and wide view angle
- 5U CCFL Backlight system
- Supported WUXGA (V:1920 lines, H:1080 pixels) Resolution
- LVDS interface
- RoHS compatible

# 1.3 General information

Item		Specification		
Outline dimension	579.0 × 352.0 >	579.0 × 352.0 × 40.4 (typ.)		
Display area	543.46 (H) x 305	5.69 (V)	mm	
Number of Pixel	1920(H) x 1080(	V)	Pixels	
Pixel pitch	0.283(H) x 0.283	8(V)	mm	
Pixel arrangement	RGB Vertical stri	ре		
Color gamut	92% (CIE1931)		NTSC	
Color gamut	102% (CIE1976)	102% (CIE1976)		
Display color	16.7M (6-bit+HiF	16.7M (6-bit+HiFRC)		
Display mode	Normally White	Normally White		
Surface treatment	Antiglare (3H)	Antiglare (3H)		
Weight	3550	3550		
Back-light	5U-CCFLs, Direc	5U-CCFLs, Direct type		
Input signal	2-ch LVDS	2-ch LVDS		
Power consumption	Logic system	9	W	
	B/L system	57	W	

#### 1.4 Applications

- Desktop and Multi-function monitors
- Display terminals for AV applications
- Monitors for industrial applications

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#### 1.5 Mechanical Information

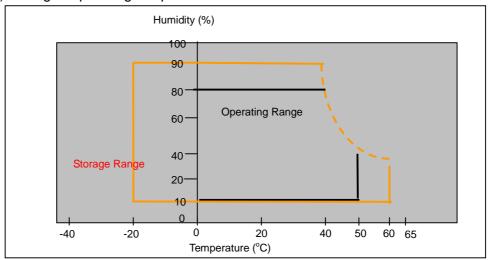
Item		Min.	Тур.	Max.	Unit
	Horizontal(H)	578.0	579.0	580.0	mm
Module Size	Vertical(V)	351.0	352.0	353.0	mm
	Depth(D)	39.9	40.4	40.9	mm
Weight (with	out inverter)	3350	3550	3750	g
Torque of custo	mer screw hole			3.0	Kgf*Cm

# 2.0 ABSOLUTE MAXIMUM RATINGS

# 2.1 Absolute Rating of Environment

Item	Symbol	Min.	Max.	Unit	Note
Storage temperature	T <sub>STG</sub>	-20	60	°C	
Operating temperature	T <sub>OPR</sub>	0	50	°C	(1)
Vibration (non-operating)	$V_{NOP}$		1.5	G	(2)
Shock (non-operating)	S <sub>NOP</sub>		50	G	(3)
Storage humidity	H <sub>STG</sub>	10	90	%RH	(3)
Operating humidity	H <sub>OP</sub>	10	80	%RH	(4)
Low pressure (operating)	P <sub>LOP</sub>	697		HPa	(5)
Low pressure (non-operating)	P <sub>LNOP</sub>	116		HPa	(6)

Note (1) Storage / Operating temperature





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- (2) 10-500Hz sine wave, X,Y,Z each directions, 30min/cycle.
- (3) 11ms, ±X, ±Y, ±Z direction, one time each. For this shock test, It is necessary to fill the silicon rubber between the shock jigs as buffer.
- (4) Max wet bulb temp. =39°C
- (5) 2 hrs. (10000 feet)
- (6) 24hrs. (50000 feet)

# 2.2 Electrical Absolute Rating:

#### 2.2.1 TFT LCD Module:

Item	Symbol	Min.	Max.	Unit.	Note
Power supply Voltage	VDD	-0.3	6.0	V(DC)	(1)(2)

#### 2.2.2 Back Light Unit:

Item	Symbol	Min.	Max.	Unit	Note
Lamp current	IL	3.0	9.0	mA	(1)(2)(3)
Lamp frequency	f∟	40	80	KHz	(1)(2)(3)

#### Note:

- (1) Permanent damage may occur to the LCD module if beyond this specification. Functional operation should be restricted to the conditions described under Normal Operating Conditions.
- (2) To exceed 7.5mA, life time accelerate drop down and if to exceed 9.0 mA has safety problem. If current is lower than 3.5 mA, CCFL would be unstable or damaged.
- (3) Within Ta=25±2℃



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#### 3.0 OPTICAL CHARACTERISTICS

# 3.1 Optical specification

 Item		Symbol	Condition	Min.	Typ	Max.	Unit	Note
					Тур.		Offic	
Contrast		CR		700	900			(1)(2)
				1	4ms	8		
Doon on oo tim					(Tr+Tf)			(2)
Response tin	ie				2ms	4	msec	(3)
					(GTG)			
White luminance		V	⊖=0°	000	400		11 2	(1)(4)(7)
(center of screen)		$Y_L$	φ=0°	320	400		cd/m <sup>2</sup>	(IL=6.5mA)
	Dod	Rx	Normal		0.652			
	Red	Ry	viewing		0.330			
	Gree	Gx	angle		0.222			
Color	n	Gy		-0.03	0.681	+0.03		(4)(5)
chromaticity (CIE1931)	Divis	Вх			0.148	+0.03		(1)(5)
,	Blue	Ву			0.054			
	White	Wx			0.31			
	vvriite	Wy			0.33			
	Hor.	θL		75	85			
Viewing angle	1101.	$\Theta_{R}$	CR>10	75	85	-		
Viewing angle	Ver.	Өн	01\/10	70	80			
	VEI.	θL	_	70	80			
Brightness uniformity		B <sub>UNI</sub>	⊖=0° φ=0°	75			%	(6)

# 3.2 Measuring Condition

■ Measuring surrounding: Dark room

■ Lamp current I<sub>BL</sub>: 6.5 mA, Inverter: MUSTEK MS-I10V-00481

■  $V_{DD1}$ =5.0V,  $f_{V}$ =60Hz,  $f_{DCLK}$ =66.28MHz

■ Surrounding temperature: 25±2°C

■ 30min. Warm-up time.

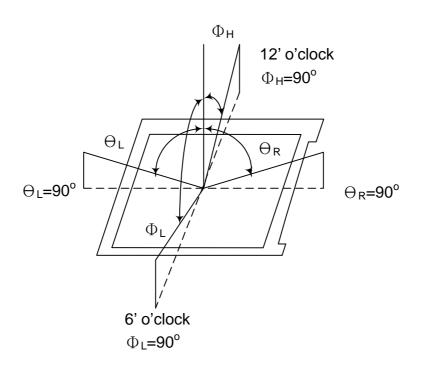


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# 3.3 Measuring Equipment

- FPM520 of Westar Display technologics, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics.
- Measuring spot size: 20~21mm

Note (1) Definition of Viewing Angle:

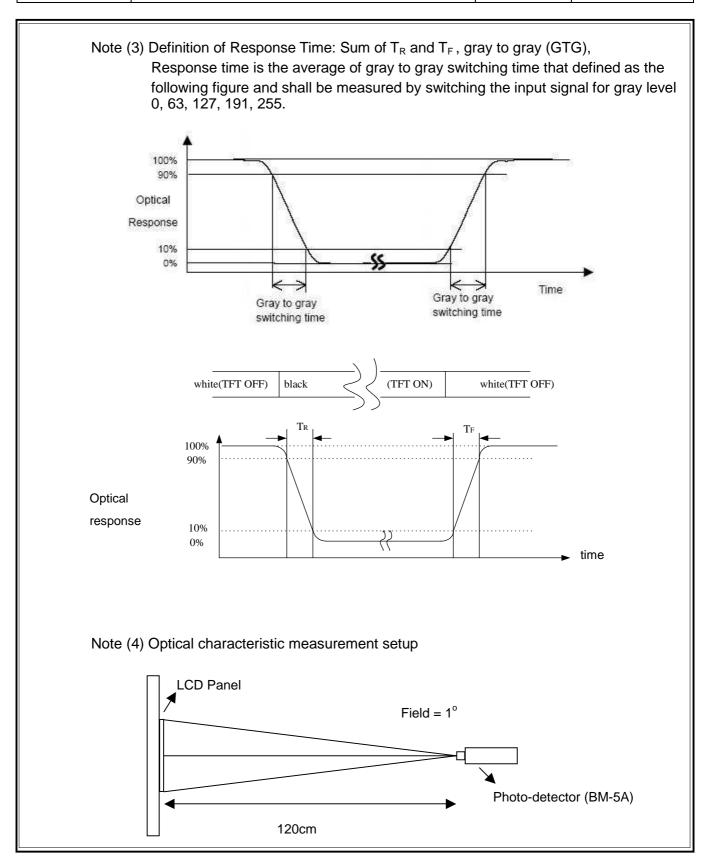


Note (2) Definition of Contrast Ratio(CR): measured at the center point of panel

> Luminance with all pixels white (L255) CR = Luminance with all pixels black (L0)

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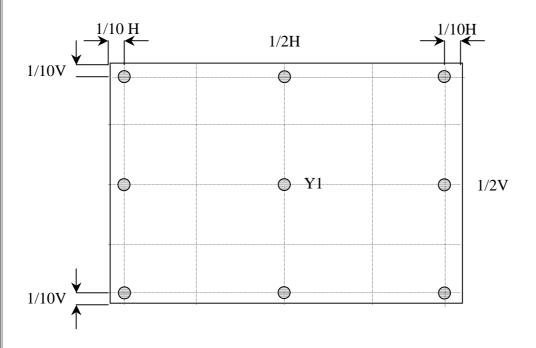
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Note (5) Definition of Center Luminance of White Center Luminance Y1

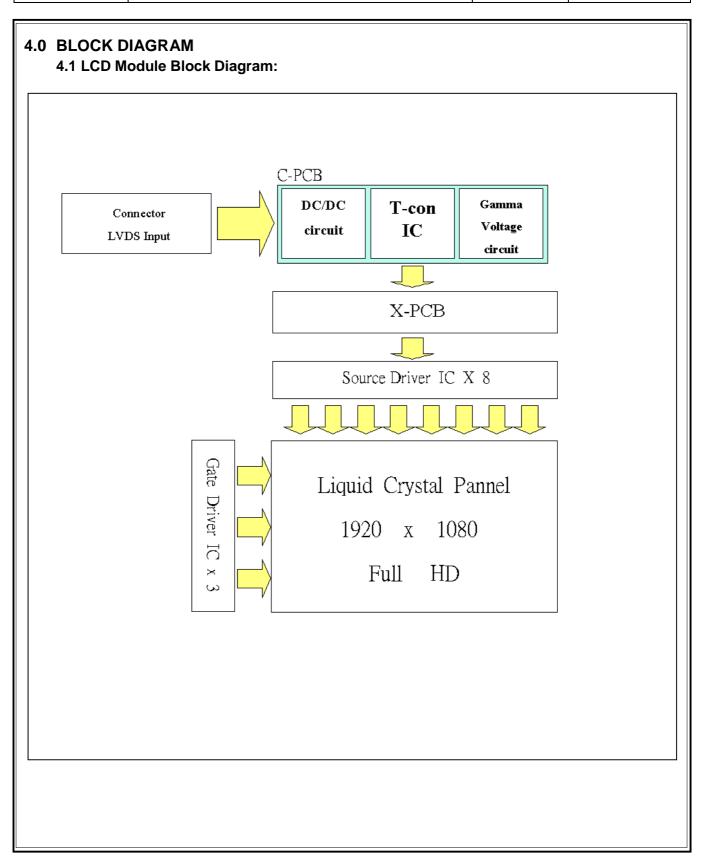


Note (6) Definition of brightness uniformity

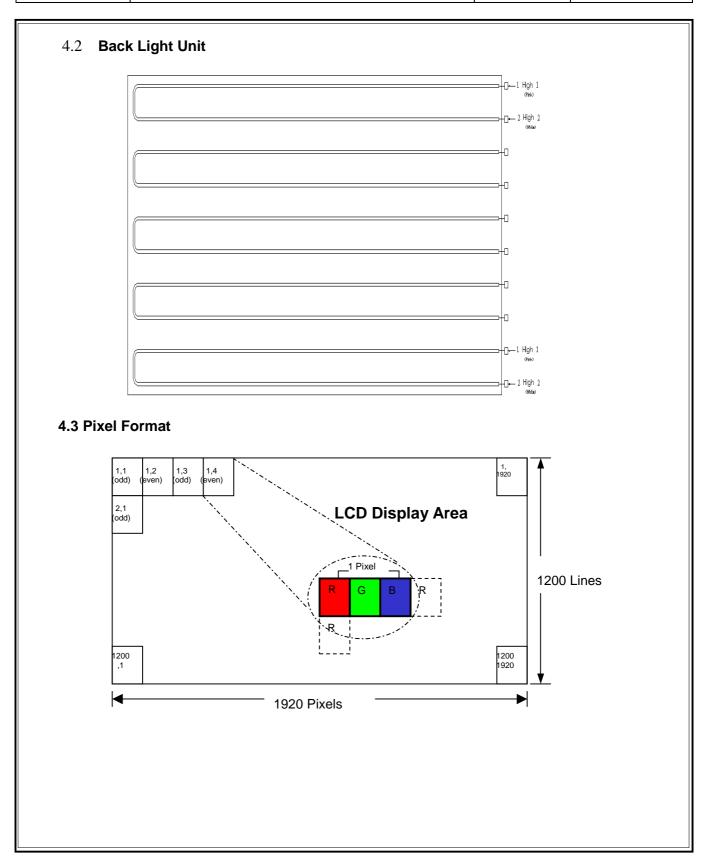
(Min Luminance of 9 points)

Luminance uniformity = (Max Luminance of 9 points) x 100%

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# 4.4 Relationship Between Displayed Color and Input

		MS	SB					LS	SB	MS	SB					L	SB	MS	SB					L	SB	Gray scale
	Display	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	В6	В5	В4	вз	B2	В1	во	Level
	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	-
	Blue	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	Н		Н	Н	Н	Н	Н	-
	Green	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	-
Basic	Light Blue	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	-
Color	Red	Н	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	-
	Purple	Н	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	-
	Yellow	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	-
	White	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	-
	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
		L	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L1
	Dark	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L2
Gray scale	1					:																:				L3…L251
of Red	$\downarrow$	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L255
	Light	Н	Н	Н	Н	Н	Н	L	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L255
		Н	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L255
	Red	Н	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Red L255
	Black	L	L	L	L	L	L	L	L	ᅵ	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	L	L1
	Dark	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	L	L	L2
Gray scale	1																					:				L3…L251
of Green	$\downarrow$	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L	L255
	Light	L	L	L	L	L	L	L	L	Η	Н	Н	Н	Н	Н	L	Н	L	L	L	L	L	L	L	L	L255
		L	L	L	L	L	L	L	L	Η	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L255
	Green	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	Green L255
	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	L1
	Dark	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	L	L2
Gray scale	1													,								:				L3…L251
of Blue	↓	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	L	L	L255
	Light	L	L	L	L	L	L	L	L	ᆚ	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	L	Н	L255
		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	L	L255
	Blue	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L							Н		Blue L255
	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
Gray scale of White & Black		L	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	Н	L	L	L			L	L	Н	L1
	Dark	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	Н	L	L2
	1																					:				L3…L251
	$\downarrow$	Н	Н	Н	Н	Н	Н	L	L	Н	Н	Н	Н	Н	Н	L	L	Н	Н	Н	Н	Н	Н	L	L	L252
	Light	Н	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	L	Н	L253
		Н	Н	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	L	L254
	White	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	White L255



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# 5.0 I/O CONNECTION PIN ASSIGNMENT

# **5.1 Interface Connector (30-pins )** (JAE: FI-X30SSL-HF or equivalent)

Pin No.	Signal	Description
1	RinO0-	Receiver Signal (-)
2	RinO0+	Receiver Signal (+)
3	RinO1-	Receiver Signal (-)
4	RinO1+	Receiver Signal (+)
5	RinO2-	Receiver Signal (-)
6	RinO2+	Receiver Signal (+)
7	VSS	Ground
8	RinOC-	Clock Signal (-)
9	RinOC+	Clock Signal (+)
10	RinO3-	Receiver Signal (-)
11	RinO3+	Receiver Signal (+)
12	RinE0-	Receiver Signal (-)
13	RinE0+	Receiver Signal (+)
14	VSS	Ground
15	RinE1-	Receiver Signal (-)
16	RinE1+	Receiver Signal (+)
17	VSS	Ground
18	RinE2-	Receiver Signal (-)
19	RinE2+	Receiver Signal (+)
20	RinEC-	Clock Signal (-)
21	RinEC+	Clock Signal (+)
22	RinE3-	Receiver Signal (-)
23	RinE3+	Receiver Signal (+)
24	VSS	Ground
25	NC	SDA
26	NC	SCL
27	NC	NC
28	VDD+5V	Power Supply, 5V (Typical)
29	VDD+5V	Power Supply, 5V (Typical)
30	VDD+5V	Power Supply, 5V (Typical)

# 5.2 Back Light Unit (CCFL) Connectors:

CN2, 3, 4, 5...8: CCFL Power Source (SM02 (12) B-BHS-1-TB or equivalent)

Pin No.	Symbol	Color	Function
1	High1	Pink	CCFL power supply (High voltage)
2	High2	White	CCFL power supply (High voltage)



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# **6.0 ELECTRICAL CHARACTERISTICS**

#### 6.1 TFT LCD Module:

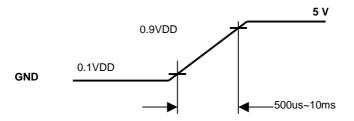
Item	Symbol	Min.	Тур.	Max.	Unit	Note
Voltage of power supply	$V_{DD}$	4.5	5.0	5.5	V	
Current of power supply	I <sub>DD</sub>	800	1000	1200	mA	(1)
Vsync frequency	f <sub>V</sub>	48	60	75	Hz	(2)
Hsync frequency	f <sub>H</sub>	52.61	65.76	82.2	KHz	
Frequency	f <sub>DCLK</sub>	53.02	66.28	85	MHz	
Input rush current	I <sub>RUSH</sub>			4.5	Α	(3)

# Note (1) Black pattern (L0):



Note (2) When fv is too low, a flicker may be occurred on the display.

Note (3) Input Rush Current condition



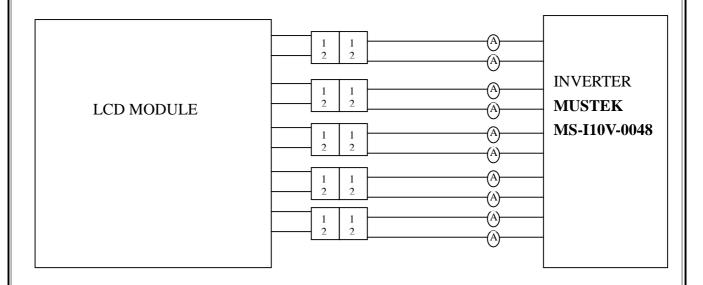


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# 6.2 Back-Light Unit

The back-light system is an direct-lighting type with 5CCFLs (Cold Cathode Fluorescent Lamp). The characteristics of the lamp are shown in the following tables.

Item	Symbol	Min.	Тур.	Max.	Unit	Note
Lamp current	IL	2.5	6.5	8.5	mA(rms)	(1)
Lamp voltage	VL	1580	1760	1940	V(rms)	I <sub>L</sub> =6.5mA
Frequency	fL	40		80	KHz	(2)
Operating Lifetime	Hr	50,000			Hour	6.5mA(3)
Startup voltage	Vs	3000			V(rms)	at 25°C
Startup voltage	VS	3500			v (11115)	at 0°C



# Note (1)

Lamp current is measured with current meter for high frequency as shown below. Specified values are for a single lamp. To exceed 7.5 mA, life time accelerate drop down and if to exceed 9.0 mA has safety problem. If current lower than 3.5 mA, CCFL would be unstable or damaged.

#### Note (2)

Lamp frequency may produce interference with horizontal synchronous frequency and this may cause ripple noise on the display. Therefore lamp frequency shall be kept away from the horizontal synchronous frequency and its harmonics as far as possible in order to avoid interference.



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# Note (3)

Lamp life time (Hr) can be defined as the time in which it continues to operate under the condition: Ta=25±3°C, Typical IL value indicated in the above table and fL=48 kHz until the brightness becomes less than 50%

# Note (4)

CCFL inverter should be able to provide a voltage over specified value (Vs) in the above table. Lamp units need at least Vs value shown above to ignition.

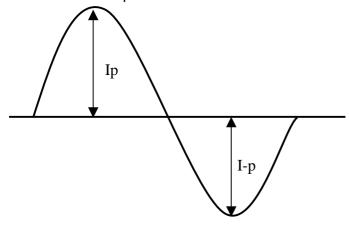
#### Note (5)

The voltage over specified value (Vs) should be applied to the lamp more than 1 second after startup. Otherwise, the lamp may not be turned on. The used lamp current is the lamp typical current.

#### Note (6)

The output voltage waveform and current waveform of the inverter must be symmetrical (Unsymmetrical ratio is less than 10%). Please do not use the inverter which has unsymmetrical voltage and current waveform, and spike waveform. The inverter design which can provide the best optical performance, power efficiency, and lamp life should under the following conditions.

- a. The asymmetry rate of the inverter waveform should be less than 10%.
- b. The distortion tae of the waveform should be within  $\sqrt{2\pm10\%}$ .
- c. The inverter output waveform should be better similar to the ideal sine wave.



Asymmetry rate =  $|I_p-I_{-p}| / I_{rms} x 100\%$ 

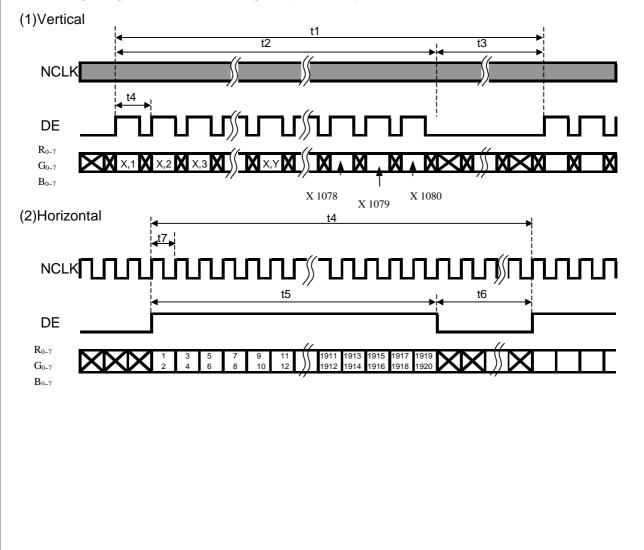
Distortion rate =  $I_p$  (or  $I_{-p}$ ) /  $I_{rms}$ 

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# 6.3 Interface Timing ( DE mode)

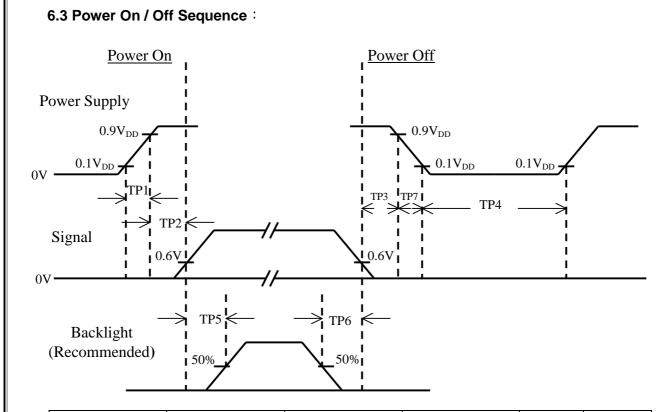
Item	Symbol	Min.	Тур.	Max.	Unit
Frame Rate		48	60	75	Hz
Frame Period	t1	1088	1096	1104	line
Vertical Display Time	t2	1080	1080	1080	line
Vertical Blanking Time	t3	8	16	24	line
1 Line Scanning Time	t4	992	1008	1276	clock
Horizontal Display Time	t5	960	960	960	clock
Horizontal Blanking Time	t6	32	48	316	clock
Clock Rate	t7	53.02	66.28	85	MHz

# Timing Diagram of Interface Signal (DE mode)





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Item	Min.	Тур.	Max.	Unit	Remark
TP1	0.5		10	msec	
TP2	0.01		50	msec	
TP3	20	35	50	msec	
TP4	1000			msec	
TP5	200			msec	
TP6	200			msec	
TP7	1		10	msec	

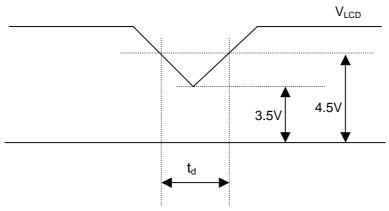
Note : (1) The supply voltage of the external system for the module input should be the same as the definition of  $V_{DD}$ .

- (2) Apply the lamp volatge within the LCD operation range. When the back-light turns on before the LCD operation or the LCD truns off before the back-light turns off, the display may momentarily become white.
- (3) In case of VDD = off level, please keep the level of input signal on the low or keep a high impedance.
- (4) TP4 should be measured after the module has been fully discharged between power off and on period.
- (5) Interface signal shall not be kept at high impedance when the power is on.



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# **6.4 V<sub>LCD</sub> Power Dip Condition**:



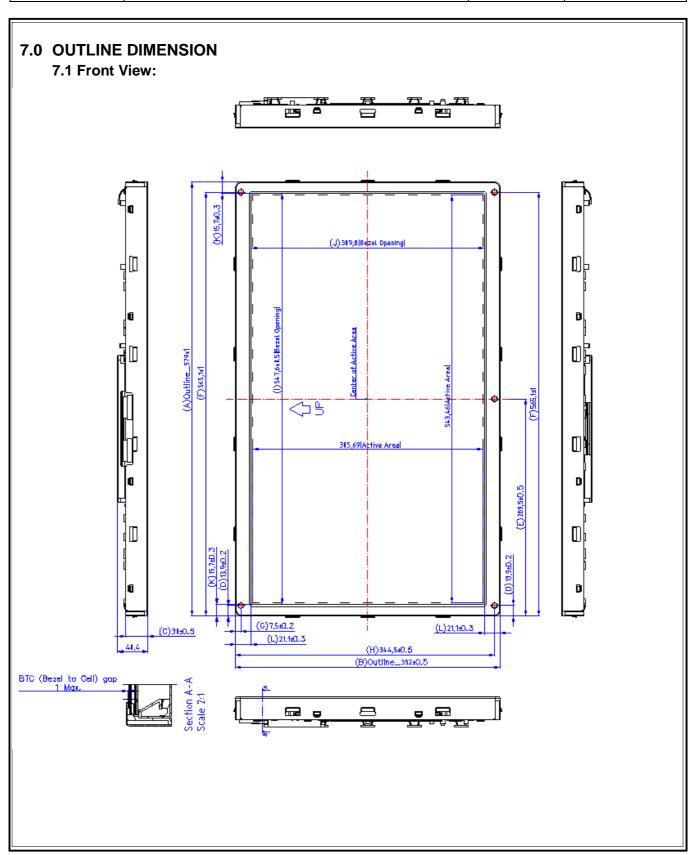
Note: (1) Dip condition

 $3.5V \leq V_{LCD} \leq 4.5V, \ t_{d} \leq 20ms$ 

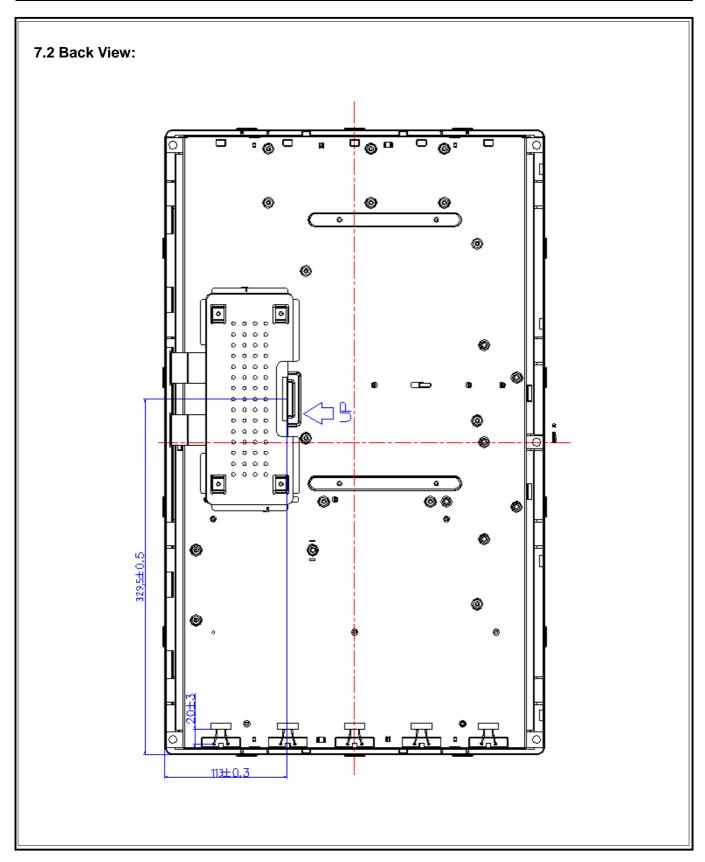
(2)  $V_{LCD} < 3.5V$ 

 $\ensuremath{V_{\text{LCD}}}\xspace$  - dip conditions should also follow the power On/Off conditions for supply voltage.

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# 8.0 LOT MARK

#### 8.1 Lot Mark



code 1,2,3,4,5,6: HannStar internal flow control code.

code 7: production location. code 8: production year.

code 9: production month.

code 10,11,12,13,14,15: serial number.

# Note (1) Production Year

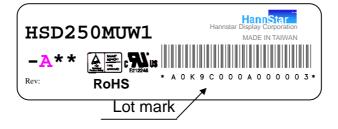
Year	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008
Mark	9	0	1	2	3	4	5	6	7	8

# Note (2) Production Month

Month	Jan.	Feb.	Mar.	Apr.	Мау.	Jun.	Jul.	Aug.	Sep.	Oct	Nov.	Dec.
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

#### 8.2 Location of Lot Mark

- (1) The label is attached to the backside of the LCD module.
- (2) This is subject to change without prior notice.





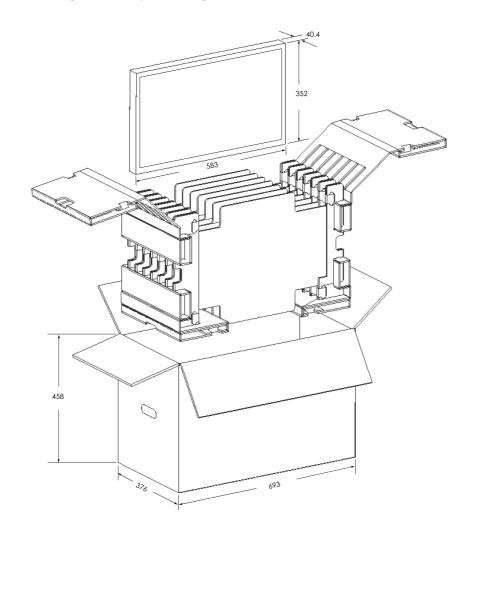
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# 9.0 PACKAGE SPECIFICATION

# 9.1 Packing form

- (1) package quantity in one carton: 6 Pieces
- (2) carton size: 693mm\*376mm\*458mm
- (3) for domestic transportation only.

# 9.2 Packing assembly drawings





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#### 10.0 GENERAL PRECAUTION

# 10.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

#### 10.2 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. HannStar does not warrant the module, if customers disassemble or modify the module.

# 10.3 Breakage of LCD Panel

- 10.3.1 If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.
- 10.3.2 If liquid crystal contacts mouth or eyes, rinse out with water immediately.
- 10.3.3 If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.
- 10.3.4 Handle carefully with chips of glass that may cause injury, when the glass is broken.

#### 10.4 Electric Shock

- 10.4.1 Disconnect power supply before handling LCD module.
- 10.4.2 Do not pull or fold the CCFL cable.
- 10.4.3 Do not touch the parts inside LCD modules and the fluorescent lamp's connector or cables in order to prevent electric shock.

#### 10.5 Absolute Maximum Ratings and Power Protection Circuit

- Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.
- Please do not leave LCD module in the environment of high humidity and high temperature for a long time.
- 10.5.3 It's recommended employing protection circuit for power supply.



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#### 10.6 Operation

- 10.6.1 Do not touch, push or rub the polarizer with anything harder than HB pencil lead.

  Use fingerstalls of soft gloves in order to keep clean display quality, when

  Persons handle the LCD module for incoming inspection or assembly.
- 10.6.2 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.
- 10.6.3 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may causes deformation or color fading.
- 10.6.4 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzine or other adequate solvent.

#### 10.7 Mechanism

Please mount LCD module by using mounting holes arranged in four corners tightly.

# 10.8 Static Electricity

- 10.8.1 Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.
- 10.8.2 Because LCD module uses CMOS-IC on circuit board and TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge.
- 10.8.3 Persons who handle the module should be grounded through adequate methods.

# 10.9 Strong Light Exposure

The module shall not be exposed under strong light such as direct sunlight. Otherwise, display characteristics may be changed.

#### 10.10 Disposal

When disposing LCD module, obey the local environmental regulations.