

# **HSL226**

## Silicon Schottky Barrier Diode for High Speed Switching

REJ03G0016-0300 Rev.3.00 Feb 06, 2007

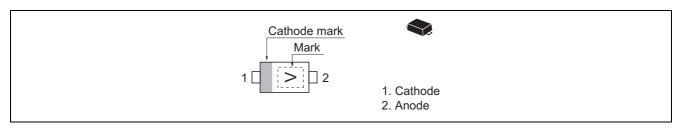
#### **Features**

- Low Power consumption (Low reverse leak current) and high speed (Low capacitance).
- Lineup of Environmental friendly Halogen free type (HSL226-N)
- Extremely small Flat Lead Package (EFP) is suitable for surface mount design.

### **Ordering Information**

Type No.	Laser Mark	Package Name	Package Code
HSL226	V	EFP	PXSF0002ZA-A
HSL226-N			
(Halogen-free type)			

### **Pin Arrangement**



## **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	$V_{RRM}$	25	V
Non-Repetitive Peak forward surge current	I <sub>FSM</sub> *	200	mA
Forward current	I <sub>F</sub>	50	mA
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Note: 10 ms Sine Wave 1 pulse

### **Electrical Characteristics**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V <sub>F1</sub>	_	_	0.33	V	I <sub>F</sub> = 1 mA
	V <sub>F2</sub>	_	_	0.38	V	I <sub>F</sub> = 5 mA
Reverse current	I <sub>R</sub>	_	_	450	nA	V <sub>R</sub> = 20 V
Capacitance	С	_	_	2.80	pF	V <sub>R</sub> = 1 V, f = 1 MHz

Note: For EFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

### **Main Characteristic**

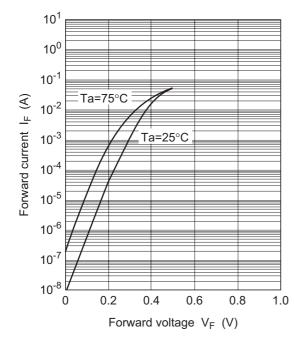
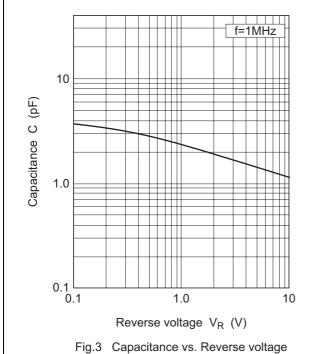


Fig.1 Forward current vs. Forward voltage



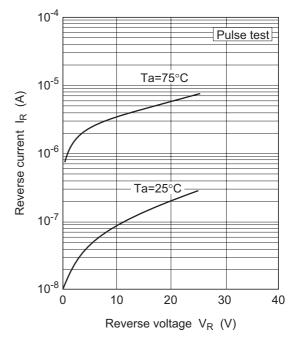
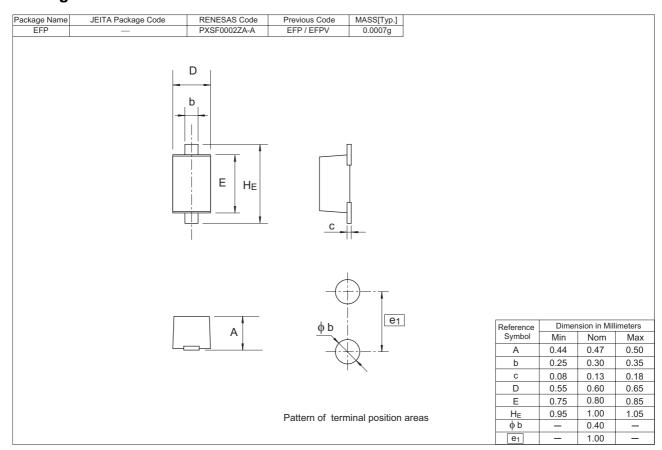


Fig.2 Reverse current vs. Reverse voltage

## **Package Dimensions**



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