High-reliability hybrid integrated DC/DC converter (HSG28 series)

1. Features (see Fig. 1 for outside view, and Table 1 for 2. Scope of application

models)

Range of input DC voltage: 18~36V, nominal input DC

voltage 28V

Output power: 5W

Operating temperature (T_c): -55~+105 Input, output and case are isolated mutually Insulation resistance: $R \ge 100M\Omega$ (DC 500V)

With the function of inhibit and short-circuit protection

Power density: 28W/in³

Function of pins: same as that of like products of

Interpoint Company Totally sealed metal case

High-reliability electronic system for aviation and aerospace, etc.

HSG28T15

Size: Style G: $34 \times 27.34 \times 9.00 \text{mm}^3$

Style R: $53.08 \times 28.19 \times 10.00$ mm³(without fixed end)

 $73.80 \times 28.19 \times 10.00 \text{mm}^3$ (with fixed end)

Weight: Style G: 27g; Style R: 43g

HSW28M15: 46g

Fig. 1 Outside view of HSG28 series

Table 1 Product models

single	style	dual	style	triple	style	multi-output	style
HSG28S12	G	HSG28D5N	G	HSG28T12	G	HSW28M15	W
HSG28S3R5	G	HSG28D12	G	HSG28T15	G		
HSG28S5	G	HSG28D15	G				
HSG28S18	G	HSG28D15N	G				
HSG28S20	G						
HSG28S55	G						
HSR28S5F-A	R						

3. Description

HSG28 series high-reliability DC/DC converter can work at 28V input voltage, and the output power is 5W. The case of this series adopts totally sealed metal structure.

HSG28 series products adopt the push-pull topology and the full-wave rectification technology, the constant switching frequency is 200~300kHz.

Both the design and manufacture of HSG28 series products satisfy the requirements of GJB2438A-2002 "General specifications for hybrid integration circuit". Test method and procedure of electric circuit shall execute GJB548A-96 "Test method and procedure for microelectronic devices".

4. Electrical performance (Table 2~6)

Table 2 Rated conditions and recommended operating conditions

Absolute max. rated value	Input voltage: 18~36V Power dissipation: 5W Lead soldering temperature: 300 /(10s) Storage temperature range: -55~125
Recommended operating conditions	Logical low voltage: $\leq 0.2V$ Range of input DC voltage: $18 \sim 36V$ Ambient temperature (T_A) : $-55 \sim +85$
	1 miletoni vemperavore (1/h).

ADD: 260 Jixi Road, Hefei, Anhui, China 230022 Page 1 of 8 Tel: 0086-551-3667943 E-mail: sales@ecrim.cn Fax:0086-551-3638101

Table 3 Electrical characteristics (single-output)

	Table 3 Elect	rical cha	racteristic	s (sing	le-output)				
	Conditions	HSG	28S5	HSG	28S18	HSG2	28S55	HSG28S12		
Domomotor	(unless otherwise specified) $V_{\text{in}} = 28V \pm 5\%,$		enterpr	ise mil	itary star	ndard				
Parameter	$V_{\text{in}} = 28 V \pm 3 \%$, $-55 \% \leqslant T_A \leqslant +85 \%$	Q/HW2	20086-94	Q/HW30275-99		Q/HW30005-97		Q/HW20	304-2001	
	-55 C 1A +65 C	min.	max	min.	max	min.	max	min.	max	
input voltage/V	_	22	32	22	34	22	34	12	18	
output voltage/V output power/W		4.8	5. 2	17.7	18. 3	54.0	56.0	11.75	12. 25	
output current/A	-		3. 5		4		1. 4		5	
	V full load, 100MHz, $T_A = 25$ °C	0	0. 7 80	0	0. 2 80	0	0.03	0	0. 4 30	
efficiency/%	full load, $T_A = 25$ °C	65		65		50		75		
load regulation/%	no-load to full load, $T_A = 25$ °C	<u>.</u>	1	·············	1		1	<u>.</u>	0.5	
voltage regulation/%	full load, $T_A = 25$ °C	_	0.1	_	0.5		0.5		0.5	
ingulating registers of MO	$T_A = 25^{\circ}\text{C}$, apply 500V DC betwe	en ₁₀₀	_	100	_	100	_	10	_	
	any two of input, output and ca	ise								
inhibit function protection function			YES YES		YES		YES			
•			1 E S				YES	YES		
Table 3 (continued)	C 1'4'									
	Conditions (unless otherwise specified)	e specified)							3 R 5	
Parameter	$V_{\rm in} = 28 \rm V \pm 5\%$,	0 /11		-	se milita	•				
- 11-11	-55 °C $\leqslant T_A \leqslant +85$ °C	min.	W30020-9		Q/HW303 min.			HW3043		
input voltage/V		22	3	ax.	18	max.	mii 2		33	
output voltage/V	_	19. 70			4. 93	5. 07	3.		3. 65	
output power/W			20.					-	3. 5	
output current/A		0	0.		0	5 1		 -	1	
	V full load, 100MHz, $T_A = 25$ °C		10		<u>V</u>	60		-	80	
efficiency/%	full load, $T_A = 25 ^{\circ}\text{C}$	60		 -	76		6			
	no-load to full load, $T_A = 25^{\circ}\text{C}$	00	1			50mV			00mV	
voltage regulation/%	full load, $T_A = 25$ °C		0.			50mV			50 mV	
	$T_A = 25^{\circ}\text{C}$ apply 500V DC between two of input, output and call	en :								
insulating resistance/M\(\Omega\)2	any two of input, output and car	se 100	_	-	100	_	_	-	_	
inhibit function	_	YES	_	-	_	_	_	-	YES	
protection function	_		_	-	_	_	_	-	YES	
	Table 4 Electric	cal Cha	racteris	tics (dı	ıal-output)					
	conditions	HSG	28D12	HSG	28D15	HSG28	BD15N	HSG2	28D5N	
Parameter	(unless otherwise specified)		ente	rprise	milit	ary	standaı	·d:		
rarameter	$V_{\rm in} = 28V \pm 5\%$,	Q/HW2	0089-64	Q/HW3	80004-97	Q/HW20	0087-94	Q/HW30	0266-99	
	-55° C $\leqslant T_A \leqslant +85^{\circ}$ C	min.	max.	min.	max.	min.	max.	min.	max.	
input voltage/V	_	22	32	18	40	22	32	20	36	
	_	11.7	+12.3	+14.80	0+15.20	+14.85	+15.15	+4.75	+5.25	
output voltage/V	_	-12.3	-11.7	-15.20	0-14.80	-15.30	-14.70	-5.25	-4.75	
output power/W	_		4		4	_	4	_	4	
output current/A	_	_	± 0.175	_	±0.133	_	0.20	0	0.70	
-							-0.08	-0.05	0	
	V full load, 100MHz, $T_A = 25 ^{\circ}\text{C}$		80	-	80	_	80		80	
efficiency/%	full load, $T_A = 25^{\circ}\text{C}$	65		72	<u>-</u>	65	<u>-</u>	70		
	no-load to full load, $T_A = 25$ °C	0.1			0.8	0.01			1.0	
voltage regulation/%		0. 1			0. 2	0.01			0.5	
	$T_A = 25$ °C apply 500V DC between any two of input, output and case		_	100	_	100	_	100	-	
inhibit function	_	YES	····	_	YES	_	YES	YES	-/	
protection function	_	_	YES	_	_	_	_	_	1	
								- 4	3	

ADD: 260 Jixi Road, Hefei, Anhui, China 230022 E-mail: sales@ecrim.cn

Page 2 of 8

Tel: 0086-551-3667943 Fax:0086-551-3638101

 Table 5 Electrical characteristics (triple-output)

	conditions (unless otherwise spec	HSG	28T15	HSG	28T12	
	$V_{\rm in}=28V\pm5\%$	enterprise military standard:				
Parameter	$-55^{\circ}\text{C} \leqslant T_A \leqslant +85^{\circ}\text{C} \text{ (H}$	ISG28T15)	Q/HW30	576-2004	Q/HW20504-2005	
	$-55\% \leqslant T_C \leqslant +125\% \text{ (H}$	HSG28T12)	min.	max.	min.	max.
input voltage/V	-		22	30	20	32
output voltage/V		$V_{ m out1}$	14.8	15. 2	11.8	12. 2
	-	$V_{ m out2}$	14.8	15. 2	11.8	12. 2
		$V_{ m out3}$	14.8	15. 2	11.8	12. 2
output power/W	_	•••••	_	5	_	5
output current/A	_		_	0.08	_	0.1
output ripple voltage/mV	full load, $100MHz$, $T_A =$	=25℃	_	100	_	100
efficiency/%	full load, $T_A = 25$ °C	2	65	_	65	_
load regulation/mV	no-load to full load, $T_A =$	25℃	-	100	_	50
voltage regulation/mV	full load, $T_A = 25\%$	2	-	100	-	100
insulating resistance/M Ω	$T_A = 25$ °C, apply 500VDC input and output or betw (except pin 2) and ca	een any pin	en 100	_	100	_
inhibit function/V T_A	=25°C, in the range of inhi the circuit output is		_	0. 2	_	0.2
protection function	_		_	YES	_	YES

Table 6 Electrical characteristics (multi-output)

	conditions (unless otherwise specified)	HSW28M15 enterprise military standard: Q/HW30413 — 2004			
Parameter					
	$-55^{\circ} \text{C} \leqslant T_{\text{A}} \leqslant +85^{\circ} \text{C}$	min.	max.		
input voltage/V	_	24	32		
output voltage/V	_	14. 9	15. 1		
output power/W	_	_	2, 7		
output current/A	_	_	0.03		
output ripple voltage/mV	full load, 20MHz, $T_A = 25$ °C	_	50		
efficiency/%	full load, $T_A = 25^{\circ}C$	30	_		
load regulation/%	no-load to full load, $T_A = 25^{\circ}C$	_	0.2		
voltage regulation/%	full load, $T_A = 25^{\circ}\text{C}$	_	0.2		
insulating resistance/MΩ	$T_A = 25$ °C apply 500VDC voltage bet any two of input, output and case	ween 100	_		

ADD: 260 Jixi Road, Hefei, Anhui, China 230022 E-mail: sales@ecrim.cn

Page 3 of 8

Tel: 0086-551-3667943 Fax:0086-551-3638101

5 Circuit block diagram (Fig.2)

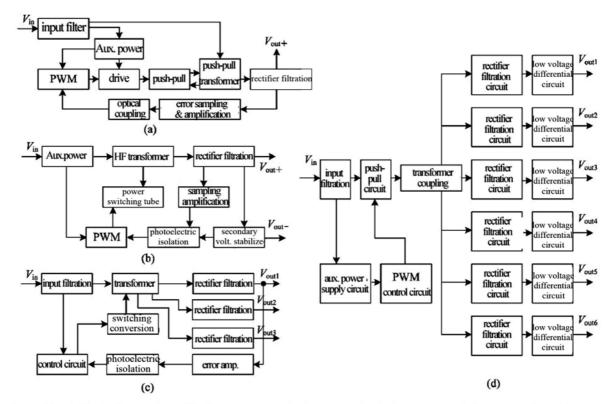
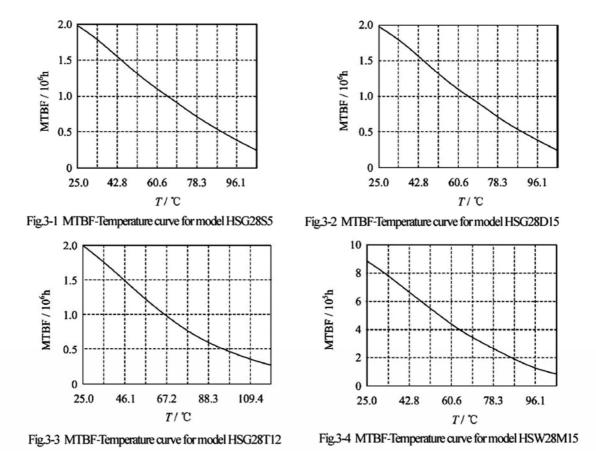


Fig. 2 Circuit block diagram for DC/DC converter (a) single-output (b) dual-output (c) triple-output (d) multi-output

6 MTBF Curve (Fig.3-1~4)



(as per GJB/Z299B-98, envisaged good ground condition)

ADD: 260 Jixi Road, Hefei, Anhui, China 230022 Page 4 of 8 Tel: 0086-551-3667943 E-mail: sales@ecrim.cn Fax:0086-551-3638101

7 Pin designation (Fig.4, Table 7~9)

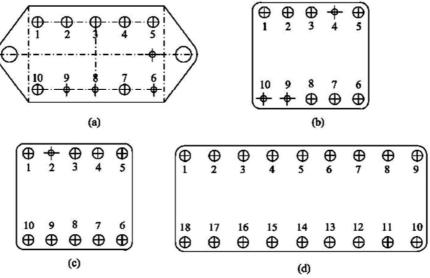


Fig. 4 Pin designation (bottom view)
(a) Style R, (b) Style G, (c) Style G (HSG28T12, HSG28T15), (d) HSW28M15

Table 7 Pin designation

6		Pinout number								
functions	HSG28S5	HSG28S18	HSG28S55	HSG15S12	HSG28S20	HSR28S5F-A	HSG28S3R5			
positive input	3	3	3	3	3	1	3			
positive output	6	6	6	5	6	5	5			
input ground	5	5	5	1	5	10	6			
output ground	7	7	8	4	8	4	7			
adjustment*	-	_	-	_	_	3	_			
inhibit	2	2	2	_	2	2	2			
case ground	4,9,10	4,9,10	4,9,10	_	4,9,10	6,7,8	4,9,10			
no connection	1,8	1,8	1,7	2,6,7,8	1,7	9	1,8			

Note: *adjustment pinout adjusts the change of output voltage through external resistance between adjustment pin and output (positive and ground)

Table 8 Pin designation

Constitute		pinout number	
functions	HSG28D5N	HSG28D15	HSG28D15N
positive input	3	3	3
input ground	5	5	5
positive output	6	6	6
negative output	8	8	8
output ground	7	7	7
inhibit	2	2	2
case ground	4,9,10	4,9,10	4,9,10
no connection	1	1	1

ADD: 260 Jixi Road, Hefei, Anhui, China 230022 Page 5 of 8 Tel: 0086-551-3667943 E-mail: sales@ecrim.cn Fax:0086-551-3638101

Table 9 Pin designation

S	pinout number							
functions —	HSG28D12	HSG28T12	HSG28T15	HSW28M15				
positive input	3	3	3	18				
input ground	5	1	1	1				
positve output	6	5(output 1), 6(output 2), 8(output 3)	5(output 1), 6(output 2), 8(output 3)	4,6,8,10,12,14				
negative output	8	_	_	_				
output ground	7	4(output 1), 7(output 2), 9(output 3)	4(output 1), 7(output 2), 9(output 3)	5,7,9,11,13,15				
inhibit	2	10	10	_				
case ground	4,9,10	2	2	_				
no connection	1	Note: *adjustment pinout	_	2,3,16,17				

Note: *adjustment pinout adjusts the change of output voltage through external resistance between adjustment pin and output (positive and ground)

8 Connecting diagram for typical application (Fig.5~7)

(1) connecting diagram for operation

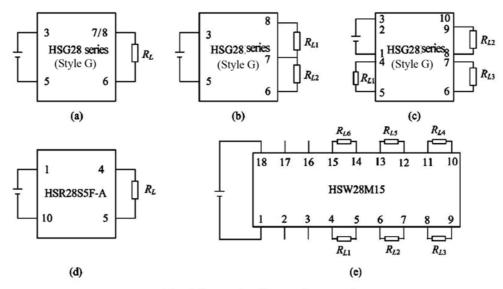


Fig. 5 Connecting diagram for operation (a) Single-output (b) Dual-output (c) Triple -output (d) Type R (e) HSW28M15

(2) connection diagram for inhibit terminal

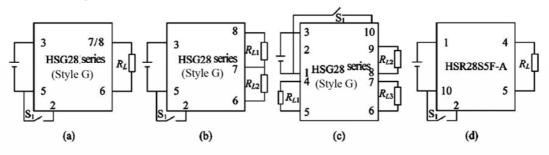


Fig. 6 Connection diagram for inhibit terminal (a) single-output (b) dual -output (c) triple -output (d) 'Style R

ADD: 260 Jixi Road, Hefei, Anhui, China 230022 Page 6 of 8 Tel: 0086-551-3667943 E-mail: sales@ecrim.cn Fax:0086-551-3638101

(3) Connecting diagram for EMI filter

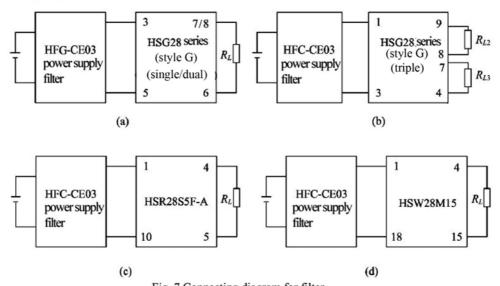
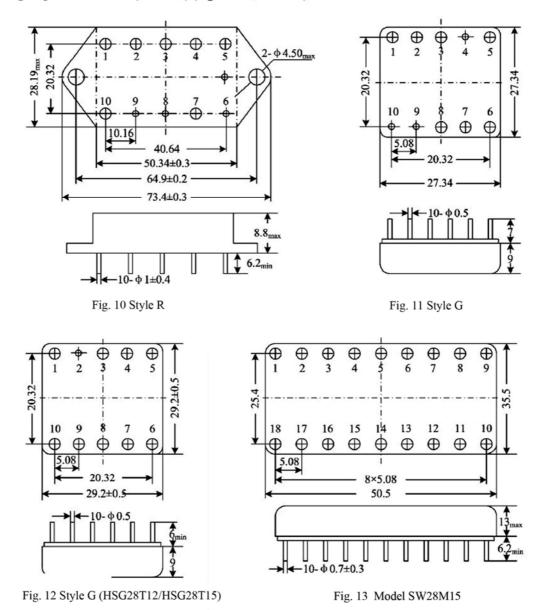


Fig. 7 Connecting diagram for filter (a) Single and dual output model $\,$ (b) Triple output model $\,$ (c) Style R $\,$ (d) Model HSW28M15

9 Package specifications (unit: mm) (Fig.10~13, Table 10)



ADD: 260 Jixi Road, Hefei, Anhui, China 230022 E-mail: sales@ecrim.cn

Page 7 of 8

Tel: 0086-551-3667943 Fax:0086-551-3638101

Table 10 Case materials

case model	header	neader olating	cover	cover plating	pin material	pin plating	sealing style	notes
UPP2727—10 (style G)	cold rolled steel	Ni	iron/nickel alloy (4J42)	Ni	iron/nickel allo (4J50)	y Ni/Au	compression seal	
UPP5328-10d (style R)	cold rolled steel	Ni	iron/nickel alloy (4J42)	Ni	copper compound	Ni/Au	compression seal p	ground pin plating is Ni
PP4833-18 (HSW28M15)	cold rolled steel (08AL)	Ni/Au	cold rolled steel (08AL)	Ni/Sn	iron/nickel allo (4J50)	y Ni/Au	compression seal	

Note: the temperature of solder pins within 10s shall not exceed 300°C

10 Part numbering key (Fig. 14)

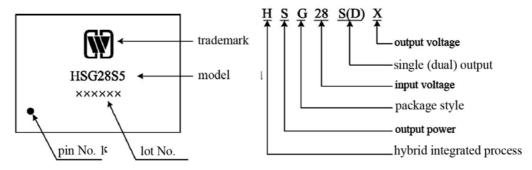


Fig. 14 Part numbering key

Application notes:

- ★ Upon power-on, be sure to correctly connect the positive and negative pole of the power supply to ensure correct power supply for fear of burning.
- ★ When carrying out the electrical performance test, the test position shall be the pinouts of the product.
- ★ Upon assembly, the bottom of the product shall fit to the circuit board closely so as to avoid damage of pins, and shockproof provision shall be added, if necessary.
- ★ Do not bend the pinouts to prevent the insulator from breaking, which affects the sealing property.
- ★ When the user places an order for the product, detailed electric performance indexes shall refer to the relevant enterprise standard.

ADD: 260 Jixi Road, Hefei, Anhui, China 230022 Page 8 of 8 Tel: 0086-551-3667943 E-mail: sales@ecrim.cn Fax:0086-551-3638101