1.0 A Low-Dropout Positive Fixed and Adjustable Voltage Regulators
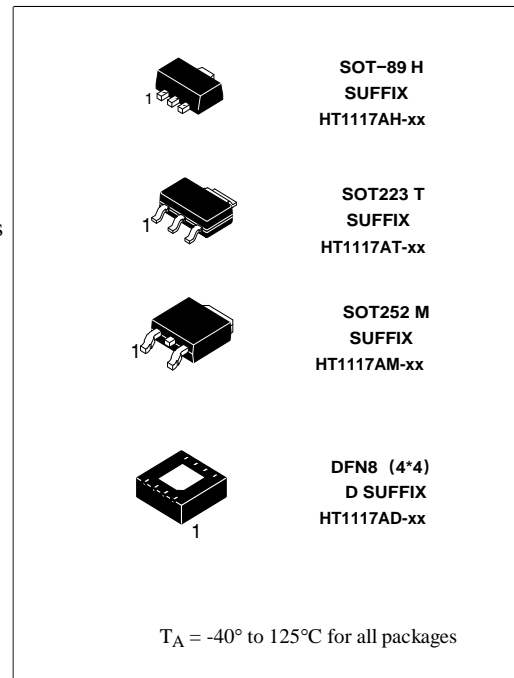
The HT1117A series of positive adjustable and fixed regulators is designed to provide 1A with high efficiency. All the internal circuitry is designed to operate down to 1.4V input-to-output differential. On-chip trimming adjusts the reference voltage to 1%. The typical current limit value of 1.5A allows the stress on both the regulator and the power source circuitry to be minimized under overload conditions.
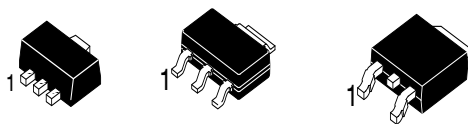
Features

- Output Current in Excess of 1.0 A
- 1.2 V Maximum Dropout Voltage at 800 mA Over Temperature
- Fixed Output Voltages of 1.5 V, 1.8 V, 1.9 V, 2.0 V, 2.5 V, 2.85 V, 3.3 V, 5.0 V, and 12 V
- Adjustable Output Voltage Option
- No Minimum Load Requirement for Fixed Voltage Output Devices
- Reference/Output Voltage Trimmed to ±1.0%
- Current Limit, Safe Operating and Thermal Shutdown Protection
- Operation to 20 V Input
- These are Pb-Free Devices

Applications

- Consumer and Industrial Equipment Point of Regulation
- Active SCSI Termination for 2.85 V Version
- Switching Power Supply Post Regulation
- Hard Drive Controllers
- Battery Chargers

SOT−89 H
SUFFIX
HT1117AH-xx

SOT223 T
SUFFIX
HT1117AT-xx

SOT252 M
SUFFIX
HT1117AM-xx

DFN8 (4*4)
D SUFFIX
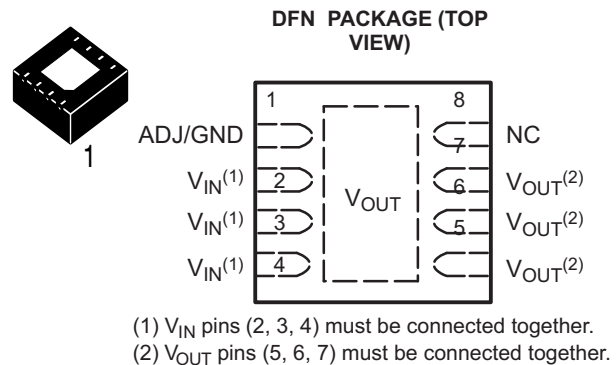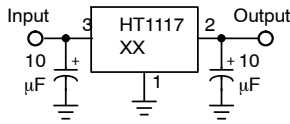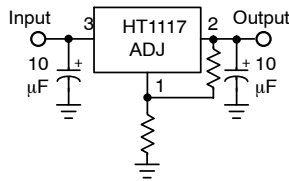HT1117AD-xx

T_A = -40° to 125°C for all packages

PIN CONFIGURATION

Pin: 1. Adjust/Ground
2. Output
3. Input

Heatsink tab is connected to Pin 2.

DFN PACKAGE (TOP VIEW)

1 ADJ/GND	8 NC
2 V_{IN}[1]	7
3 V_{IN}[1]	6 V_{OUT}[2]
4 V_{IN}[1]	5 V_{OUT}[2]

V_{OUT}

(1) V_{IN} pins (2, 3, 4) must be connected together.
(2) V_{OUT} pins (5, 6, 7) must be connected together.

TYPICAL APPLICATIONS

Figure 1. Fixed Output Regulator

Figure 2. Adjustable Output Regulator
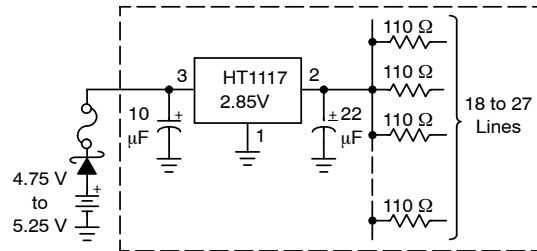
Figure 3. Active SCSI Bus Terminator

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Input Voltage (Note 1)	V_{in}	20	V
Output Short Circuit Duration (Notes 2 and 3)	–	Infinite	–

Power Dissipation and Thermal Characteristics
  Case 318H (SOT−223)
    Power Dissipation (Note 2)
    Thermal Resistance, Junction−to−Ambient, Minimum Size Pad
    Thermal Resistance, Junction−to−Case
  Case 369A (DPAK)
    Power Dissipation (Note 2)
    Thermal Resistance, Junction−to−Ambient, Minimum Size Pad
    Thermal Resistance, Junction−to−Case
	P_D
	$R_{\theta JA}$
	$R_{\theta JC}$
	
	P_D
	$R_{\theta JA}$
	$R_{\theta JC}$
		Internally Limited
		160
		15
		
		Internally Limited
		67
		6.0
			W
			°C/W
			°C/W
			
			W
			°C/W
			°C/W

Maximum Die Junction Temperature Range	T_J	−55 to 150	°C
Storage Temperature Range	T_{stg}	−65 to 150	°C
Operating Ambient Temperature Range	T_A	−40 to +125	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. This device series contains ESD protection and exceeds the following tests:
   Human Body Model (HBM), Class 2, 2000 V
   Machine Model (MM), Class B, 200 V
   Charge Device Model (CDM), Class IV, 2000 V.
2. Internal thermal shutdown protection limits the die temperature to approximately 175°C. Proper heatsinking is required to prevent activation.
   The maximum package power dissipation is: $P_D = \dfrac{T_{J(max)} - T_A}{R_{\theta JA}}$
3. The regulator output current must not exceed 1.0 A with V_{in} greater than 12 V.

ELECTRICAL CHARACTERISTICS

(C_{in} = 10 µF, C_{out} = 10 µF, for typical value T_A = 25°C, for min and max values T_A is the operating ambient temperature range that applies unless otherwise noted.) (Note 4)

Characteristic	Symbol	Min	Typ	Max	Unit
Reference Voltage, Adjustable Output Devices ($V_{in}-V_{out}$ = 2.0 V, I_{out} = 10 mA, T_A = 25°C) ($V_{in}-V_{out}$ = 1.4 V to 10 V, I_{out} = 10 mA to 800 mA) (Note 4)	V_{ref}	 1.238 1.225	 1.25 –	 1.262 1.270	V
Output Voltage, Fixed Output Devices 1.5 V (V_{in} = 3.5 V, I_{out} = 10 mA, T_A = 25 °C) (V_{in} = 2.9 to 11.5 V, I_{out} = 0 mA to 800 mA) (Note 4)	V_{out}	 1.485 1.470	 1.500 –	 1.515 1.530	V
1.8 V (V_{in} = 3.8 V, I_{out} = 10 mA, T_A = 25 °C) (V_{in} = 3.2 to 11.8 V, I_{out} = 0 mA to 800 mA) (Note 4)		1.782 1.755	1.800 –	1.818 1.845	
1.9 V (V_{in} = 3.9 V, I_{out} = 10 mA, T_A = 25 °C) (V_{in} = 3.3 to 11.9 V, I_{out} = 0 mA to 800 mA) (Note 4)		1.872 1.862	1.900 1.900	1.929 1.938	
2.0 V (V_{in} = 4.0 V, I_{out} = 10 mA, T_A = 25 °C) (V_{in} = 3.4 to 12 V, I_{out} = 0 mA to 800 mA) (Note 4)		1.970 1.960	2.000 –	2.030 2.040	
2.5 V (V_{in} = 4.5 V, I_{out} = 10 mA, T_A = 25 °C) (V_{in} = 3.9 to 10 V, I_{out} = 0 mA to 800 mA,) (Note 4)		2.475 2.450	2.500 –	2.525 2.550	

2.85 V (V_{in} = 4.85 V, I_{out} = 10 mA, T_A = 25 °C)
         (V_{in} = 4.25 V to 10 V, I_{out} = 0 mA to 800 mA) (Note 4)
         (V_{in} = 4.0 V, I_{out} = 0 mA to 500 mA) (Note 4)
		2.821
		2.790
		2.790
			2.850
			–
			–
				2.879
				2.910
				2.910	

3.3 V (V_{in} = 5.3 V, I_{out} = 10 mA, T_A = 25 °C) (V_{in} = 4.75 V to 10 V, I_{out} = 0 mA to 800 mA) (Note 4)		3.267 3.235	3.300 –	3.333 3.365	
5.0 V (V_{in} = 7.0 V, I_{out} = 10 mA, T_A = 25 °C) (V_{in} = 6.5 V to 12 V, I_{out} = 0 mA to 800 mA) (Note 4)		4.950 4.900	5.000 –	5.050 5.100	
12 V (V_{in} = 14 V, I_{out} = 10 mA, T_A = 25 °C) (V_{in} = 13.5 V to 20 V, I_{out} = 0 mA to 800 mA) (Note 4)		11.880 11.760	12.000 –	12.120 12.240	
Line Regulation (Note 5) Adjustable (V_{in} = 2.75 V to 16.25 V, I_{out} = 10 mA)	Reg_{line}	–	0.04	0.1	%
1.5 V (V_{in} = 2.9 to 11.5 V, I_{out} = 0 mA) 1.8 V (V_{in} = 3.2 to 11.8 V, I_{out} = 0 mA) 1.9 V (V_{in} = 3.3 to 11.9 V, I_{out} = 0 mA) 2.0 V (V_{in} = 3.4 to 12 V, I_{out} = 0 mA) 2.5 V (V_{in} = 3.9 to 10 V, I_{out} = 0 mA) 2.85 V (V_{in} = 4.25 V to 10 V, I_{out} = 0 mA) 3.3 V (V_{in} = 4.75 V to 15 V, I_{out} = 0 mA) 5.0 V (V_{in} = 6.5 V to 15 V, I_{out} = 0 mA) 12 V (V_{in} = 13.5 V to 20 V, I_{out} = 0 mA)		– – – – – – – – –	0.3 0.4 0.5 0.5 0.5 0.8 0.8 0.9 1.0	1.0 1.0 2.5 2.5 2.5 3.0 4.5 6.0 7.5	mV
Load Regulation (Note 5) Adjustable (I_{out} = 10 mA to 800 mA, V_{in} = 4.25 V)	Reg_{line}	–	0.2	0.4	%
1.5 V (I_{out} = 0 mA to 800 mA, V_{in} = 2.9 V) 1.8 V (I_{out} = 0 mA to 800 mA, V_{in} = 3.2 V) 1.9 V (I_{out} = 0 mA to 800 mA, V_{in} = 3.3 V) 2.0 V (I_{out} = 0 mA to 800 mA, V_{in} = 3.4 V) 2.5 V (I_{out} = 0 mA to 800 mA, V_{in} = 3.9 V) 2.85 V (I_{out} = 0 mA to 800 mA, V_{in} = 4.25 V) 3.3 V (I_{out} = 0 mA to 800 mA, V_{in} = 4.75 V) 5.0 V (I_{out} = 0 mA to 800 mA, V_{in} = 6.5 V) 12 V (I_{out} = 0 mA to 800 mA, V_{in} = 13.5 V)		– – – – – – – – –	2.3 2.6 2.7 3.0 3.3 3.8 4.3 6.7 16	5.5 6.0 6.0 6.0 7.5 8.0 10 15 28	mV
Dropout Voltage (Measured at V_{out} – 100 mV) (I_{out} = 100 mA) (I_{out} = 500 mA) (I_{out} = 800 mA)	$V_{in}-V_{out}$	 – – –	 0.95 1.01 1.07	 1.10 1.15 1.20	V
Output Current Limit ($V_{in}-V_{out}$ = 5.0 V, T_A = 25°C, Note 6)	I_{out}	1000	1500	2200	mA
Minimum Required Load Current for Regulation, Adjustable Output Devices (V_{in} = 15 V)	$I_{L(min)}$	–	0.8	5.0	mA

ELECTRICAL CHARACTERISTICS (continued)

(C_{in} = 10 μF, C_{out} = 10 μF, for typical value T_A = 25°C, for min and max values T_A is the operating ambient temperature range that applies unless otherwise noted.) (Note 4)

| Characteristic	Symbol	Min	Typ	Max	Unit
Quiescent Current	I_Q				mA
1.5 V (V_{in} = 11.5 V)		–	3.6	10	
1.8 V (V_{in} = 11.8 V)		–	4.2	10	
1.9 V (V_{in} = 11.9 V)		–	4.3	10	
2.0 V (V_{in} = 12 V)		–	4.5	10	
2.5 V (V_{in} = 10 V)		–	5.2	10	
2.85 V (V_{in} = 10 V)		–	5.5	10	
3.3 V (V_{in} = 15 V)		–	6.0	10	
5.0 V (V_{in} = 15 V)		–	6.0	10	
12 V (V_{in} = 20 V)		–	6.0	10	
Thermal Regulation (T_A = 25°C, 30 ms Pulse)		–	0.01	0.1	%/W
Ripple Rejection (V_{in}–V_{out} = 6.4 V, I_{out} = 500 mA, 10 V_{pp} 120 Hz Sinewave)	RR				dB
Adjustable		67	73	–	
1.5 V		66	72	–	
1.8 V		66	70	–	
1.9 V		66	72	–	
2.0 V		64	70	–	
2.5 V		62	68	–	
2.85 V		62	68	–	
3.3 V		60	64	–	
5.0 V		57	61	–	
12 V		50	54	–	
Adjustment Pin Current (V_{in} = 11.25 V, I_{out} = 800 mA)	I_{adj}	–	52	120	μA
Adjust Pin Current Change (V_{in}–V_{out} = 1.4 V to 10 V, I_{out} = 10 mA to 800 mA)	ΔI_{adj}	–	0.4	5.0	μA
Temperature Stability	S_T	–	0.5	–	%
Long Term Stability (T_A = 25°C, 1000 Hrs End Point Measurement)	S_t	–	0.3	–	%
RMS Output Noise (f = 10 Hz to 10 kHz)	N	–	0.003	–	%V_{out}
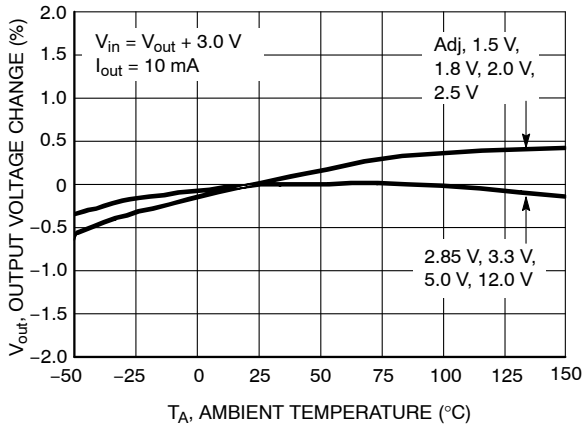
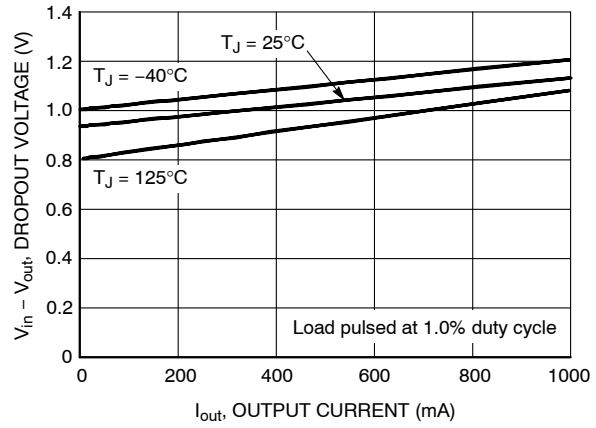**Figure 4. Output Voltage Change
vs. Temperature**

**Figure 5. Dropout Voltage
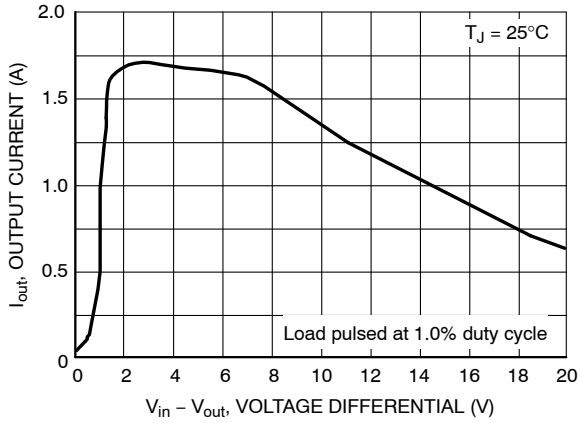vs. Output Current**

**Figure 6. Output Short Circuit Current
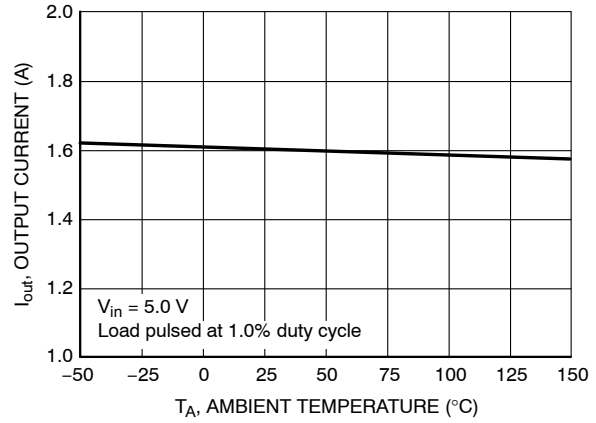vs. Differential Voltage**

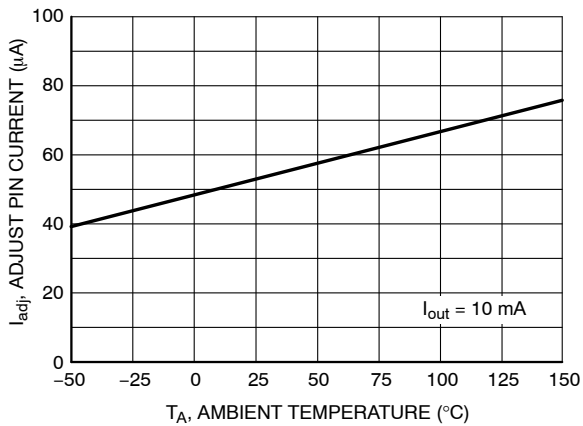**Figure 7. Output Short Circuit Current
vs. Temperature**

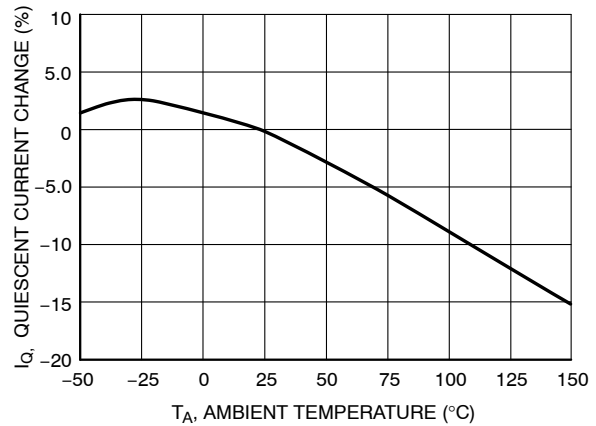**Figure 8. Adjust Pin Current
vs. Temperature**
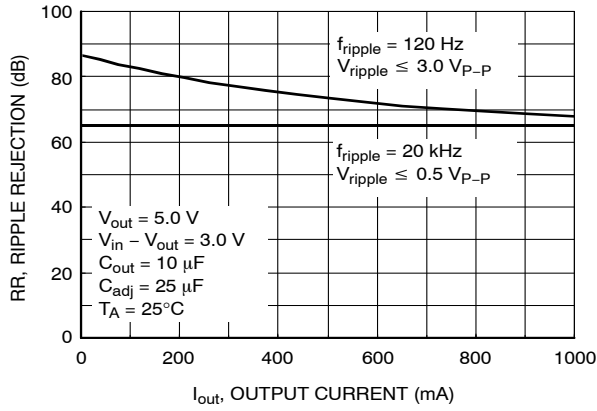

**Figure 9. Quiescent Current Change
vs. Temperature**

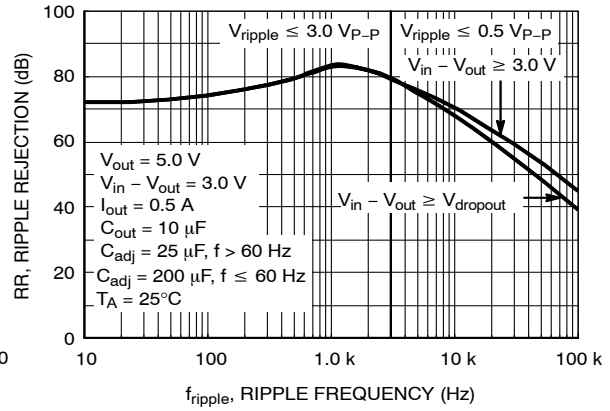**Figure 10. HT1117AT-ADJ Ripple Rejection vs. Output Current**
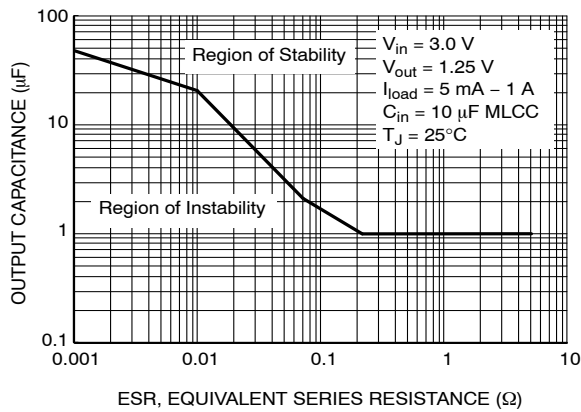

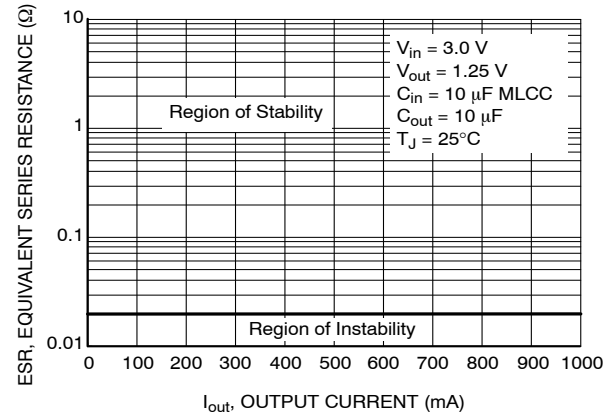**Figure 11. HT1117AT-ADJ Ripple Rejection vs. Frequency**

Figure 12. Output Capacitance vs. ESR

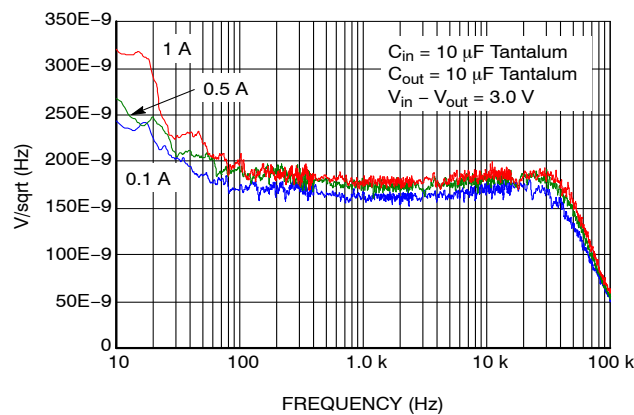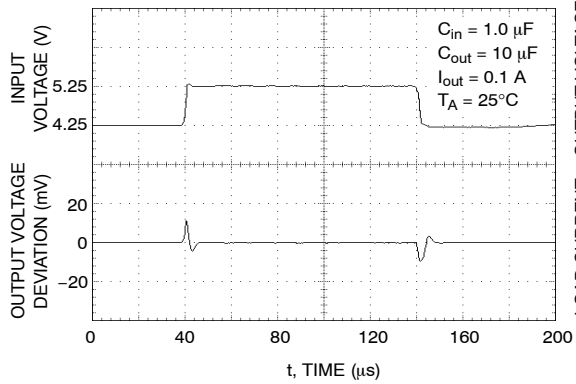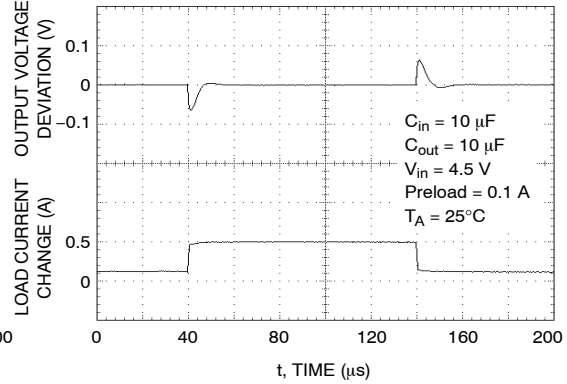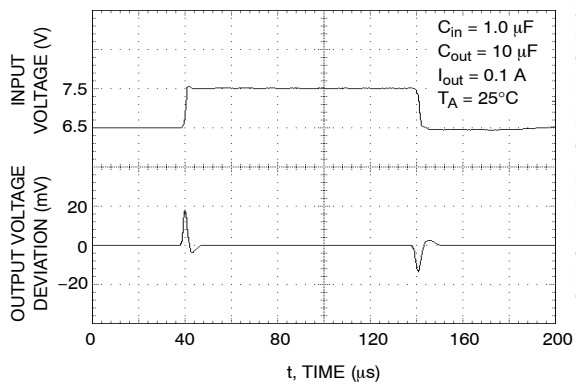**Figure 13. Typical ESR vs. Output Current**

Figure 14. Output Spectral Noise Density vs. Frequency, V_{out} = 1V5
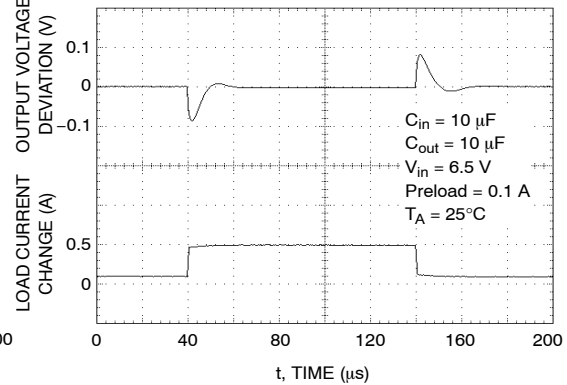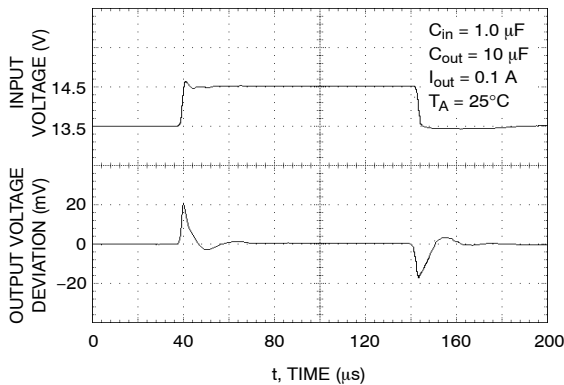
Figure 15. HT1117AT-285
Line Transient Response

Figure 16. HT1117AT-285 Load
Transient Response
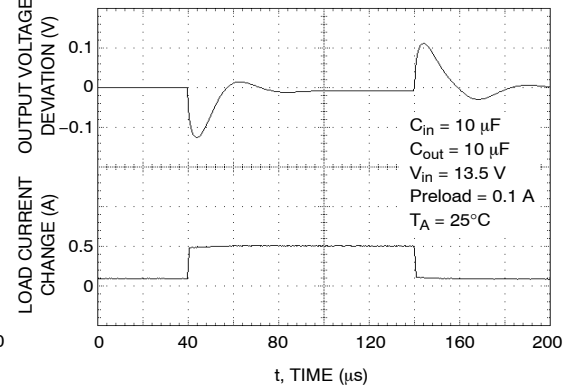

Figure 17. HT1117AT-50
Line Transient Response

Figure 18. HT1117AT-50
Load Transient Response

Figure 19. HT1117AT-12
Line Transient Response

Figure 20. HT1117AT-12
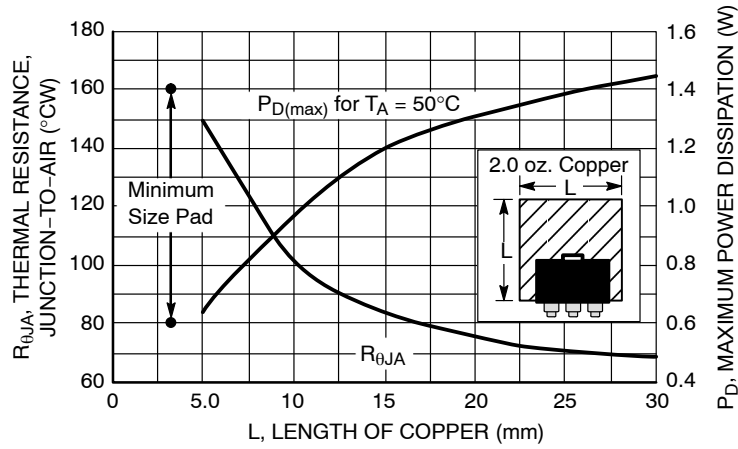Load Transient Response

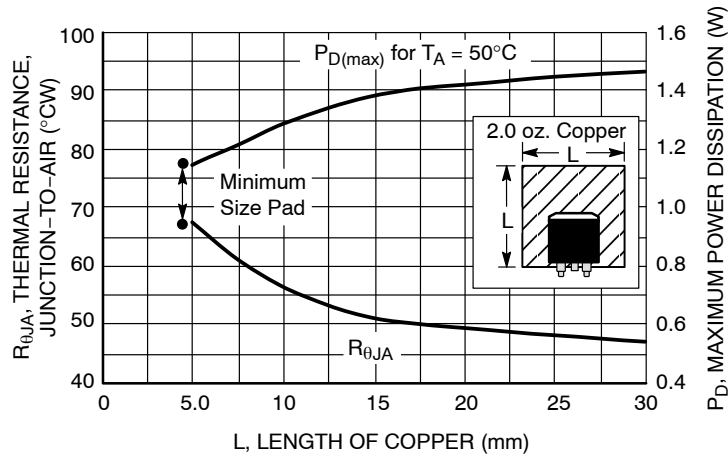**Figure 21. SOT−223 Thermal Resistance and Maximum Power Dissipation vs. P.C.B. Copper Length**

Figure 22. DPAK Thermal Resistance and Maximum Power Dissipation vs. P.C.B. Copper Length

Introduction

The HT1117 features a significant reduction in dropout voltage along with enhanced output voltage accuracy and temperature stability when compared to older industry standard three−terminal adjustable regulators. These devices contain output current limiting, safe operating area compensation and thermal shutdown protection making them designer friendly for powering numerous consumer and industrial products. The HT1117 series is pin compatible with the older LM317 and its derivative device types.

Output Voltage

The typical application circuits for the fixed and adjustable output regulators are shown in Figures 23 and 24. The adjustable devices are floating voltage regulators. They develop and maintain the nominal 1.25 V reference voltage between the output and adjust pins. The reference voltage is programmed to a constant current source by resistor R1, and this current flows through R2 to ground to set the output voltage. The programmed current level is usually selected to be greater than the specified 5.0 mA minimum that is required for regulation. Since the adjust pin current, I_{adj}, is significantly lower and constant with respect to the programmed load current, it generates a small output voltage error that can usually be ignored. For the fixed output devices R1 and R2 are included within the device and the ground current I_{gnd}, ranges from 3.0 mA to 5.0 mA depending upon the output voltage.

External Capacitors

Input bypass capacitor C_{in} may be required for regulator stability if the device is located more than a few inches from the power source. This capacitor will reduce the circuit's sensitivity when powered from a complex source impedance and significantly enhance the output transient response. The input bypass capacitor should be mounted with the shortest possible track length directly across the regulator's input and ground terminals. A 10 μF ceramic or tantalum capacitor should be adequate for most applications.
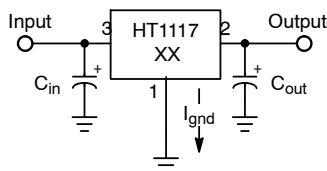

Figure 23. Fixed Output Regulator

Frequency compensation for the regulator is provided by capacitor C_{out} and its use is mandatory to ensure output stability. A minimum capacitance value of 4.7 μF with an equivalent series resistance (ESR) that is within the limits of 33 mΩ (typ) to 2.2 Ω is required. See Figures 12 and 13. The capacitor type can be ceramic, tantalum, or aluminum electrolytic as long as it meets the minimum capacitance value and ESR limits over the circuit's entire operating temperature range. Higher values of output capacitance can be used to enhance loop stability and transient response with the additional benefit of reducing output noise.
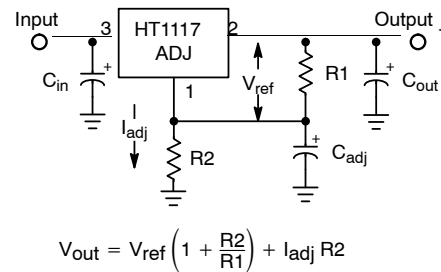

$$V_{out} = V_{ref}\left(1 + \frac{R2}{R1}\right) + I_{adj}\, R2$$

Figure 24. Adjustable Output Regulator

The output ripple will increase linearly for fixed and adjustable devices as the ratio of output voltage to the reference voltage increases. For example, with a 12 V regulator, the output ripple will increase by 12 V/1.25 V or 9.6 and the ripple rejection will decrease by 20 log of this ratio or 19.6 dB. The loss of ripple rejection can be restored to the values shown with the addition of bypass capacitor C_{adj}, shown in Figure 24. The reactance of C_{adj} at the ripple frequency must be less than the resistance of R1. The value of R1 can be selected to provide the minimum required load current to maintain regulation and is usually in the range of 100 Ω to 200 Ω.

$$C_{adj} > \frac{1}{2\,\pi\, f_{ripple}\, R1}$$

The minimum required capacitance can be calculated from the above formula. When using the device in an application that is powered from the AC line via a transformer and a full wave bridge, the value for C_{adj} is:

$f_{ripple} = 120\ Hz,\ R1 = 120\ \Omega,\ then\ C_{adj} > 11.1\ \mu F$

The value for C_{adj} is significantly reduced in applications where the input ripple frequency is high. If used as a post regulator in a switching converter under the following conditions:

$f_{ripple} = 50\ kHz,\ R1 = 120\ \Omega,\ then\ C_{adj} > 0.027\ \mu F$

Figures 10 and 11 shows the level of ripple rejection that is obtainable with the adjust pin properly bypassed.

Protection Diodes

The HT1117 family has two internal low impedance diode paths that normally do not require protection when used in the typical regulator applications. The first path connects between V_{out} and V_{in}, and it can withstand a peak surge current of about 15 A. Normal cycling of V_{in} cannot generate a current surge of this magnitude. Only when V_{in} is shorted or crowbarred to ground and C_{out} is greater than 50 μF, it becomes possible for device damage to occur. Under these conditions, diode D1 is required to protect the device. The second path connects between C_{adj} and V_{out}, and it can withstand a peak surge current of about 150 mA. Protection diode D2 is required if the output is shorted or crowbarred to ground and C_{adj} is greater than 1.0 μF.
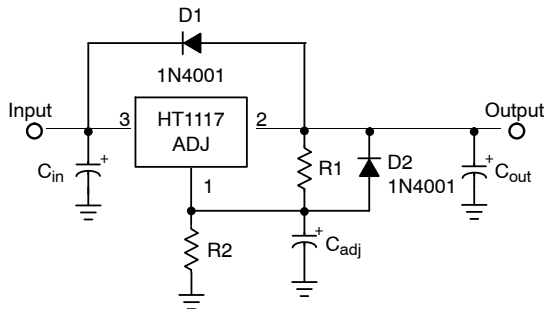

Figure 25. Protection Diode Placement

A combination of protection diodes D1 and D2 may be required in the event that V_{in} is shorted to ground and C_{adj} is greater than 50 μF. The peak current capability stated for the internal diodes are for a time of 100 μs with a junction temperature of 25°C. These values may vary and are to be used as a general guide.

Load Regulation

The HT1117 series is capable of providing excellent load regulation; but since these are three terminal devices, only partial remote load sensing is possible. There are two conditions that must be met to achieve the maximum available load regulation performance. The first is that the top side of programming resistor R1 should be connected as close to the regulator case as practicable. This will minimize the voltage drop caused by wiring resistance RW + from appearing in series with reference voltage that is across R1.

The second condition is that the ground end of R2 should be connected directly to the load. This allows true Kelvin sensing where the regulator compensates for the voltage drop caused by wiring resistance RW −.
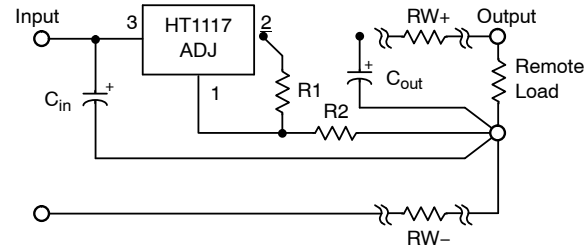


Figure 26. Load Sensing

Thermal Considerations

This series contains an internal thermal limiting circuit that is designed to protect the regulator in the event that the maximum junction temperature is exceeded. When activated, typically at 175°C, the regulator output switches off and then back on as the die cools. As a result, if the device is continuously operated in an overheated condition, the output will appear to be oscillating. This feature provides protection from a catastrophic device failure due to accidental overheating. It is not intended to be used as a substitute for proper heatsinking. The maximum device power dissipation can be calculated by:

$$P_D = \frac{T_{J(max)} - T_A}{R_{\theta JA}}$$

The devices are available in surface mount SOT−223 and DPAK packages. Each package has an exposed metal tab that is specifically designed to reduce the junction to air thermal resistance, $R_{\theta JA}$, by utilizing the printed circuit board copper as a heat dissipater. Figures 21 and 22 show typical $R_{\theta JA}$ values that can be obtained from a square pattern using economical single sided 2.0 ounce copper board material. The final product thermal limits should be tested and quantified in order to insure acceptable performance and reliability. The actual $R_{\theta JA}$ can vary considerably from the graphs shown. This will be due to any changes made in the copper aspect ratio of the final layout, adjacent heat sources, and air flow.

$$I_{out} = \frac{V_{ref}}{R} + I_{adj}$$
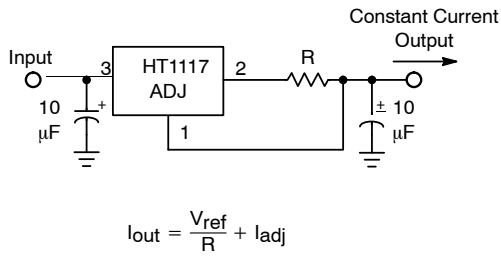
Figure 27. Constant Current Regulator
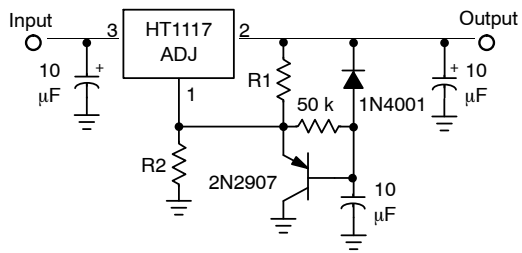
Figure 28. Slow Turn–On Regulator

$$V_{out}(Off) = V_{ref}$$
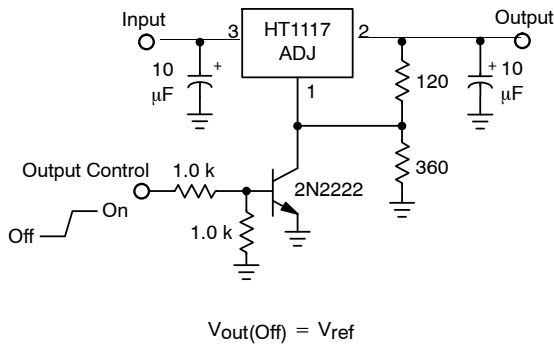
Figure 29. Regulator with Shutdown
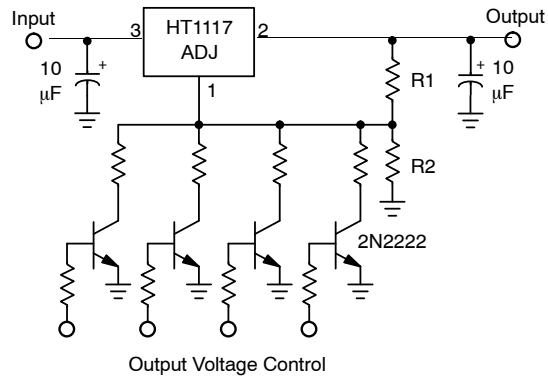
Resistor R2 sets the maximum output voltage. Each transistor reduces the output voltage when turned on.

Figure 30. Digitally Controlled Regulator
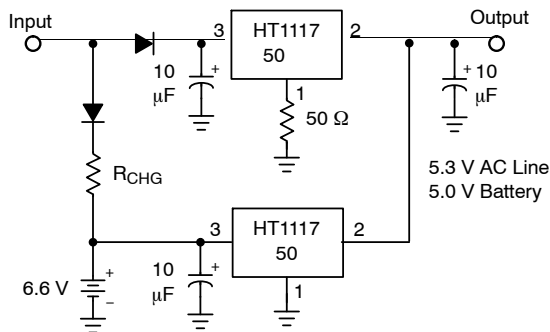
The 50 Ω resistor that is in series with the ground pin of the upper regulator level shifts its output 300 mV higher than the lower regulator. This keeps the lower regulator off until the input source is removed.

Figure 31. Battery Backed–Up Power Supply
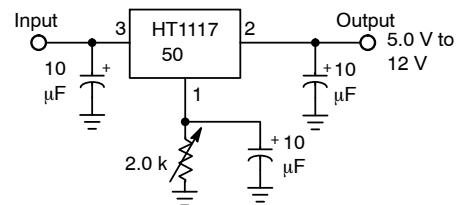
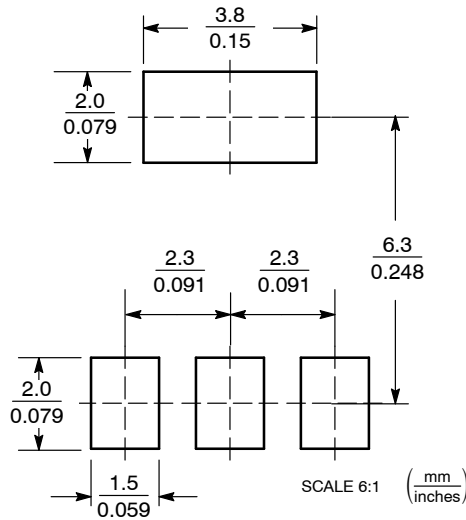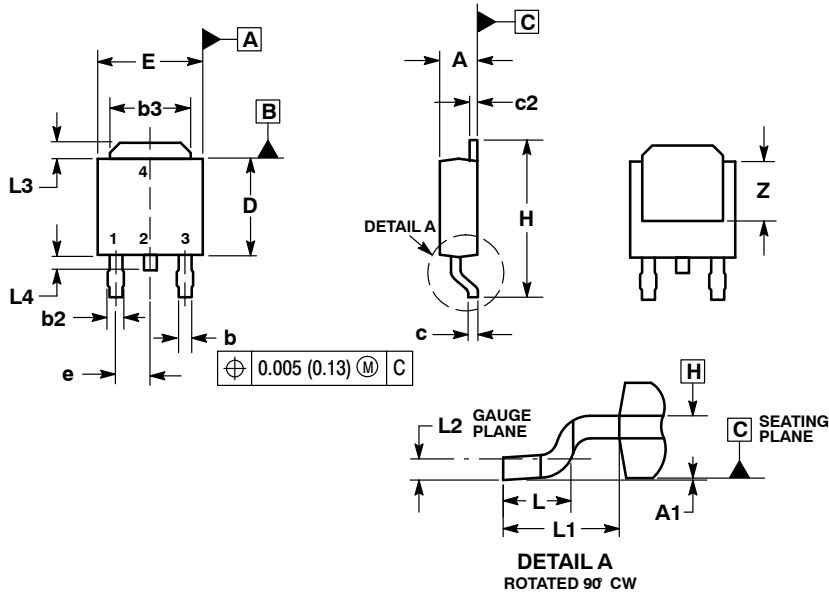**Figure 32. Adjusting Output of Fixed Voltage Regulators**

SOT−223
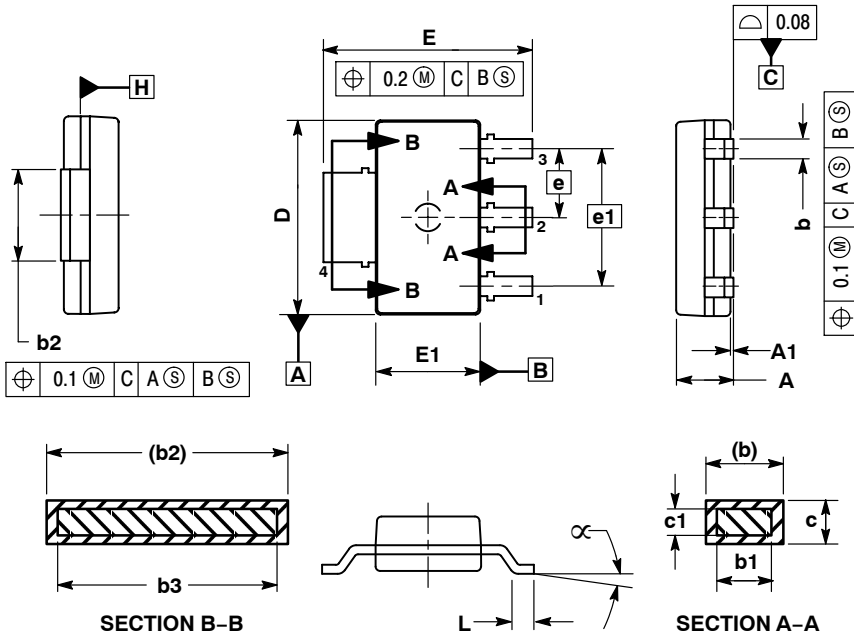
NOTES:
1. DIMENSIONS ARE IN MILLIMETERS.
2. INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 1994.
3. DIMENSION E1 DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION. INTERLEAD FLASH OR PROTRUSION SHALL NOT EXCEED 0.23 PER SIDE.
4. DIMENSIONS b AND b2 DO NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 TOTAL IN EXCESS OF THE b AND b2 DIMENSIONS AT MAXIMUM MATERIAL CONDITION.
5. TERMINAL NUMBERS ARE SHOWN FOR REFERENCE ONLY.
6. DIMENSIONS D AND E1 ARE TO BE DETERMINED AT DATUM PLANE H.

DIM	MILLIMETERS	
---	MIN	MAX
A	---	1.80
A1	0.02	0.11
b	0.60	0.88
b1	0.60	0.80
b2	2.90	3.10
b3	2.90	3.05
c	0.24	0.35
c1	0.24	0.30
D	6.30	6.70
E	6.70	7.30
E1	3.30	3.70
e	2.30	
e1	4.60	
L	0.25	---
∝	0°	10°

SECTION B−B

SECTION A−A

SOLDERING FOOTPRINT*

$\dfrac{3.8}{0.15}$

$\dfrac{2.0}{0.079}$

$\dfrac{2.3}{0.091}$ $\dfrac{2.3}{0.091}$ $\dfrac{6.3}{0.248}$

$\dfrac{2.0}{0.079}$

$\dfrac{1.5}{0.059}$

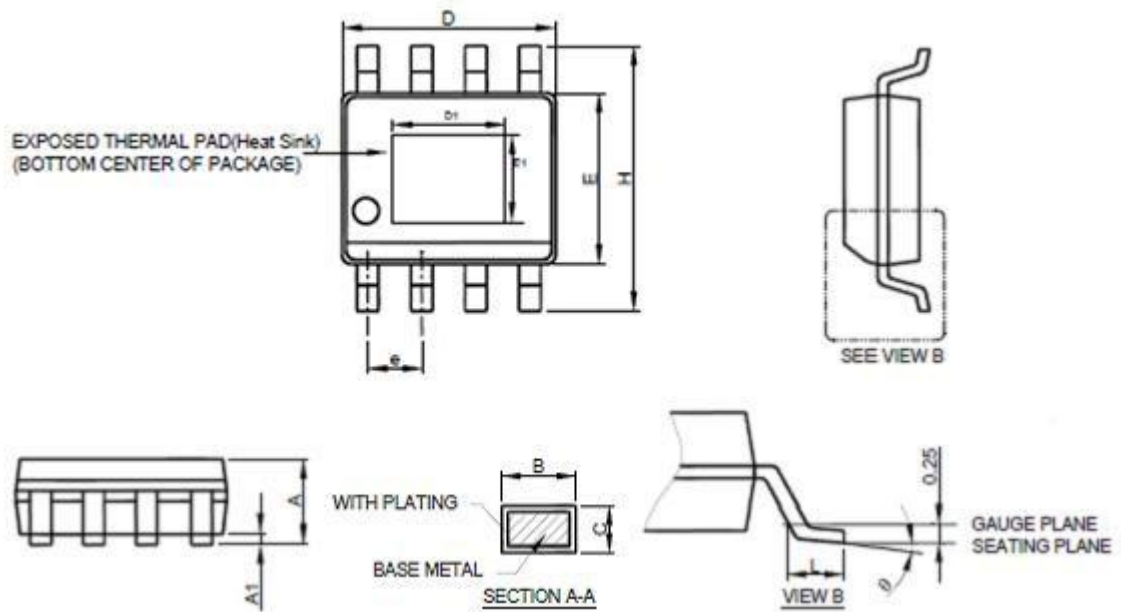SCALE 6:1 $\left(\dfrac{mm}{inches}\right)$

TO252

NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: INCHES.
3. THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSIONS b3, L3 and Z.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.006 INCHES PER SIDE.
5. DIMENSIONS D AND E ARE DETERMINED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY.
6. DATUMS A AND B ARE DETERMINED AT DATUM PLANE H.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.086	0.094	2.18	2.38
A1	0.000	0.005	0.00	0.13
b	0.025	0.035	0.63	0.89
b2	0.030	0.045	0.76	1.14
b3	0.180	0.215	4.57	5.46
c	0.018	0.024	0.46	0.61
c2	0.018	0.024	0.46	0.61
D	0.235	0.245	5.97	6.22
E	0.250	0.265	6.35	6.73
e	0.090 BSC		2.29 BSC	
H	0.370	0.410	9.40	10.41
L	0.055	0.070	1.40	1.78
L1	0.108 REF		2.74 REF	
L2	0.020 BSC		0.51 BSC	
L3	0.035	0.050	0.89	1.27
L4	---	0.040	---	1.01
Z	0.155	---	3.93	---

DETAIL A
ROTATED 90° CW

SOLDERING FOOTPRINT*
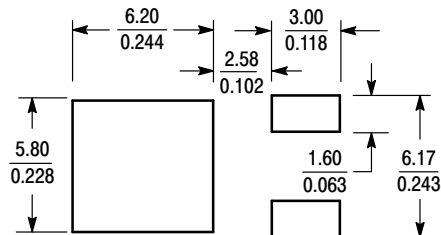
ESOP8


Symbol	Dimensions In Millimeters	
	Min	Max
A	1.35	1.75
A1	0.05	0.25
B	0.31	0.51
C	0.17	0.25
D	4.70	5.10
E	3.70	4.10
e	1.27BSC	
H	5.80	6.20

SOT−89

TOP VIEW

SIDE VIEW

BOTTOM VIEW
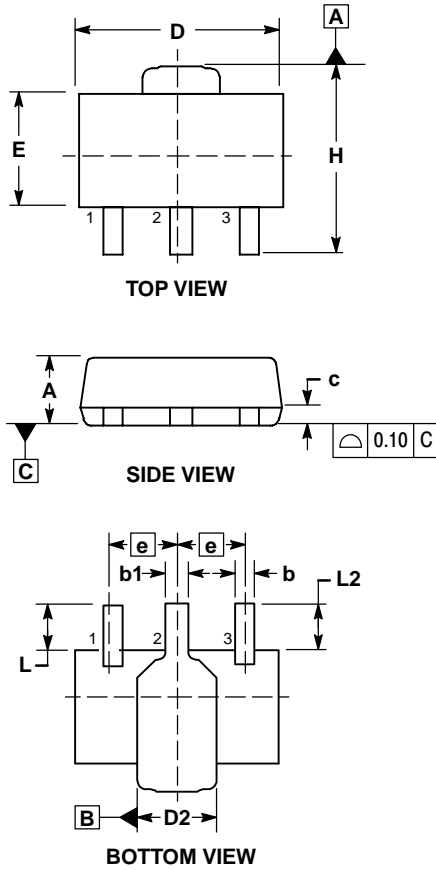
NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. LEAD THICKNESS INCLUDES LEAD FINISH.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.
5. DIMENSIONS L, L2, D2, AND H ARE MEASURED AT DATUM PLANE C.
6. CENTER LEAD CONTOUR MAY VARY WITHIN THE REGION DEFINED BY DIMENSION E.
7. DIMENSION D2 IS DEFINED AT ITS WIDEST POINT.

DIM	MILLIMETERS	
	MIN	MAX
A	1.40	1.60
b	0.38	0.47
b1	0.46	0.55
c	0.40	0.44
D	4.40	4.60
D2	1.60	1.90
E	2.40	2.60
e	1.50 BSC	
H	4.05	4.25
L	0.89	1.20

RECOMMENDED MOUNTING FOOTPRINT*

PACKAGE OUTLINE

2.00

4.45

1.57

0.86

2X 0.50

0.58

2X 1.50
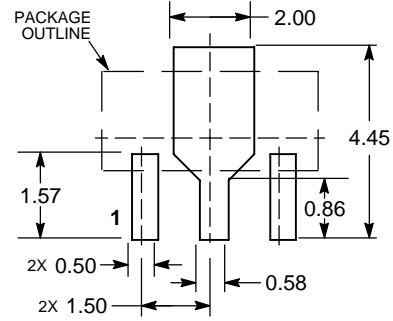
DIMENSIONS: MILLIMETERS

*For additional information on our Pb−Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

DFN8 4*4

DRJ (S-PWSON-N8)   PLASTIC SMALL OUTLINE NO-LEAD

Pin 1 Index Area
Top and Bottom

Exposed Thermal Die Pad

Bottom View

4205439/C 12/10