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**SPEC. NUMBER**PRODUCT GROUP  
TFT-LCD**REV.**  
P2**ISSUE DATE**  
2011.3.23**PAGE**  
1 OF 27**TITLE : HT260WXC-100 Product Specification****Rev. P2**

 <b>京东方</b> <b>BOE</b>	<b>PRODUCT GROUP</b>		REV	ISSUE DATE
	TFT- LCD PRODUCT		P2	2011.3.23
<b>SPEC. NUMBER</b>	<b>SPEC. TITLE</b>			<b>PAGE</b>
	HT260WXC-100 Product Specification Rev P2			2 OF 26
<b>REVISION HISTORY</b>				
REV.	ECN NO.	DESCRIPTION OF CHANGES	DATE	PREPARED
PO		Initial Release	10.12.30	金在光
P1		量产对应 Release	11.2.15	金在光
P2		BL CN modify	11.3.23	石美霞
B2010-6011-O(2/3)				A4(210 X 297)

 <b>京东方 BOE</b>	<b>PRODUCT GROUP</b>	<b>REV</b>	<b>ISSUE DATE</b>
	TFT- LCD PRODUCT	P2	2011.3.23
<b>SPEC. NUMBER</b>	<b>SPEC. TITLE</b>		<b>PAGE</b>
	HT260WXC-100 Product Specification Rev P2		3 OF 27

## Contents

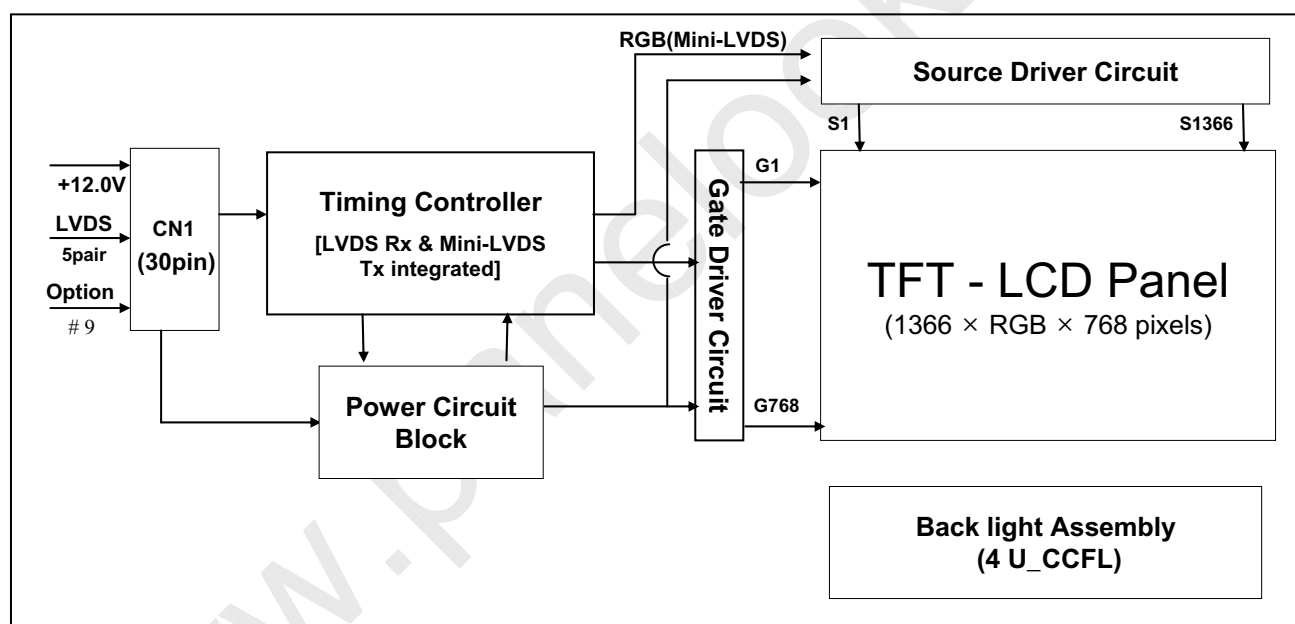
No	ITEM	Page
	REVISIONS HISTORY	2
	CONTENTS	3
1	GENERAL DESCRIPTION	4
	1.1 Introduction	
	1.2 Features	
	1.3 Applications	
	1.4 General Specification	
2	ABSOLUTE MAXIMUM RATINGS	6
3	ELECTRICAL SPECIFICATIONS	7
4	INTERFACE CONNECTION	9
	4.1 Module Input Signal & power	
	4.2 Back-light Interface Connection	
	4.3 LVDS Interface	
5	SIGNAL TIMING SPECIFICATIONS	12
	5.1 Timing Parameter	
	5.2 Signal Timing Waveform	
	5.3 Input Signals, Basic Display Colors & Cray Scale Of Colors	
	5.4 Power Sequence	
6	OPTICAL SPECIFICATIONS	16
7	MECHANICAL CHARACTERISTICS	18
8	RELIABILITY	19
9	PRODUCT SERIAL NUMBER	20
10	PACKING INFORMATION	21
11	HANDING & CAUTIONS	23
12	APPENDIX	24

 <b>京东方</b> <b>BOE</b>	<b>PRODUCT GROUP</b>	<b>REV</b>	<b>ISSUE DATE</b>
	TFT- LCD PRODUCT	P2	2011.3.23
<b>SPEC. NUMBER</b>	<b>SPEC. TITLE</b>		<b>PAGE</b>
	HT260WXC-100 Product Specification Rev P2		4 OF 27

## 1.0 GENERAL DESCRIPTION

### 1.1 Introduction

HT260WXC-100 is a color active matrix TFT LCD module using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This module has a 26.0inch diagonally measured active area with WXGA resolutions (1366 horizontal by 768 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe and this module can display 16.7M colors. The TFT-LCD panel used for this module is adapted for a low reflection and higher color type.



### 1.2 Features

- LVDS Interface with 1 pixel / clock
- High-speed response
- Lower Color shift Image Quality
- 6-bit Hi-FRC color depth, display 16.7M colors
- High luminance and contrast ratio, low reflection and wide viewing angle
- DE (Data Enable) only mode
- RoHS Compliant

 <b>京东方 BOE</b>	<b>PRODUCT GROUP</b>	<b>REV</b>	<b>ISSUE DATE</b>
	TFT- LCD PRODUCT	P2	2011.3.23
<b>SPEC. NUMBER</b>	<b>SPEC. TITLE</b> HT260WXC-100 Product Specification Rev P2		<b>PAGE</b> 5 OF 27


### 1.3 Application

- Home Alone Multimedia TFT-LCD TV
- Display Terminals for Control System
- High Definition TV(HD TV)
- AV application Products

### 1.4 General Specification

< Table 1. General Specifications >

Parameter	Specification	Unit	Remarks
Active area	575.769(W) x 323.712(H)	mm	
Number of pixels	1366(H) × 768(V)	pixels	
Pixel pitch	140.5(H) × RGB × 421.5(V)	μm	
Pixel arrangement	Pixels RGB stripe arrangement		
Display colors	16.7M(6bits + Hi FRC)	colors	
Display mode	Transmission mode, Normally White		
Outline Dimension	626.0(H) × 373.0(V) × 32.0(D) (typ.)	mm	
Weight	3700 (typ.)	gram	
Power Consumption	Total 40.6Watt (Typ.) (Logic=2W, Lamp=38.6W [ $I_{BL}=7mA$ ])	Watt	
Surface Treatment	Haze 25%, 3H, Semi-glare treatment (Front Polarizer)		

	<b>PRODUCT GROUP</b>	<b>REV</b>	<b>ISSUE DATE</b>
	TFT- LCD PRODUCT	P2	2011.3.23
<b>SPEC. NUMBER</b>	<b>SPEC. TITLE</b>		<b>PAGE</b>
	HT260WXC-100 Product Specification Rev P2		6 OF 27

## 2.0 ABSOLUTE MAXIMUM RATINGS

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

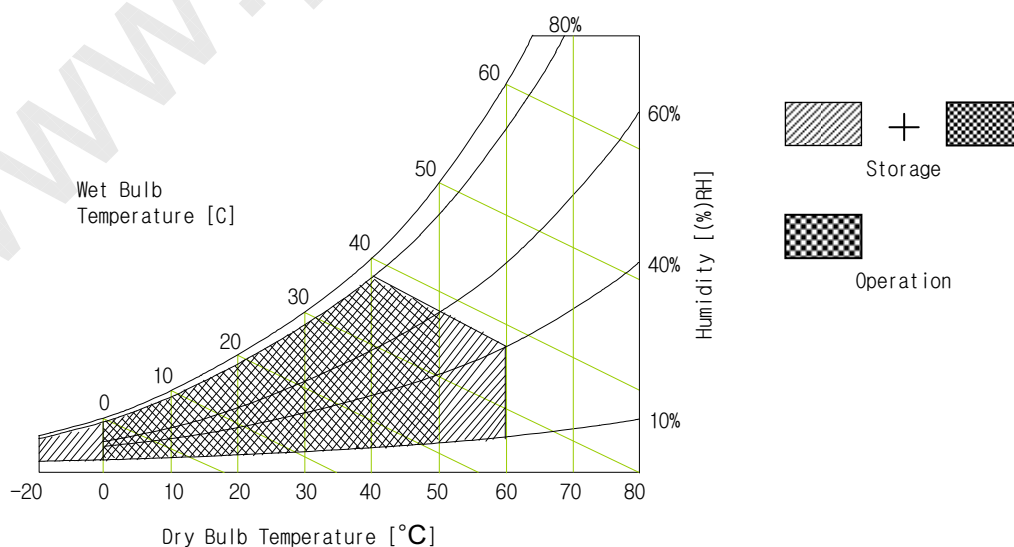
< Table 2. LCD Module Electrical Specifications >

[VSS=GND=0V]

Parameter	Symbol	Min.	Max.	Unit	Remarks
Power Supply Voltage	VDD	VSS-0.3	13	V	Ta = 25 °C
Back-light Lamp Current	IBL	6.0	9.0	mArms	
Back-light Lamp Frequency	FL	40	80	KHz	
Operating Temperature	T <sub>OP</sub>	0	+50	°C	1)
	T <sub>SUR</sub>	0	+60	°C	
Storage Temperature	T <sub>ST</sub>	-20	+60	°C	
Operating Ambient Humidity	Hop	10	80	%RH	
Storage Humidity	Hst	10	80	%RH	

Note : 1) Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39 °C max. and no condensation of water.




 <b>京东方</b> <b>BOE</b>	<b>PRODUCT GROUP</b>	<b>REV</b>	<b>ISSUE DATE</b>
	TFT- LCD PRODUCT	P2	2011.3.23
<b>SPEC. NUMBER</b>	<b>SPEC. TITLE</b> HT260WXC-100 Product Specification Rev P2		<b>PAGE</b> 7 OF 27

### 3.0 ELECTRICAL SPECIFICATIONS

< Table 3. LCD Module Electrical Specifications >

[Ta =25±2 °C]

Parameter		Symbol	Values			Unit	Notes
			Min	Typ	Max		
Power Supply Input Voltage		VDD	11.4	12	12.6	V	
Power Supply Ripple Voltage		VRP			300	mV	
Power Supply Current		IDD	-	170	300	mA	1
Power Consumption of VDD		PDD	-	2	3.5	Watt	
Rush current		IRUSH	-	-	3.0	A	2
LVDS Interface	Differential Input High Threshold Voltage	VLVTH	+100			mV	
	Differential Input Low Threshold Voltage	VLVTL			-100	mV	
	Common Input Voltage	VLVC	1.0	1.2	1.4	V	
CMOS Interface	Input High Threshold Voltage	VIH	2.7	-	3.3	V	
	Input Low Threshold Voltage	VIL	0	-	0.7	V	
Back-light Lamp Voltage		VBL	1242	1380	1518	Vrms	IBL=7.0 mA
Back-light Lamp Current		IBL	6.5	7.0	7.5	mArms	
Back-light Lamp operating Frequency		FL	40	-	80	KHz	3
Lamp Start Voltage		Vs	-	-	2200	Vrms	25°C, 4
			-	-	2400	Vrms	0°C, 4
Lamp Life			50,000	-	-	Hrs	IBL=7.0mA, 5
Power Consumption of BL							
		PBL	-	38.6	-	W	IBL=7.0mA, 6

 <b>京东方</b> <b>BOE</b>	<b>PRODUCT GROUP</b>	<b>REV</b>	<b>ISSUE DATE</b>
	TFT- LCD PRODUCT	P2	2011.3.23
<b>SPEC. NUMBER</b>	<b>SPEC. TITLE</b>		<b>PAGE</b>
	HT260WXC-100 Product Specification Rev P2		8 OF 27

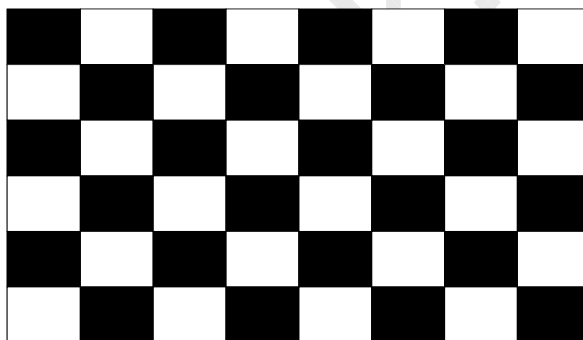
## Notes :

- The supply voltage is measured and specified at the interface connector of LCM.  
The current draw and power consumption specified is for VDD=12.0V, Frame rate=60Hz and Clock frequency = 75.4MHz. Test Pattern of power supply current
  - Typ : Black Pattern
  - Max : Sub Dot Pattern
- The duration of rush current is about 2ms and rising time of Power Input is 1ms(min)

White : 255Gray


Black : 0Gray

Mosaic Pattern(8 x 6)



- The frequency range can be keep within  $\pm 10\%$  range of electrical and optical characteristics. (except the chromaticity)
- Output voltage of test circuit in order to stabilize discharge in tube. Lamp and test circuit should be left at the above standard conditions over 1Hr.
- The lifetime is judged to be over when any situation mentioned below occurs:
  - When the central lamp luminance decline to 50% of the initial luminance.
  - When the lamp starting voltage is 10% higher than its specification
- Calculated value for reference  $(VBL \times IBL) \times 4$  excluding inverter loss



	<b>PRODUCT GROUP</b>	<b>REV</b>	<b>ISSUE DATE</b>
	TFT- LCD PRODUCT	P2	2011.3.23
<b>SPEC. NUMBER</b>	<b>SPEC. TITLE</b>		<b>PAGE</b>
	HT260WXC-100 Product Specification Rev P2		9 OF 27

## 4.0 INTERFACE CONNECTION

### 4.1 Module Input Signal & Power

- Connector : IS100-L30B-C23(Manufactured by UJU) or Equivalent.

< Table 4. LCM Module Input Connector Pin Configuration >

Pin No	Symbol	Description	Pin No	Symbol	Description
1	VDD	Power Supply +12.0V	16	RX1+	LVDS Receiver Signal(+)
2	VDD	Power Supply +12.0V	17	GND	Ground
3	VDD	Power Supply +12.0V	18	RX2-	LVDS Receiver Signal(-)
4	VDD	Power Supply +12.0V	19	RX2+	LVDS Receiver Signal(+)
5	GND	Ground	20	GND	Ground
6	GND	Ground	21	RCLK-	LVDS Receiver Clock Signal(-)
7	GND	Ground	22	RCLK+	LVDS Receiver Clock Signal(+)
8	GND	Ground	23	GND	Ground
9	LVDS_SEL	'L'=JEIDA , 'H' or NC= VESA	24	RX3-	LVDS Receiver Signal(-)
10	NC	No Connection	25	RX3+	LVDS Receiver Signal(+)
11	GND	Ground	26	GND	Ground
12	RX0-	LVDS Receiver Signal(-)	27	NC	No Connection
13	RX0+	LVDS Receiver Signal(+)	28	NC	No Connection
14	GND	Ground	29	GND	Ground
15	RX1-	LVDS Receiver Signal(-)	30	GND	Ground

Notes : 1. NC(Not Connected) : This pins are only used for BOE internal operations.

2. Input Level of LVDS signal is based on the IEA 664 Standard.

3. LVDS\_SEL : This pin is used for selecting LVDS signal data format.

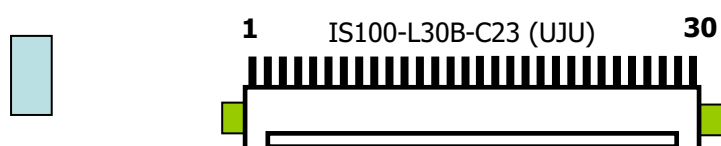
If this Pin : High (3.3V) or Open (NC) → Normal NS LVDS format


Otherwise : Low (GND) → JEIDA LVDS format

Sequence : On = Vdd ≥ LVDS Option ≥ Interface signal

Off = Interface signal ≥ LVDS Option ≥ Vdd

#### Rear view of LCM



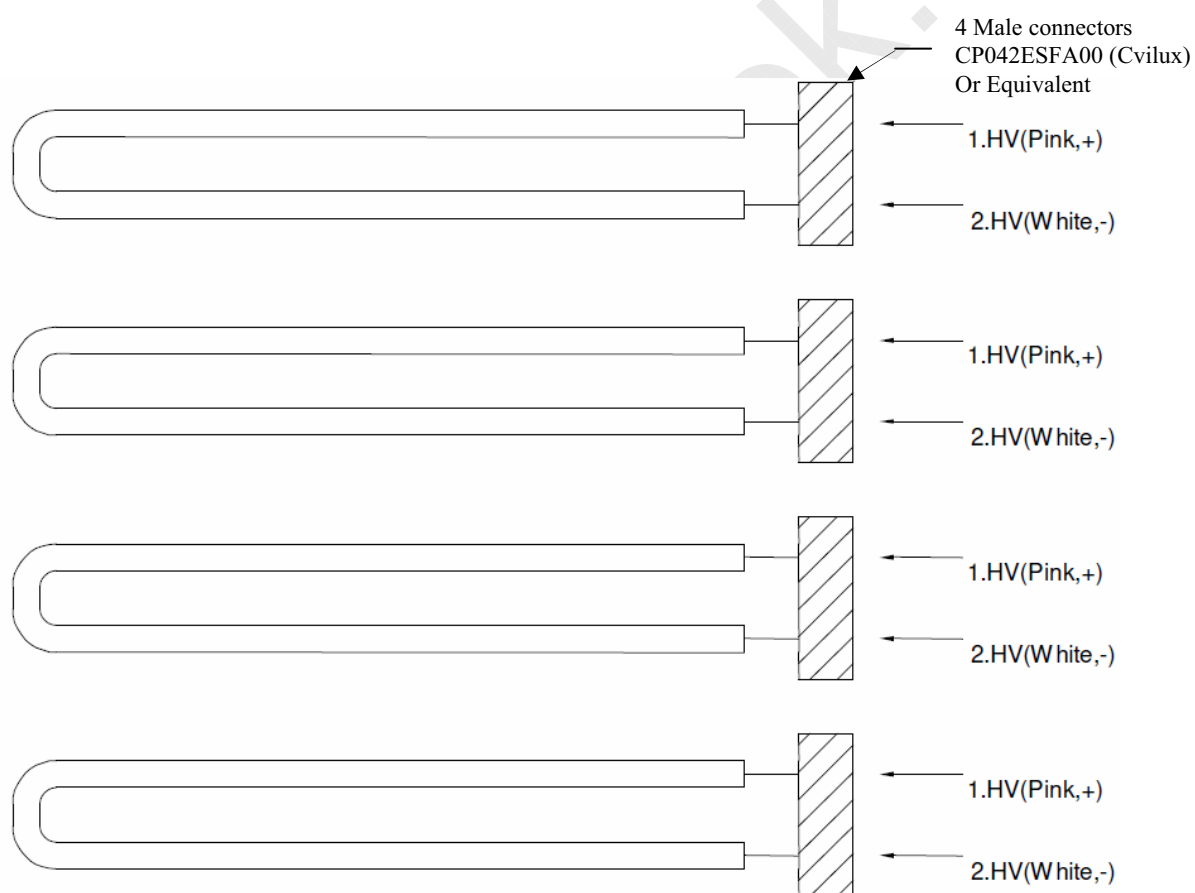
 <b>京东方 BOE</b>	<b>PRODUCT GROUP</b>	<b>REV</b>	<b>ISSUE DATE</b>
	TFT- LCD PRODUCT	P2	2011.3.23
<b>SPEC. NUMBER</b>	<b>SPEC. TITLE</b>		<b>PAGE</b>
	HT260WXC-100 Product Specification Rev P2		10 OF 27

## 4.2 Back-light Interface Connection

Module Side Connector: CP0404SL000 (Cvilux) or Equivalent

User Side Connector : CP042EP1ML0-NH (Cvilux) or Equivalent

Pin No.	Symbol	Description	Wire Color
1	HV	High Voltage	pink
2	HV	High Voltage	white



 <b>京东方 BOE</b>	<b>PRODUCT GROUP</b>	<b>REV</b>	<b>ISSUE DATE</b>
	TFT- LCD PRODUCT	P2	2011.3.23
<b>SPEC. NUMBER</b>	<b>SPEC. TITLE</b>		<b>PAGE</b>
	HT260WXC-100 Product Specification Rev P2		11 OF 27

### 4.3 LVDS Interface

- LVDS Receiver : Timing Controller (LVDS Rx merged)

- LVDS Data : Pixel Data

< Table 5. LCM Module Input Connector Pin Configuration >

	LVDS Pin	Vesa Data format	JEIDA Data format	Remark
TxOUT/RxIN0	TxIN/RxOUT0	Red0 [LSB]	R2	
	TxIN/RxOUT1	Red1	R3	
	TxIN/RxOUT2	Red2	R4	
	TxIN/RxOUT3	Red3	R5	
	TxIN/RxOUT4	Red4	R6	
	TxIN/RxOUT6	Red5	R7 [MSB]	
	TxIN/RxOUT7	Green0 [LSB]	G2	
TxOUT/RxIN1	TxIN/RxOUT8	Green1	G3	
	TxIN/RxOUT9	Green2	G4	
	TxIN/RxOUT12	Green3	G5	
	TxIN/RxOUT13	Green4	G6	
	TxIN/RxOUT14	Green5	G7 [MSB]	
	TxIN/RxOUT15	Blue0 [LSB]	B2	
	TxIN/RxOUT18	Blue1	B3	
TxOUT/RxIN2	TxIN/RxOUT19	Blue2	B4	
	TxIN/RxOUT20	Blue3	B5	
	TxIN/RxOUT21	Blue4	B6	
	TxIN/RxOUT22	Blue5	B7 [MSB]	
	TxIN/RxOUT24	HSYNC	HSYNC	
	TxIN/RxOUT25	VSYNC	VSYNC	
	TxIN/RxOUT26	DEN	DEN	
TxOUT/RxIN3	TxIN/RxOUT27	Red6	R0 [LSB]	
	TxIN/RxOUT5	Red7 [MSB]	R1	
	TxIN/RxOUT10	Green6	G0 [LSB]	
	TxIN/RxOUT11	Green7 [MSB]	G1	
	TxIN/RxOUT16	Blue6	B0 [LSB]	
	TxIN/RxOUT17	Blue7 [MSB]	B1	
	TxIN/RxOUT23	Reserved	Reserved	

B2010-6011-O(3/3)

A4(210 X 297)

 <b>京东方</b> <b>BOE</b>	<b>PRODUCT GROUP</b>	<b>REV</b>	<b>ISSUE DATE</b>
	TFT- LCD PRODUCT	P2	2011.3.23
<b>SPEC. NUMBER</b>	<b>SPEC. TITLE</b> HT260WXC-100 Product Specification Rev P2		<b>PAGE</b> 12 OF 27


## 5.0 SIGNAL TIMING SPECIFICATION

### 5.1 Timing Parameters ( DE only mode)

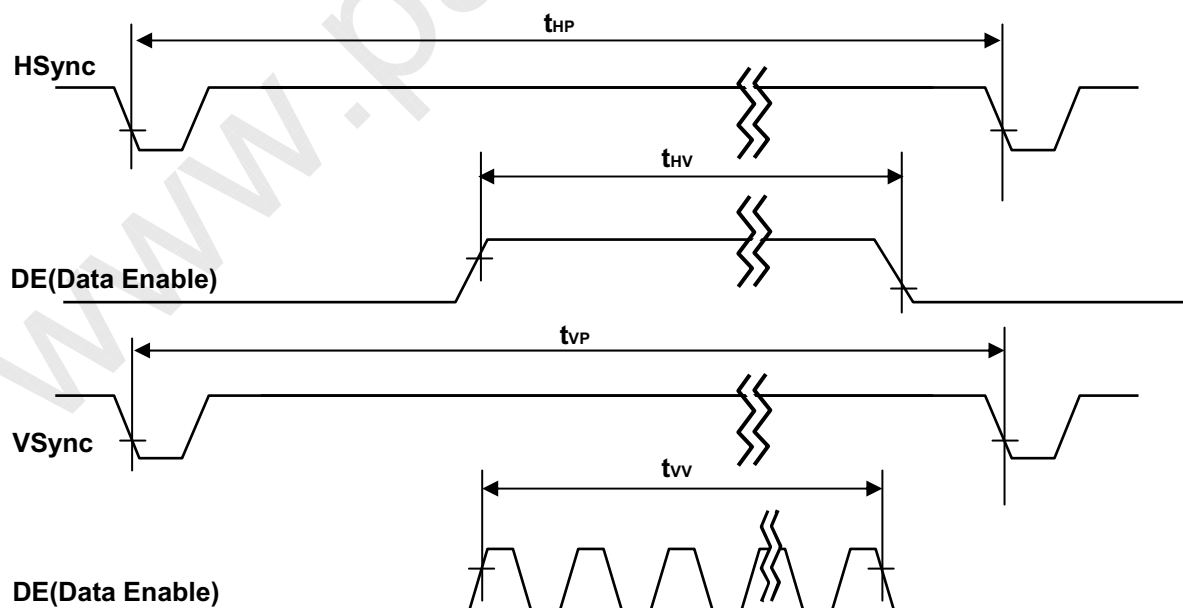
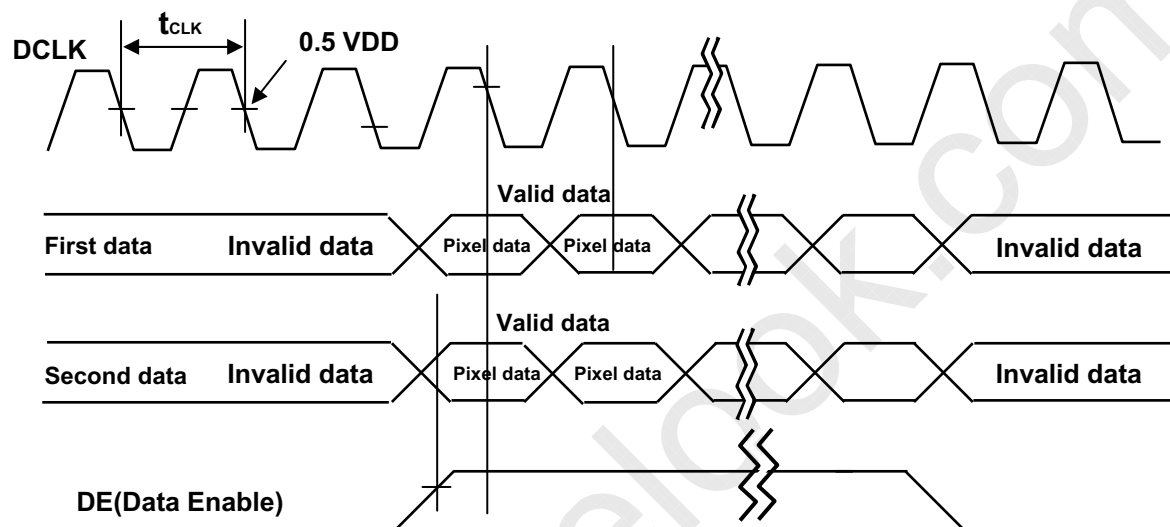
< Table 6. Timing Table >

ITEM	Symbol		Min	Typ	Max	Unit	Note
CLK	Period	$t_{CLK}$	11.8	13.3	17.9	ns	
	Frequency	-	56	75.4	85.0	MHz	
Hsync	Period	$t_{HP}$	1442	1560	1936	$t_{CLK}$	
	Frequency	$f_H$	39.4	48.4	53	KHz	
Vsync	Period	$t_{VP}$	778	806	888	$t_{HP}$	
	Frequency	$f_V$	50	60	65	Hz	
Horizontal Active Display Term	Valid	$t_{HV}$	-	1366	-	$t_{CLK}$	
	Total	$t_{HP}$	1442	1560	1936	$t_{CLK}$	
Vertical Active Display Term	Valid	$t_{VV}$	-	768	-	$t_{HP}$	
	Total	$t_{VP}$	778	806	888	$t_{HP}$	

Notes: This product is DE only mode. The input of Hsync & Vsync signal does not have an effect on normal operation.

 <b>京东方</b> <b>BOE</b>	<b>PRODUCT GROUP</b>	<b>REV</b>	<b>ISSUE DATE</b>
	TFT- LCD PRODUCT	P2	2011.3.23
<b>SPEC. NUMBER</b>	<b>SPEC. TITLE</b>		<b>PAGE</b>
	HT260WXC-100 Product Specification Rev P2		13 OF 27

## 5.2 Signal Timing Waveform






 <b>京东方 BOE</b>	<b>PRODUCT GROUP</b>	<b>REV</b>	<b>ISSUE DATE</b>
	TFT- LCD PRODUCT	P2	2011.3.23
<b>SPEC. NUMBER</b>	<b>SPEC. TITLE</b>		<b>PAGE</b>
	HT260WXC-100 Product Specification Rev P2		14 OF 27

5.3 Input Signals, Basic Display Colors & Gray Scale Of Colors

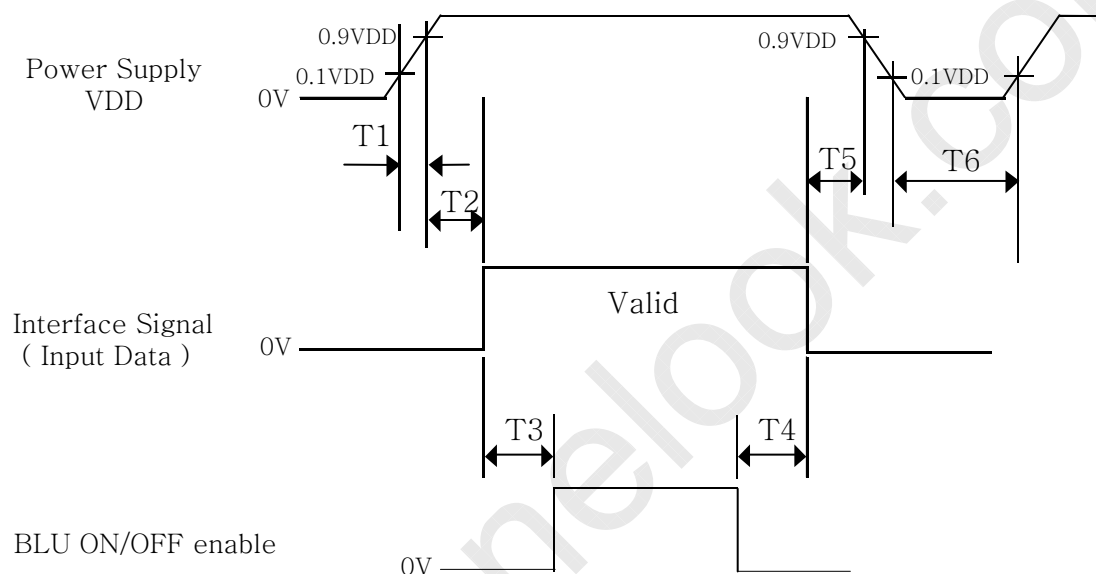
< Table 7. Input Signal and Display Color Table >

Color & Gray Scale		Input Data Signal																							
		Red Data								Green Data						Blue Data									
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale of Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△	↑								↑						↑									
	▽	↓								↓						↓									
	Brighter	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	▽	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	
	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	
	△	↑								↑						↑									
	▽	↓								↓						↓									
	Brighter	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	
	▽	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
Gray Scale of Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	
	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	
	△	↑								↑						↑									
	▽	↓								↓						↓									
	Brighter	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	1
	▽	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1
Gray Scale of White	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	
	△	↑								↑						↑									
	▽	↓								↓						↓									
	Brighter	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1
	▽	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	1	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

 <b>京东方 BOE</b>	<b>PRODUCT GROUP</b>	<b>REV</b>	<b>ISSUE DATE</b>
	TFT- LCD PRODUCT	P2	2011.3.23
<b>SPEC. NUMBER</b>	<b>SPEC. TITLE</b>		<b>PAGE</b>
	HT260WXC-100 Product Specification Rev P2		15 OF 27

## 5.4 Power Sequence

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown in below



< Table 8. Sequence Table >

Parameter	Values			Units
	Min	Typ	Max	
T1	0.5	-	20	ms
T2	0	-	50	ms
T3	200	-	-	ms
T4	200	-	-	ms
T5	0	-	100	ms
T6	1	-	-	s

### Notes:

1. When the power supply VDD is 0V, Keep the level of input signals on the low or keep high impedance.
2. Do not keep the interface signal high impedance when power is on.
3. Back Light must be turn on after power for logic and interface signal are valid.

 <b>京东方</b> <b>BOE</b>	<b>PRODUCT GROUP</b>	<b>REV</b>	<b>ISSUE DATE</b>
	TFT- LCD PRODUCT	P2	2011.3.23
<b>SPEC. NUMBER</b>	<b>SPEC. TITLE</b>		<b>PAGE</b>
	HT260WXC-100 Product Specification Rev P2		16 OF 27

## 6.0 OPTICAL SPECIFICATION


The test of Optical specifications shall be measured in a dark room (ambient luminance  $\leq 1$  lux and temperature =  $25 \pm 2$  °C) with the equipment of Luminance meter system (Goniometer system and TOPCON BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of  $\Theta$  and  $\Phi$  equal to  $0^\circ$ . We refer to  $\Theta_{\phi=0}$  ( $=\Theta_3$ ) as the 3 o'clock direction (the "right"),  $\Theta_{\phi=90}$  ( $=\Theta_{12}$ ) as the 12 o'clock direction ("upward"),  $\Theta_{\phi=180}$  ( $=\Theta_9$ ) as the 9 o'clock direction ("left") and  $\Theta_{\phi=270}$  ( $=\Theta_6$ ) as the 6 o'clock direction ("bottom"). While scanning  $\Theta$  and/or  $\Phi$ , the center of the measuring spot on the Display surface shall stay fixed. The measurement shall be executed after 30 minutes warm-up period. VDD shall be 12.0V  $\pm 10\%$  at 25°C. Optimum viewing angle direction is 6 'clock.

[VDD = 12.0V, Frame rate = 60Hz, Ta =  $25 \pm 2$  °C]

< Table 9. Optical Table >

Parameter		Symbol	Condition	Min	Typ	Max	Unit	Remark
Viewing Angle	Horizontal	$\Theta_3$	CR > 10		80		Deg.	Note 1
		$\Theta_9$			80		Deg.	
	Vertical	$\Theta_{12}$			80		Deg.	
		$\Theta_6$			70		Deg.	
Color Temperature				-	10,000		K	
Color Gamut				70	72		%	
Contrast ratio		CR		600:1	800:1	-		Note 2
Luminance of White		$Y_w$		340	400	-	cd/m <sup>2</sup>	Note 3
White luminance uniformity		$\Delta Y$		75	80		%	Note 4
Reproduction of color	White	$W_x$	$\Theta = 0^\circ$ (Center) Normal Viewing Angle	TYP. - 0.03	0.280	TYP. + 0.03		Note 5
		$W_y$			0.290			
	Red	$R_x$			0.636			
		$R_y$			0.335			
	Green	$G_x$			0.291			
		$G_y$			0.603			
	Blue	$B_x$			0.146			
		$B_y$			0.061			
Response Time		$T_g$		-	5	10	ms	Note 6
Gamma Scale				2.0	2.2	2.4		



 <b>京东方</b> <b>BOE</b>	<b>PRODUCT GROUP</b>	<b>REV</b>	<b>ISSUE DATE</b>
	TFT- LCD PRODUCT	P2	2011.3.23
<b>SPEC. NUMBER</b>	<b>SPEC. TITLE</b>		<b>PAGE</b>
	HT260WXC-100 Product Specification Rev P2		17 OF 27

**Note :**

1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface.
2. Contrast measurements shall be made at viewing angle of  $\theta = 0^\circ$  and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (See FIGURE 1 shown in Appendix) Luminance Contrast Ratio (CR) is defined mathematically.

$$CR = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$

3. Center Luminance of white is defined as the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.
4. The White luminance uniformity on LCD surface is then expressed as :  
 $\Delta Y = (\text{Minimum Luminance of 5points} / \text{Maximum Luminance of 5points}) * 100$   
 (See FIGURE 2 shown in Appendix).
5. The color chromaticity coordinates specified in Table 9. shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
6. The electro-optical response time measurements shall be made as FIGURE 3 shown in Appendix by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Td, and 90% to 10% is Tr.

 <b>京东方 BOE</b>	<b>PRODUCT GROUP</b>	<b>REV</b>	<b>ISSUE DATE</b>
	TFT- LCD PRODUCT	P2	2011.3.23
<b>SPEC. NUMBER</b>	<b>SPEC. TITLE</b>		<b>PAGE</b>
	HT260WXC-100 Product Specification Rev P2		18 OF 27

## 7.0 MECHANICAL CHARACTERISTICS

### 7.1 Dimensional Requirements

FIGURE 4 (located in Appendix) shows mechanical outlines for the model HT260WXC-100. Other parameters are shown in Table 10.

<Table 10. Dimensional Parameters>


Parameter	Specification	Unit
Dimensional outline	626.0(H) × 373.0(V) × 32.0(D)	mm
Weight	3700 (typ)	gram
Active area	575.769(W) x 323.712(H)	mm
Pixel pitch	0.4215(H) × 0.4215(V)	mm
Number of pixels	1366(H) × 768(V) (1 pixel = R + G + B dots)	pixels
Back-light	Direct Light 4 U_CCFL type	

### 7.2 Mounting

See FIGURE 5. (shown in Appendix)

### 7.3 Semi-Glare and Polarizer Hardness.

The surface of the LCD has an semi-glare coating to minimize reflection and a coating to reduce scratching.

 <b>京东方</b> <b>BOE</b>	<b>PRODUCT GROUP</b>	<b>REV</b>	<b>ISSUE DATE</b>
	TFT- LCD PRODUCT	P2	2011.3.23
<b>SPEC. NUMBER</b>	<b>SPEC. TITLE</b>		<b>PAGE</b>
	HT260WXC-100 Product Specification Rev P2		19 OF 27


## 8.0 RELIABILITY TEST

The Reliability test items and its conditions are shown in below.

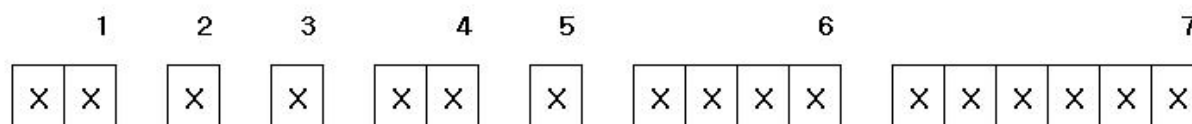
<Table 11. Reliability Test Parameters >

No	Test Items	Conditions
1	High temperature storage test	Ta = 60 °C , 240 hrs
2	Low temperature storage test	Ta = -20 °C , 240 hrs
3	High temperature & high humidity operation test	Ta = 50 °C , 80%RH, 240hrs
4	High temperature operation test	Ta = 50 °C , 240hrs
5	Low temperature operation test	Ta = 0 °C , 240hrs
6	Thermal shock	Ta = -20 °C ↔ 60 °C (0.5 hr), 100 cycle
7	Vibration test (non-operating)	Frequency : 10 ~ 300 Hz, Sweep rate 10 min Gravity / AMP : 1.5 G Sine Period : X, Y, Z 30 min
8	Shock test (non-operating)	Gravity : 50G Pulse width : 11msec, Sine wave ± X, ± Y, ± Z Once for each direction
9	Electro-static discharge test	Air : ± 15kV , 50pF/330Ω , 100Point , 1time/Point Contact : ± 8kV , 150pF/330Ω , 100Point , 1time/Point



 <b>京东方 BOE</b>	<b>PRODUCT GROUP</b>	<b>REV</b>	<b>ISSUE DATE</b>
	TFT- LCD PRODUCT	P2	2011.3.23
<b>SPEC. NUMBER</b>	<b>SPEC. TITLE</b>		<b>PAGE</b>
	HT260WXC-100 Product Specification Rev P2		20 OF 27

### 9.0 Product Serial Number



**Type**

No 1. Control

No 2. Rank

No 3. Line Classification(BOE HYDIS : H, LCM : L, BOE OT : A/B/C)

No 4. Year(2001 : 01, 2002 : 02, ...)

No 5. Month(1, 2, 3, ...,9 X, Y, Z)

No 6. FG Code

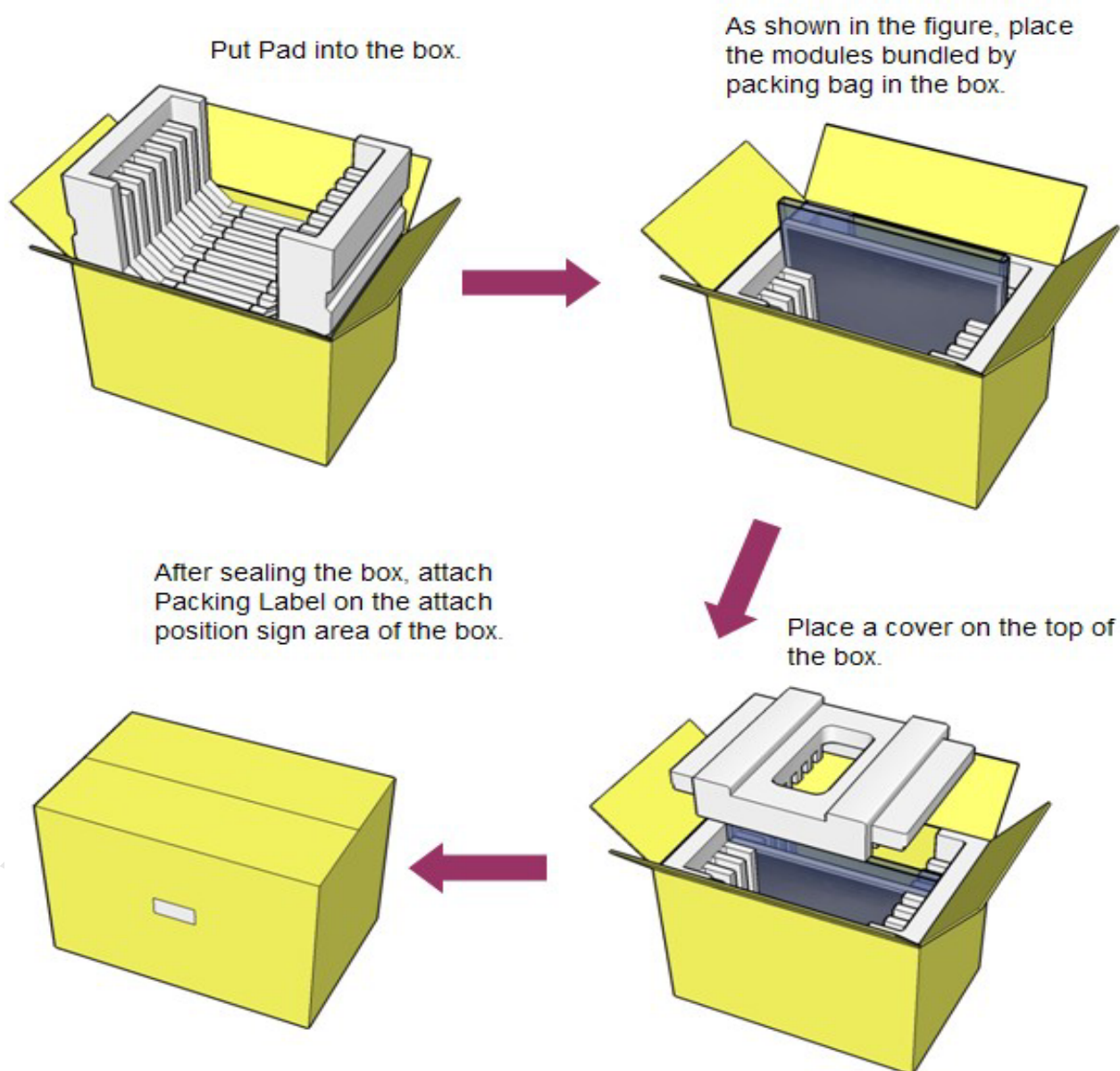
No 7. Serial No.

 <b>京东方 BOE</b>	<b>PRODUCT GROUP</b>	<b>REV</b>	<b>ISSUE DATE</b>
	TFT- LCD PRODUCT	P2	2011.3.23
<b>SPEC. NUMBER</b>	<b>SPEC. TITLE</b>		<b>PAGE</b>
	HT260WXC-100 Product Specification Rev P2		21 OF 27


## 10.0 PACKING INFORMATION

BOE provides the standard shipping container for customers, unless customer specifies their packing information. The standard packing method and Barcode information are shown in below.

### 10.1 Packing Order





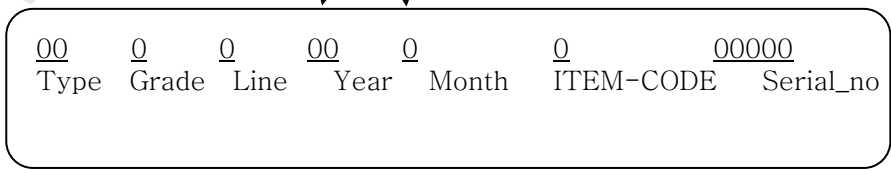
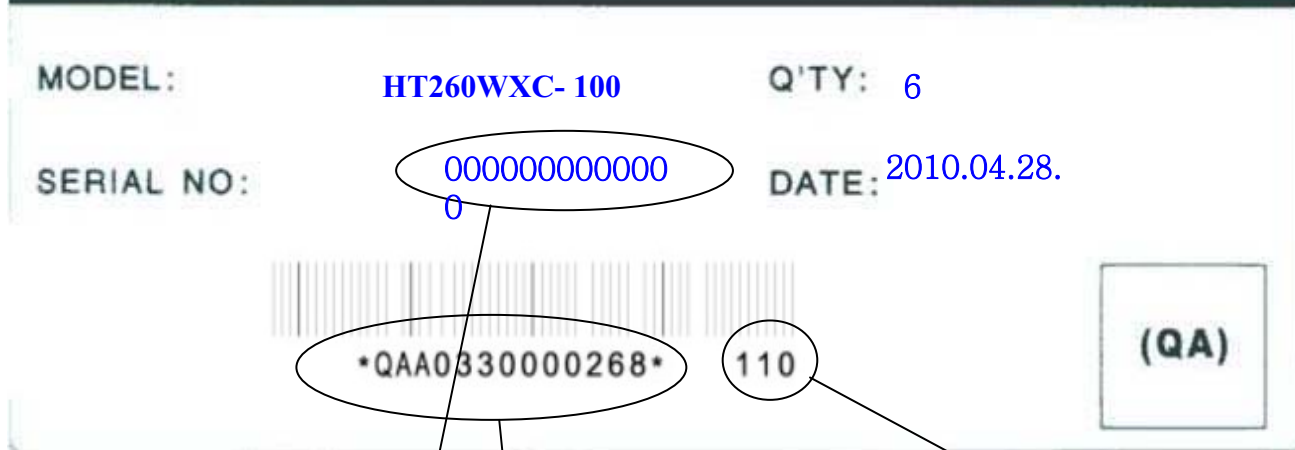
 <b>京东方 BOE</b>	<b>PRODUCT GROUP</b>	<b>REV</b>	<b>ISSUE DATE</b>
	TFT- LCD PRODUCT	P2	2011.3.23
<b>SPEC. NUMBER</b>	<b>SPEC. TITLE</b>		<b>PAGE</b>
	HT260WXC-100 Product Specification Rev P2		22 OF 27

10.2 Packing Note

- Box Dimension : 740L× 434W× 467H
- Package Quantity in one Box : 6pcs

10.3 Box label

- Label Size : 108 mm (L) 56 mm (W)
- Contents  
Model : HT2600WXC  
Q'ty : 6 Module in one box.  
Serial No. : Box Serial No. See next page for detail description.  
Date : Packing Date  
FG Code : FG Code of Product



FG CODE



 <b>京东方</b> <b>BOE</b>	<b>PRODUCT GROUP</b>	<b>REV</b>	<b>ISSUE DATE</b>
	TFT- LCD PRODUCT	P2	2011.3.23
<b>SPEC. NUMBER</b>	<b>SPEC. TITLE</b>		<b>PAGE</b>
	HT260WXC-100 Product Specification Rev P2		23 OF 27

## 11.0 HANDLING & CAUTIONS

- (1) Cautions when taking out the module
  - Pick the pouch only, when taking out module from a shipping package.
- (2) Cautions for handling the module
  - As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protection sheet off from the LCD panel surface as slowly as possible.
  - As the LCD panel and back - light element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
  - As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
  - Do not pull the interface connector in or out while the LCD module is operating.
  - Put the module display side down on a flat horizontal plane.
  - Handle connectors and cables with care.
- (3) Cautions for the operation
  - When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the LCD panel would be damaged.
  - Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.
- (4) Cautions for the atmosphere
  - Dew drop atmosphere should be avoided.
  - Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.
- (5) Cautions for the module characteristics
  - Do not apply fixed pattern data signal to the LCD module at product aging.
  - Applying fixed pattern for a long time may cause image sticking.
- (6) Other cautions
  - Do not disassemble and/or re-assemble LCD module.
  - Do not re-adjust variable resistor or switch etc.
  - When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.

京东方 BOE	PRODUCT GROUP	REV	ISSUE DATE
	TFT- LCD PRODUCT	P2	2011.3.23
SPEC. NUMBER	SPEC. TITLE		PAGE
	HT260WXC-100 Product Specification Rev P2		24 OF 27

12.0 APPENDIX

Figure 1. Measurement Set Up

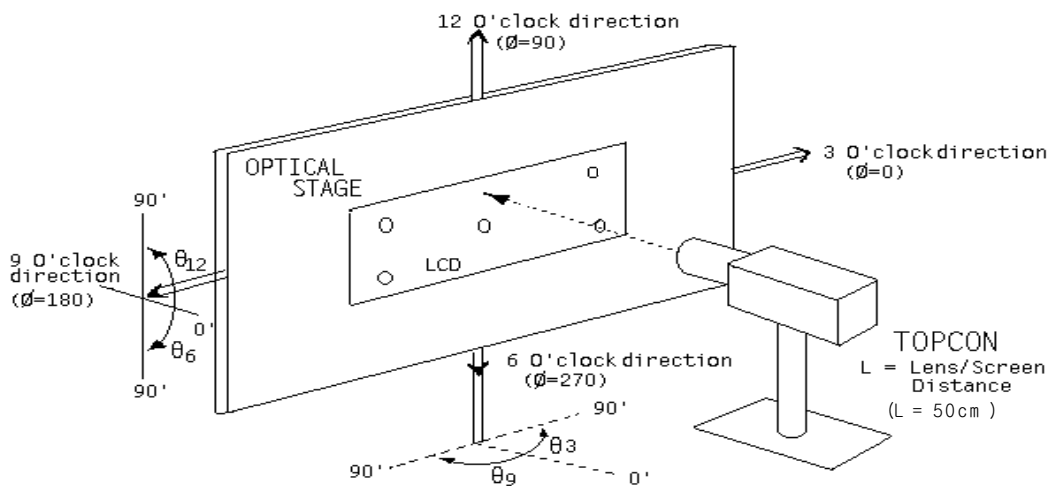
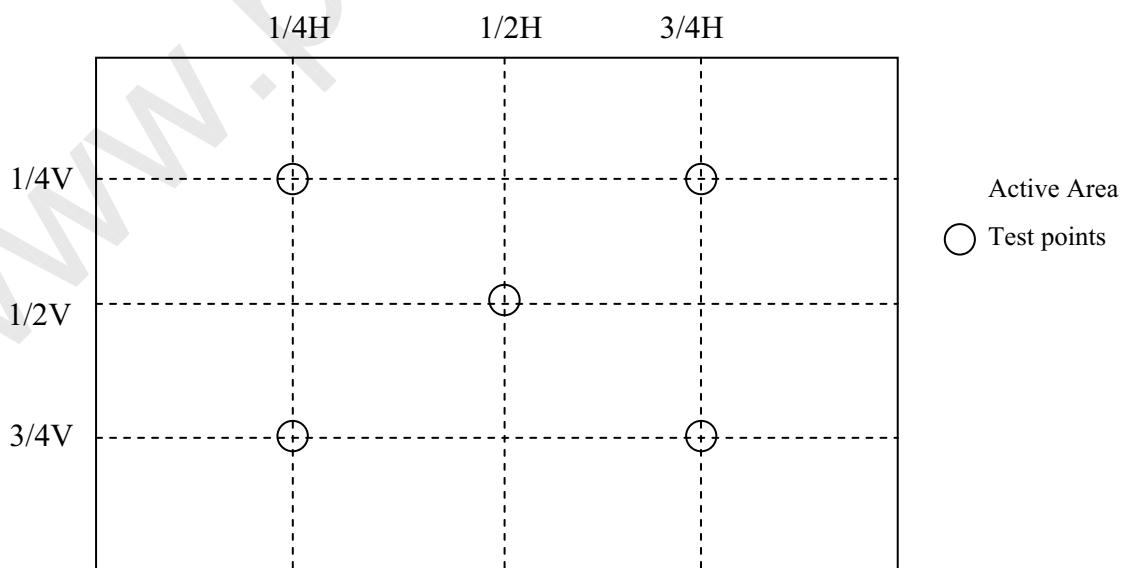



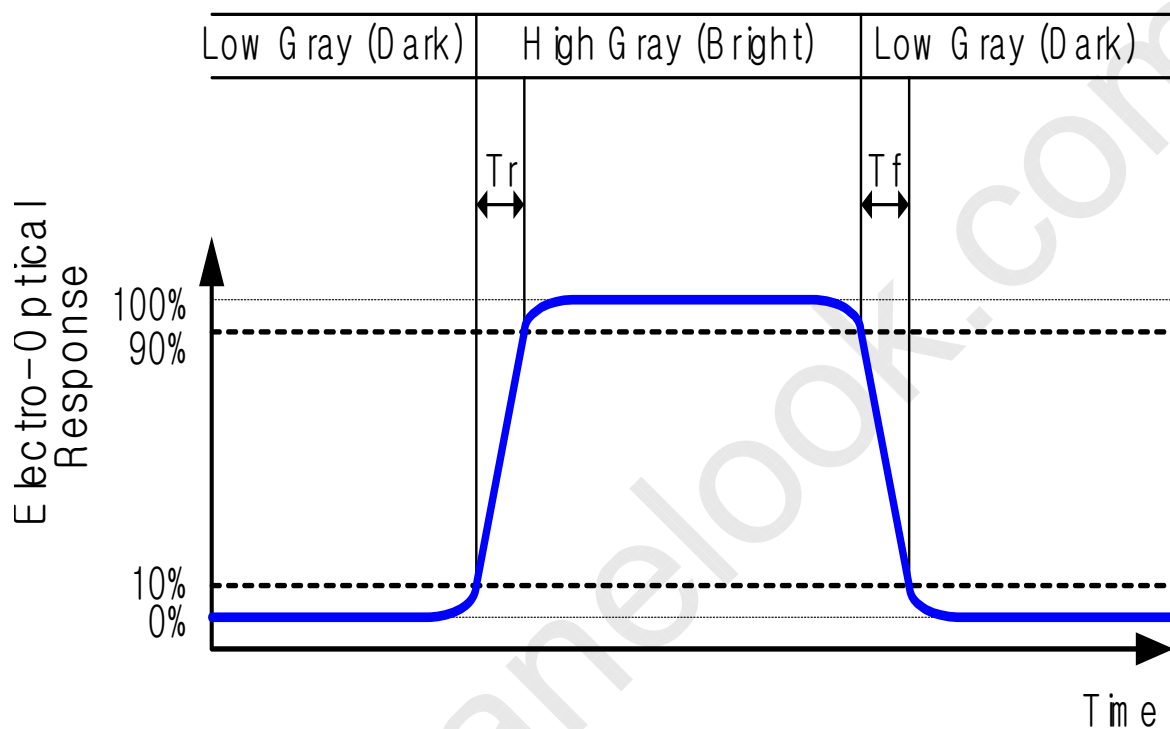
Figure 2. White Luminance and Uniformity Measurement Locations





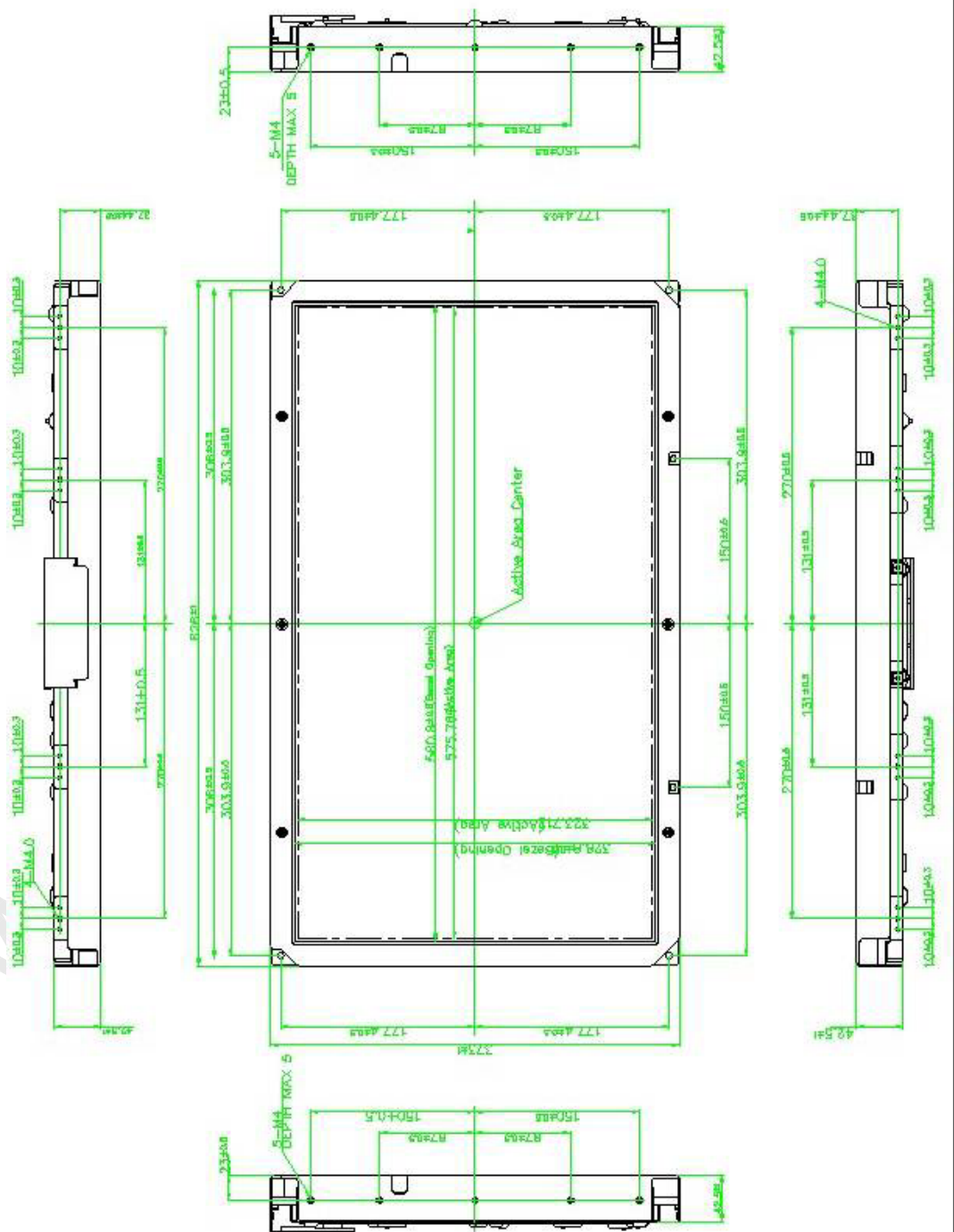
 <b>京东方</b> <b>BOE</b>	<b>PRODUCT GROUP</b>	<b>REV</b>	<b>ISSUE DATE</b>
	TFT- LCD PRODUCT	P2	2011.3.23
<b>SPEC. NUMBER</b>	<b>SPEC. TITLE</b>		<b>PAGE</b>
	HT260WXC-100 Product Specification Rev P2		25 OF 27

**Figure 3. Response Time Testing**



 <b>京东方 BOE</b>	<b>PRODUCT GROUP</b>	<b>REV</b>	<b>ISSUE DATE</b>
	TFT- LCD PRODUCT	P2	2011.3.23
<b>SPEC. NUMBER</b>	<b>SPEC. TITLE</b>	<b>PAGE</b>	
	HT260WXC-100 Product Specification Rev P2	26 OF 27	

Figure 4. TFT-LCD Module Outline Dimensions (Front view)



**NOTE**

- 1. Unspecified tolerances are to be ±1.0mm.
- 2. I/F CONNECTOR SPECIFICATION IS100-L30B-C23 or equivalent
- 3. INVERTER CONNECTOR SPECIFICATION Cxlux C10114M1HRL-LF or equivalent
- 4. USER MOUNTING TORQUE SPEC: 4~5kgf.cm

	<b>PRODUCT GROUP</b> TFT- LCD PRODUCT	<b>REV</b> P2	<b>ISSUE DATE</b> 2011.3.23
	<b>SPEC. NUMBER</b> HT260WXC-100 Product Specification Rev P2	<b>PAGE</b> 27 OF 27	

Figure 5. TFT-LCD Module Outline Dimensions (Rear view)

