

General Description

DTEHIP

The HT3402S is a constant current, constant voltage power supply converter, which utilizes a voltage mode, a current mode circuit, and pulse width modulation (PWM) switching regulator control circuit

An external sense resistor will set the charge current. An internal resistor divider and precision reference set the final float voltage to 5.1V with \pm 3% accuracy. With a 150 KHz switching frequency, HT3402S can provide a simple solution against EMI. High efficiency up to 90% can also diminish heat significantly on all related components.

The HT3402S also possesses over-voltage protect, over-thermal protect, and short- circuit protect function. At the beginning of the charge, the over- current circuit will limit the charge current not too high.

The HT3402S is available in a 8- pin SOP8 package

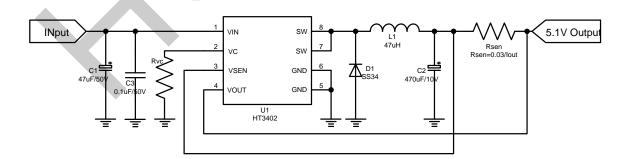
Features

- ♦ Wide Input Supply Range: 10V to 40V
- \pm 3% Charge Voltage Accuracy;
- High Efficiency Current Mode PWM
 Control with 150KHz Switching Frequency
- Constant Switching Frequency for Minimum Noise
- ♦ Cable compensation function
- ♦ Automatic Battery Recharge
- Automatic Shutdown When Input Supply Removed
- ♦ Available in a 8-pin SOP8 package

Applications

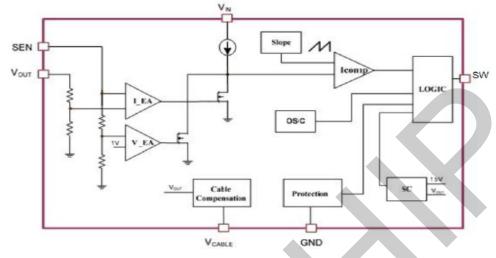
- ♦ SMPS
- ♦ Charger
- ♦ Portable Computers
- ♦ Handheld Instruments







Application Circuit



Operation

General Introduction

HT3402S utilizes a constant current and constant voltage circuit to manage charging current by an external sensing Resistor via Pins of SEN and VOUT. The preset Output Voltage is internally built
 @ 5.1V, with internal amplifier and resistor divider to ensure <±3% regulation accuracy.

Automatic Start- Up

 \diamond When a > 4V signal being inputted and sensed by HT3402S, it starts up automatically.

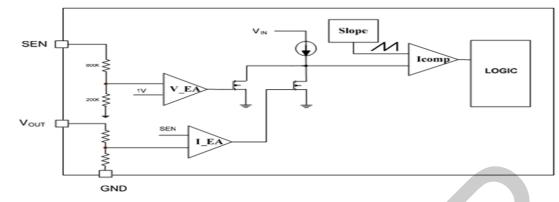
CC and CV Circuitry

- In regular operation, the constant current charge loop starts- up at first, and will be taken over by the constant voltage loop when the output voltage is higher than preset value of 5V. In the constant current loop, the average current of inductor is set to be followed the external Sensing Resistor. This constant current function is well controlled by an Error- amplifier, I-EA, and which ensures current accuracy
- range within 5%. In voltage loop, system regulates SEN voltage by switching at a fixed frequency transferring power to Load in each cycle by adopting a slope circuit, and Voltage mode PWM Controlled A simplified function diagram of charge current and charge voltage is shown as:



HT3402S

40V/2A Constant Voltage & Constant Current Buck Converter IC



Cable Resistance Compensation

The longer cable of car charger reduces output voltage. Add voltage compensation function will increase the output voltage and make the output of cable is remained to be the design value. An external resistor will be connected to the VCABLE pin to match the immanent resistor of cable. In the cable compensation circuit, the voltage of the VCABLE pin will be raised as the load current increase; the reference voltage of voltage loop will be raised to an appropriate value, and finished the compensation function. If there is no load, no compensation resistors or output current is zero, the output voltage will not rise. Set VCABLE pin in floating state to turn off this function off. Selection of external compensation resistor is based on the equivalent resistance of cable. Compensating voltage can be determined by equation:

$$V_{cable} = \frac{2000K}{R_{cable}} \times I_{out} \times R_{sen}$$

Over Voltage protection

When the output voltage of HT3402S is 10% higher than the targeting voltage, the PMOS will be turned off.

Thermal Shutdown (OTP)

An internal thermal loop can monitor internal junction temperature rigorously. If the temperature surpasses a preset value of approximately 120°C, HT3402S stops output

Short- circuit protection

At the beginning of enable HT3402S, system charges the load for about 100ms. If the short circuit protection module detect the output voltage is fallen down to 1.5V, it sends a single to Logic Circuit to turn off IC. When shor-t circuit happens, a current of about 2 mA will charge the load.
 If the short circuit state is aligned, charge voltage will raise above 1.5V and being detected, system is resumed to normal



Over Current Protection

HT3402S has an internal over-current protection circuit that limits the inrush current during start-up. At the beginning of the start-up, the over-current protection circuit will detect the current, if it's too high to exceed the safe value, the PMOS will be turn-off. But when output is rise to the design voltage, the protect function will not active anymore. The I-EA will replace the function to limiting the current.

Application Information

The basic application circuit of HT3402S is shown in Figure 1. External components selection is depend on load requirement, and begin with the selection of inductor followed by capacitor

Inductor Selection

♦ For most application, HT3402S operates well with inductors of 100uH, the inductor is selected to limit the ripple current with some predetermined value, typically 20~40% of the full load current at the maximum input voltage. The equation of inductance is as below: below:

$$\Delta I_L = \mathbf{K} \times \frac{1 - V_{OUT} / V_{IN}}{L \times f}$$
 (K= 0.2~0.4)

$$L = \frac{V_{OUT}}{f \times \Delta I_{L}} \left(1 - \frac{V_{OUT}}{V_{IN}} \right)$$

♦ The DC current rating of the inductor should be at least equal to the maximum load current plus half of the ripple current to prevent core saturation. For example △IL=1000mA×40%=400mA, thus, a 1400mA rated inductor should be enough for most application(1000mA+400mA). For better efficiency, choose a low DCR inductor.

Input Capacitor Selection

The input capacitor reduces surge current drawn from input and switching noise from the device. Input capacitor impedance at the switching frequency shall be less than input source impedance to prevent high frequency switching current passing to the input. A low ESR input capacitor sized for max. RMS current must be used. A 100uF ceramic capacitor for most application is sufficient and recommended.

Output Capacitor Selection

The output capacitor is required to keep the output voltage ripple small and to stabilize regulation loop. It must have low impedance at the switching frequency. Ceramic capacitors with X5R or X7R dielectrics are recommended due to their low ESR and high ripple current. The output ripple Vour is determined by below equation:



$$\Delta V_{OUT} = \Delta I_{L} \left(ESR + \frac{1}{8 fC_{OUT}} \right)$$

Load Current Limit Programming

The constant current ICHG is controlled by a sensing resistor connected between Inductor and Output. The Voltage- drop of the sensing resistor (RSEN) is internally regulated to 30mV, which sets the current flowing through RSEN. For better accuracy, a 2% or more accurate Resistor is recommended. Table 1 shows several typical 1% RSEN values:

Table 1					
I _{CHG} (mA)	R _{SEN} (mΩ)				
2000	15				
1000	30				

♦ For other constant current values, RSEN can be determined by the following equation::

$$R_{sen} = \frac{0.03}{I_{out}}$$

	Name	Pin #	Function Description		
	VIN	1	Power Supply Input		
1 - 8	V _{CABLE}	2	Connect a suitable resistor to the compensation voltage and improve the output power which the cable wasted.		
			Set this pin sets float to turn off cable compensation function.		
2 7	SEN	3	Output Voltage feedback		
3	V _{OUT}	4	Output power supply to back- end devices. Note1		
GND 5		5	Ground of IC		
4 5	GND	6	Ground of IC		
	SW	7	SW Output.		
	SW	8	SW Output.		

Pin Configuration

Note1: VOUT can be programmed to compensate the drop- out voltage from longer cable.



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Absolute Maximum Ratings

PARAMETER	SYMBOL	MIN	MAX	UNITS
Input Supply Voltage	V _{IN}	10	40	V
Operating Temperature Range	Topr	-20	85	°C
Storage Temperature Range	T_{STG}	-20	150	°C
Junction Temperature	θJA		125	°C
Lead Temperature (Soldering, 10s)	T_{LTG}		260	٦°

Electrical Characteristics

(Circuit of Figure 1,T_A = 25°C, V_{IN}=12V, V_{OUT}=5.1V, unless otherwise noted.)

SYMBOL	PARAMETER	CONDITIONS	MIN	ТҮР	MAX	UNITS
V _{IN}	Power Supply Input Voltage	-	10	-	40	V
V _{OUT}	Output Voltage	-	4.95	5.1	5.25	V
I _{CHG}	Constant Current Mode Charge Current	R_{SEN} =15m Ω	2.0	2.40	2.8	А
	Current	R_{SEN} =30m Ω	1.0	1.20	1.40	А
lq	Quiescent current	No load	-	-	220	μA
V _{RIPPLE}	Output Ripple Voltage	I _{LOAD} =1A	50	-	80	mV
T _{ON}	The time of output voltage up to V_{OUT}	V _{OUT} =5.1V	6	10	12	mS
F	Frequency	I _{LOAD} =0.5A	120	150	180	KHz
V _{SHORT}	Short Circuit threshold	-	1.0	1.5	1.8	V
	Recover Short Circuit threshold	-	1.5	1.8	2.0	V
I _{SHORT}	Short Circuit Current	When short- circuit appears	1.5	2	3	mA
VCABLE	Cable compensation voltage	V _{CABLE=} 2000k x <i>Iload</i> x Rsen <i>R</i> case	-	-	1000	mV
T _{PRO}	Over temperature threshold	-	130	140	150	°C
T _{RE}	Release threshold from over temperature	-	110	120	130	°C

Typical Operating Characteristics

(Circuit of Figure 1,TA = 25°C, VIN=12V, VOUT=5.1V, unless other specified.)

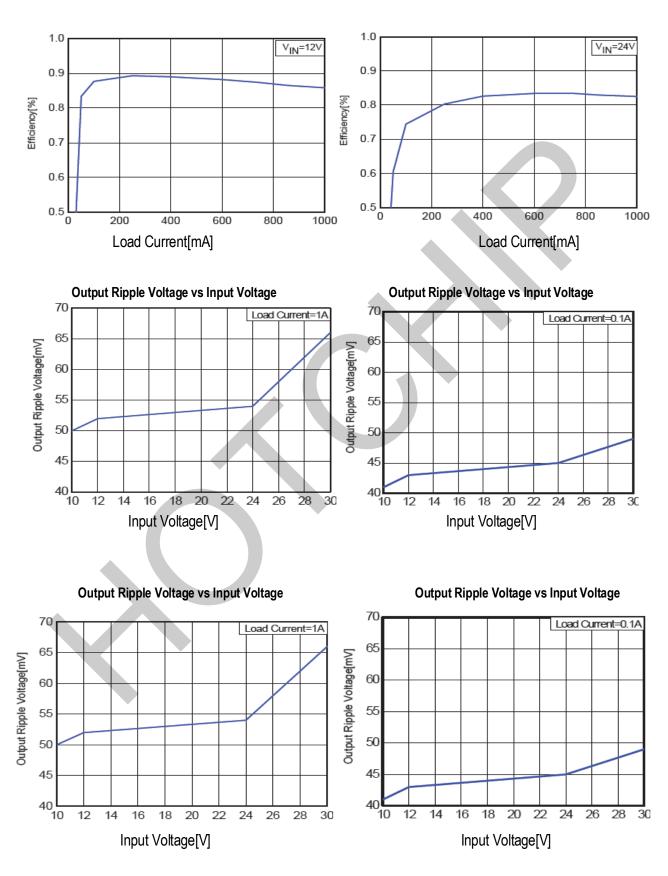
HT3402S

40V/2A Constant Voltage & Constant Current Buck Converter I

Efficiency vs Load Current

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Efficiency vs Load Current



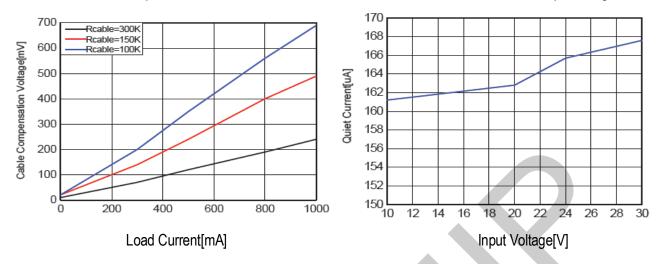
HT3402S



40V/2A Constant Voltage & Constant Current Buck Converter

Cable compensation vs Load Current

Quiescent current vs Input Voltage



Layout Notice

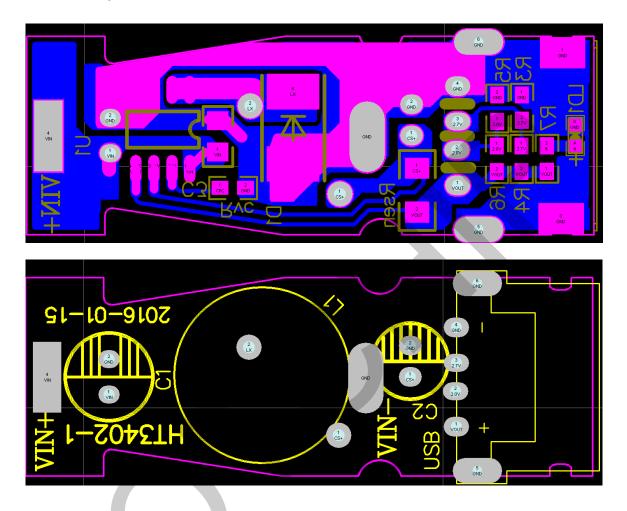
- C3 is adopted for filtering high Freq. noise, should be set as close as it can be to Pin #1 of U1, <1mm is recommended; C1 is however with consideration of its size to be installed a bit distant. Input power flows thru. C1 and then C3 to U1.</p>
- D1 generates higher heat and composes a loop with L1 and C2, it is suggested to arrange these three components as close as possible, and keep distant from U1 with at least 10mm to avoid heat to trigger OTP function on U1
- Sensing Resistor connects to Pin # 3 and # 4 of U1, and routing must compose Kelvin Method between anode of C1 and USB output so that sample being sensed is directly from both sides of Rsen, and avoid from other resistance coming from other PCB route.

Current must flow from L1 to anode of C2, and connect to one node of Rsen, then flown out from the other node to USB directly. If current flows inappropriately, sampling/ sensing resistance might increase and result in lower output current.

- Pin # 1/ 5/ 6/ 7/ 8 of U1 on PCB is recommended to manipulated with larger area of bare copper and a bit thicker tin paste spread on this area can also help for better heat dissipation. This action can also apply to both soldering pads of D1.
- USB port on PCB can be managed on D+/ D- anodes with designated resistors if necessary to fit in with
 Apple handsets, while in most occasions, put them short- CKT will also do.
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 8/11
 Ver1.1

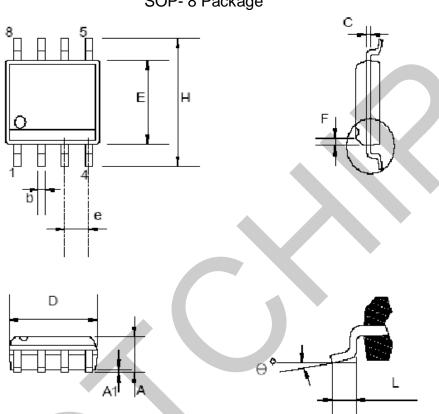


• Reference PCB Layout is shown as followed--





Package Outline Dimension

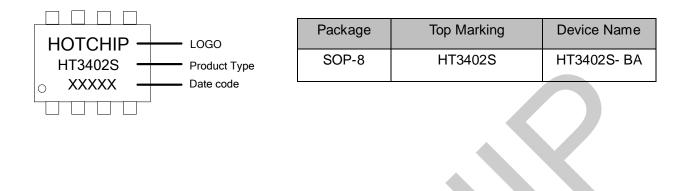


Symbo	Dimen	sions In Mili	imeters	Dimensions In Inches			
	Min	Тур.	Max	Min	Тур.	Max	
А	1.346		1.752	0.053		0.069	
A1	0.101		0.254	0.004		0.010	
b		0.406			0.016		
С		0.203			0.008		
D	4.648		4.978	0.183		0.196	
E	3.810		3 87	0.150		0.157	
е	1.016	1.270	1.524	0.040	0.050	0.060	
F		0.381*45°			0.015*45°		
Н	5.791		6.197	0.228		0.244	
L	0.406		1.270	0.016		0.050	
θ°	0°		8°	0°		8°	

SOP- 8 Package



Packing And Ordering Info.



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