

Features

- Complete Data Transmission on Power Line functions
- High Maximum Input Voltage: 42V
- Integrated Low Dropout Voltage Regulator
- Integrated Voltage Detector for Power Supply Monitoring
- Integrated Comparator
- Open drain NMOS driver for flexible interfacing
- Power and Reset Protection Features
- 8-pin SOP package type
- Minimal external component requirements

General Description

In systems where a master controller controls a number of individual interconnected subsystems such as found in smoke detector systems, water metering systems, solar energy system etc., the cost of the lengthy interconnecting cabling can be a major factor. By sending data along the power supply lines, the interconnecting cables can be reduced to a simple two line type, thus greatly reducing both cable and installation costs.

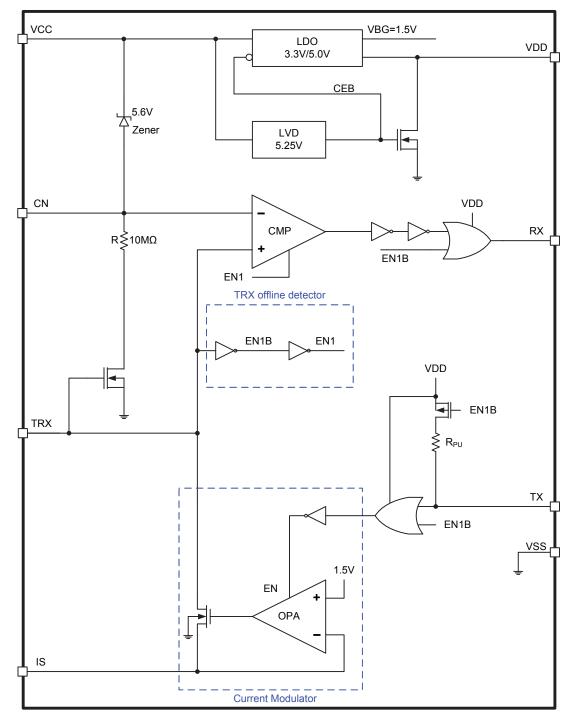
With the addition of a few external components, this power line data transceiver devices contain all the internal components required to provide users with a system for power line data transmission and reception. Data is modulated onto the power line by the simple reduction of the power line voltage for a specific period of time. Power supply voltage changes can be initiated by the master controller for data reception or initiated by the HT45B000x devices for data transmission. An internal voltage regulator within the devices ensures that a constant voltage power supply is provided to the interconnected subsystem units while an internal voltage detector monitors the power line voltage level. An internal comparator is used to translate the differential signal into a logic signal for the MCU.

Selection Guide

Part No.	LDO Voltage	Detect Voltage	Package
HT45B0003	3.3V	5.25V	8SOP
HT45B0005	5.0V	5.25V	8SOP



Block Diagram





Pin Assignment

VSS □ 1	8 IS						
TRX □ 2	7 TX						
CN □ 3	6 RX						
VCC □ 4	5 VDD						
HT45B0003/HT45B0005 8 SOP-A							

Pin Description

Pin Name	I/O	Description
VCC		Input voltage
CN	I	Comparator Negative Input
TRX	В	Transceiver signal detect/modulate
VSS		Ground pin – VSS
IS	0	Source terminal of constant current NMOS driver
TX	0	Input pin for constant current modulate
RX	0	Comparator output, transmitter signal detect output
VDD	—	LDO output voltage

Absolute Maximum Ratings

Supply Voltage V _{SS} -0.3~50V	I _{OL} Total
Operating Temperature40°C~85°C	IoH Total80mA
Storage Temperature	Total Power Dissipation 500mW

Note: These are stress ratings only. Stresses exceeding the range specified under "Absolute Maximum Ratings" may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

D.C. Characteristics

Cumb al	Demonster		Test Conditions		-		11
Symbol	Parameter	VDD	Conditions	Min.	Тур.	Max.	Unit
Vcc	Operating Voltage	_	_	7	—	42	V
Icc	Operating Current	_	V _{CC} =42V, V _{DD} No Load,	_	20	40	μA
I _{OFF}	Offline Current	_	V _{cc} =42V, V _{DD} No Load, TRX=0V	_	10	20	μA
Voff	TRX Offline Voltage	_	—	_	—	0.5	V
Von	TRX Online Voltage	_	_	4	—	_	V
VT	Threshold Voltage	_	—	_	V_{MARK} -5.6	—	V
1	Modulate Current	_	Rs=100Ω	_	15	_	mA
Мс		_	Rs=47Ω	—	32	_	mA
V	Input low voltage for TV nin	5V	_	0	—	1.5	V
V _{IL} Input low voltage for TX pin	3.3V	_	0	—	$0.2V_{\text{DD}}$	V	
V	Innut high voltage for TV nin	5V	—	3.5	—	5	V
VIH	Input high voltage for TX pin	3.3V	—	0.8V _{DD}	—	V _{DD}	V

Ta=25°C



Symbol Parameter		Test Conditions		Min.	Turn	Max.	Unit
Symbol	Parameter	VDD	Conditions	wiin.	Тур.	wax.	Unit
	IoL Sink current for RX pin	3.3V	Vol=0.1Vdd	4	8	_	mA
IOL		5V	Vol=0.1Vdd	10	20	_	mA
	I _{OH} Source current for RX pin	3.3V	V _{OH} =0.9V _{DD}	-2	-4	_	mA
ЮН		5V	V _{OH} =0.9V _{DD}	-5	-10	—	mA
Rph	Pull-high Resistance for TX		—	-30%	50	30%	kΩ

LDO Characteristics

							Ta=25°C
Symbol	Parameter		Test Conditions		_		
Symbol	Parameter	VDD	Conditions	Min.	Тур.	Max.	Unit
Vout	Output Voltage	3.3V	V _{CC} =7V, I _{LOAD=} 10mA	3.2	3.3	3.4	V
VOUT	Output voltage	5V	V _{CC} -7V, I _{LOAD} = IUIIIA	4.85	5	5.15	V
1	Output Current		V _{CC=} 10V, ΔV _{OUT=} -3%	60	_	—	mA
IOUT	Output Current		V _{CC=} 7V, ΔV _{OUT=} -3%	30	_	_	mA
ΔV_{LINE}	Line Regulation		$7V \le V_{IN} \le 42V$, $I_{LOAD=}1mA$	—	—	0.2	%/V
	To more thing Coofficient	3.3V	Ta=-40°C ~ 85°C, V _{CC=} 7V, I _{LOAD=} 10mA		±0.5	±1	mV/°C
TC	Temperature Coefficient	5V	Ta=-40°C ~ 85°C, V _{CC=} 7V, I _{LOAD=} 10mA		±0.75	±1.5	mV/°C
$\Delta V_{\text{OUT}_{\text{RIPPLE}}}$	Output Voltage Ripple	_	V _{CC} =7V, I _{LOAD} =10mA	_	_	40	mV
t _{START} LDO Startup Time	3.3V	V _{CC} =7V, I _{LOAD} =1mA, V _{OUT} =3.3V ± 3%	_	_	10	ms	
	LDO Startup Time	5V	V_{CC} =7V, I _{LOAD} =1mA, V _{OUT} =5V ± 3%	_	_	10	ms
I _{OL}	Sink current for VDD		V _{CC} =5V, V _{OL} =0.5V	0.8	_	_	mA

LVD Characteristics

Ta=25°C

Symbol	Deveneter	٦	Test Conditions	Min	Turn	Max.	Unit
Symbol	Parameter	Parameter Min. Typ. N		wax.	Unit		
V _{LVD}	Low Voltage Detection Voltage	—	—	5.0	5.25	5.5	V
тс	Temperature Coefficient ($\Delta V_{LVD}/\Delta Ta$)	—	Ta=-40°C ~ 85°C	—	±0.9	_	mV/°C
V _{DET}	Power-up Detection Voltage (VCC Positive-going Threshold Voltage)	—	_	6.2	6.5	6.8	V

Comparator Characteristics

		-				٦	Гa=25°C
Symbol	Parameter	Test Conditions		Min.	Тур.	Max.	Unit
	Faranieter	VDD	Conditions	IVIIII.	Typ.	Wax.	Unit
Aol	Open loop gain	_	_	60	80	_	dB
V _{HYS}	Hysteresis	_	—	—	0.15	—	V
t _{RP}	Response time	_	_		_	5	μs



							a=25°C
Symbol	Parameter		Test Conditions		Тур.	Max.	Unit
	Parameter	VDD	Conditions	Min.	тур.	WICK.	Unit
t _{RP}	Response Time	—	No Load	_	_	5	μA

Constant Current Modulator Characteristics

Function Description

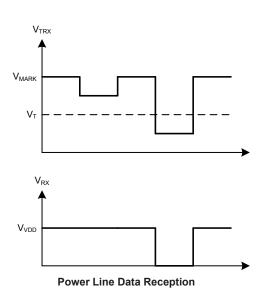
These devices provide a way to transmit and receive data on the common power lines of an interconnected array of microcontroller based subsystems. By having one of these devices inside each subsystem, the shared power and data cabling can be reduced to a simple two line type, offering major installation cost reductions.

Shared Power Line

All microcontroller based subsystems are connected together via the same two line power connection. The ground line is hardwired to each subsystem while the positive power line is connected to the VCC pin on each of the HT45B000x devices. An internal Low Dropout Voltage Regulator within the HT45B000x devices converts this input power supply voltage to a fixed voltage level which is supplied to the subsystem microcontroller and other circuit components. In this way when the power line voltage is changed due to the transmission or reception of data the subsystem circuits still continue to receive a regulated power supply.

Data Transmission (From Master Controller to Slave Device)

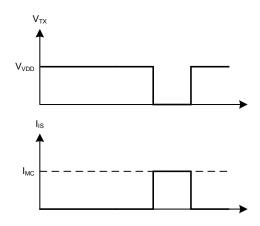
Refer to the application circuit when reading the following description. The master controller transmites the data by modulate the positibe power line (L+) voltage. Using this method should be pay attention to the noise tolerance of the devices operating voltage and the TRX pin receiving data. As the devices include a voltage regulator which is used as the power supply to the subsystem units, then the subsystem power supply voltage will not be affected as long as the regulator minimum dropout voltage is maintained. Then a voltage modulation signal will be detected in the TRX pin to make the TRX pin voltage drop lower than the threshold voltage (V_T) . However a reduction in the power supply will be detected by the CMP internal comparator. The output of this comparator is connected to pin RX can be connected to a microcontroller input for use as a data signal.



Data Reception (From Slave Device to Master Controller)

Refer to the application circuit when reading the following description. The HT45B000X slave devices can transmit data to the master controller by modulating the current on the power supply line. The slave devices pull the TX pin voltage to a low level to enable the internal current modulator. The modeulator will provide a constant current load by the transistor connected to the internal modulator OPA output NMOS terminal. The constant current load is supplied by the power line through the TRX pin, and can be adjusted by the Rs resistor connected on the IS pin. Therefore, the current modulation signals can be generated on the TRX pin by control the TX pin voltage level. The current modulation signal can return to the master controller through the power supply line.





Offline Detection

Refer to the application circuit when reading the following description. When the master controller power line is offline, the TRX pin will be pulled low via $2M\Omega$ resistor which will enable offline detection function. In this case, the CMP and OPA functions will be foreced to disable to reduce power consumption. The offline mechanism will be dispelled until the power line restarts to be powered on to pull high the TRX pin voltage.

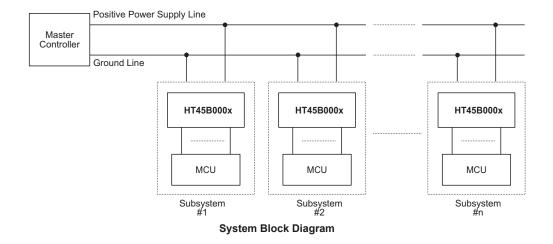
Current Modulator

The devices can modulate power line current by adding additional constant current source which control by TX pin. When TX pin connect to low level will enable this current modulation function. The modulation current can be calculated by the following formula:

 $I_{IS} = 1.5 V/R_S$

Application Considerations

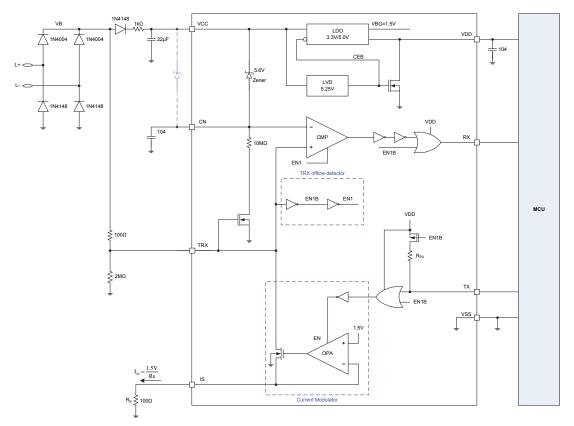
It is envisaged that the devices will be used together with microcontroller based subsystems which will be required to provide two I/O pins for data transmission and reception. The MCU pin connected to theTX pin must be setup as an output while the MCU pin connected to the RX pin must be setup as an input.





Application Circuits

The following application circuit shows the device used in conjunction with a microcontroller.





Package Information

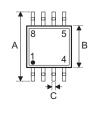
Note that the package information provided here is for consultation purposes only. As this information may be updated at regular intervals users are reminded to consult the <u>Holtek website</u> for the latest version of the <u>Package/Carton Information</u>.

Additional supplementary information with regard to packaging is listed below. Click on the relevant section to be transferred to the relevant website page.

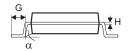
- Further Package Information (include Outline Dimensions, Product Tape and Reel Specifications)
- Packing Meterials Information
- Carton information



8-pin SOP (150mil) Outline Dimensions







Symbol	Dimensions in inch						
Symbol	Min.	Nom.	Max.				
A	—	0.236 BSC	—				
В	_	0.154 BSC	—				
С	C 0.012 —		0.020				
C'	— 0.193 BSC	0.193 BSC	—				
D	—	_	0.069				
E	_	0.050 BSC	—				
F	0.004	—	0.010				
G	0.016 —		0.050				
Н	0.004	_	0.010				
α	0°	—	8°				

Symbol	Dimensions in mm						
Symbol	Min.	Nom.	Max.				
A	_	6.00 BSC	—				
В	—	3.90 BSC	—				
С	0.31	—	0.51				
C'	—	4.90 BSC	—				
D	—	—	1.75				
E	_	1.27 BSC	—				
F	0.10	—	0.25				
G	0.40	_	1.27				
Н	0.10	_	0.25				
α	0°	_	8°				



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