

HT6010/HT6012/HT6014 3¹² Series of Encoders

Features

- Operating voltage: 2.4V~12V
- Low power and high noise immunity CMOS technology
- Low standby current
- Minimum transmission word
- Four words for TE trigger
- One word for Data trigger

- Built-in oscillator needs only 5% resistor
- Easy interface with an RF or an infrared transmission medium
- Minimal external components
- Pair with Holtek's 3¹² series of decoders
- 18-pin DIP, 20-pin SOP package

Applications

- Burglar alarm system
- Smoke and fire alarm system
- Garage door controllers
- Car door controllers

Selection Table

- Car alarm system
- Security system
- Cordless telephones
- Other remote control systems

General Description

The 3¹² encoders are a series of CMOS LSIs for remote control system applications. They are capable of encoding 12 bits of information which consists of N address bits and 12–N data bits. Each address/data input is externally trinary programmable if bonded out. They are otherwise set floating internally. Various packages of the 3¹² encoders offer flexible combinations of programmable address/data which meet various applications. The programmable address/data is transmitted together with the header bits via an RF or an infrared transmission medium upon receipt of a trigger signal. A $\overline{\text{TE}}$ (HT6010) or a DATA (HT6012/HT6014) trigger can be selected for application flexibility.

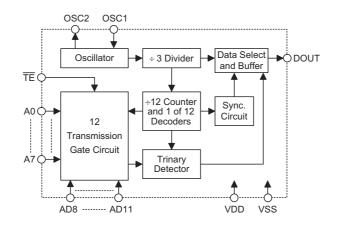
Function Part No.	Address No.	Address/ Data No.	Data No.	Oscillator	Trigger	LED Indicator	Package
HT6010	8	4	0	RC oscillator	TE	No	18DIP, 20SOP
HT6012	10	0	2	RC oscillator	D10~D11	Yes	18DIP, 20SOP
HT6014	8	0	4	RC oscillator	D8~D11	Yes	18DIP, 20SOP

Note: Address/Data represents pins that can be either address or data according to the application requirement.

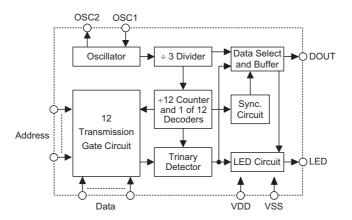


Block Diagram

TE Trigger HT6010



DATA Trigger HT6012/HT6014



Note: The address/data pins are available in various combinations (refer to the address/data table).



Pin Assignment

TE Trigger Type

8-Addr 4-Addr		ta		8-Address 4-Address/
			1	
A0 🗆	1	18		A0 🗖 2
A1 🗆	2	17		A1 🗖 3
A2 🗆	3	16	🗅 OSC2	A2 🗖 4
A3 🗆	4	15	D OSC1	A3 🗖 5
A4 🗆	5	14	D TE	A4 🗖 6
A5 🗆	6	13	D AD11	A5 🗖 7
A6 🗆	7	12	D AD10	A6 🗖 8
A7 🗆	8	11	AD9	A7 🗖 9
VSS 🗆	9	10		VSS 🗖 10
	HT601 			HT - 20

I-Address/Data								
NC	1	20	Ы №С					
A0 🗆	2	19						
A1 🗆	3	18						
A2 🗆	4	17	D OSC2					
A3 🗆	5	16	OSC1					
A4 🗆	6	15						
A5 🗆	7	14	AD11					
A6 🗆	8	13	AD10					
A7 🗆	9	12	🗆 AD9					
VSS 🗆	10	11	D AD8					
-	HT6010 - 20 SOP-A							

DATA Trigger Type

10-Address
2-Data

A0 🗆	1	\cup	18	þ	VDD			
A1 🗆	2		17	þ	DOUT			
A2 🗆	3		16	þ	OSC2			
A3 🗆	4		15	þ	OSC1			
A4 🗆	5		14	þ	LED			
A5 🗆	6		13	þ	D11			
A6 🗆	7		12	Þ	D10			
A7 🗆	8		11	þ	A9			
VSS□	9		10	Þ	A8			
I	HT6012 							

8-Address 4-Data

-Data			
NC	1	20	
A0 🗆	2	19	
A1 🗆	3	18	
A2 🗆	4	17	osc2
A3 🗆	5	16	OSC1
A4 🗆	6	15	
A5 🗆	7	14	D11
A6 🗆	8	13	D10
A7 🗆	9	12	D9
VSS 🗆	10	11	D8 🗆
	HT60 ²		

-20 SOP-A

10-Address 2-Data								
	1	20	□NC					
A0 🗆	2	19						
A1 🗆	3	18	□ dout					
A2 🗆	4	17	⊐osc2					
A3 🗆	5	16	□ OSC1					
A4 🗆	6	15	LED					
A5 🗆	7	14	□ D11					
A6 🗆	8	13	□ D10					
A7 🗆	9	12	□ A9					
VSS 🗆	10	11	□ A8					
HT6012 -20 SOP-A								

8-Address 4-Data

A0	1	U	18					
A1 🗆	2		17	🗆 DOUT				
A2	3		16	□ osc2				
A3 🗆	4		15	□ OSC1				
A4 🗆	5		14	LED				
A5 🗆	6		13	🗆 D11				
A6 🗆	7		12	D10				
A7 🗆	8		11	🗆 D9				
VSS□	9		10	🗆 D8				
	HT6014							
	-18	B DIF	'-A					



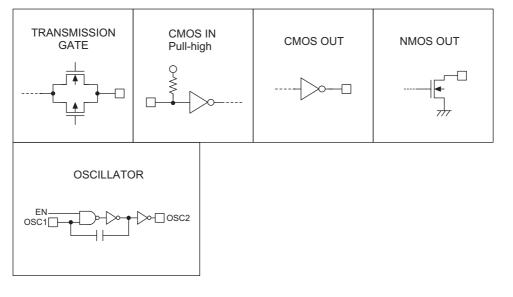
Pin Description

Pin Name	I/O	Internal Connection	Description
A0~A9	I	TRANSMISSION GATE	Input pins for address A0~A9 setting They can be externally set to VDD or VSS or left open.
AD8~AD11	I	TRANSMISSION GATE	Input pins for address/data (AD8~AD11) setting They can be externally set to VDD or VSS or left open.
D8~D11	I	CMOS IN Pull-high	Input pins for data (D8~D11) setting and transmission enable (active low) They can be externally set to VSS or left open (see Note).
DOUT	0	CMOS OUT	Encoder data serial transmission output
LED	0	NMOS OUT	Transmission enable indicator, active low
TE	I	CMOS IN Pull-high	Transmission enable, active low (see Note)
OSC1	Ι	OSCILLATOR	Oscillator input pin
OSC2	0	OSCILLATOR	Oscillator output pin
VSS			Negative power supply, ground
VDD			Positive power supply

Note: D8~D11 are data input and transmission enable pins of the HT6012/HT6014.

 $\overline{\text{TE}}$ is the transmission enable pin of the HT6010.

Approximate Internal Connections



Absolute Maximum Ratings

Supply Voltage	V _{SS} –0.3V to V _{SS} +13V	Storage Temperature	–50°C to 125°C
Input Voltage	V _{SS} -0.3 to V _{DD} +0.3V	Operating Temperature	–20°C to 75°C

Note: These are stress ratings only. Stresses exceeding the range specified under "Absolute Maximum Ratings" may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.



Ta=25°C

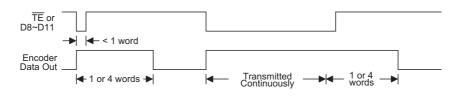
Electrical Characteristics

Cumb al	Demonster		Test Conditions	— 600 1200 1.5 3 —	_		
Symbol	Parameter	V _{DD}	Conditions		Unit		
V _{DD}	Operating Voltage	_		2.4	5	12	V
		3V			0.1	1	μA
I _{STB}	Standby Current	12V	Oscillator stops		2	4	μA
		3V			250	500	μA
I _{DD}	Operating Current		No load, f _{OSC} =3kHz		600	1200	μA
I _{LED}	LED Sink Current	5V	V _{LED} =0.5V	1.5	3	_	mA
		5V	V _{OH} =0.9V _{DD} (Source)	-0.6	-1.2		mA
IDOUT	Output Drive Current	5V	V _{OL} =0.1V _{DD} (Sink)	0.6	1.2		mA
V _{IH}	"H" Input Voltage	_		0.8V _{DD}		V _{DD}	V
V _{IL}	"L" Input Voltage	_		0		$0.2V_{DD}$	V
f _{OSC}	Oscillator Frequency	5V	R_{OSC} =1M Ω		3	_	kHz
RTE	TE Pull-high Resistance	5V	V _{TE} =0V	_	1.5	3	MΩ
R _{DATA}	D8~D11 Pull-high Resistance	5V	V _{DATA} =0V	_	1.5	3	MΩ

Functional Description

Operation

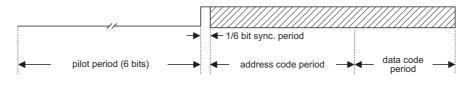
The 3^{12} series of encoders begin with a four (HT6010) or a one (HT6012/HT6014) word transmission cycle upon receipt of a transmission enable (TE for the HT6010 or D8~D11 for the HT6012/HT6014, active low). This cycle will repeat itself as long as the transmission enable (TE or D8~D11) is held low. Once the transmission enable returns high the encoder output completes its final cycle and then stops as shown below.





Information Word

An information word is composed of four periods as shown:

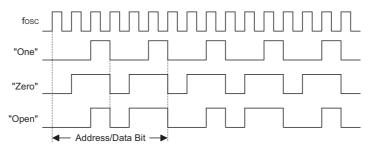


Composition of Information



Address/Data Waveform

Each programmable address/data pin can be externally set to one of the following three logic states:



Address/Data Bit Waveform

The "Open" state data input is interpreted as logic high by the decoder since its output has only two states.

Address/Data Programming (Preset)

The status of each address/data pin can be individually preset to a logic "high", "low", or "floating". If a transmission enable signal is applied, the encoder scans and transmits the status of the 12 bits of address/data serially in the order A0 to AD11 for the HT6010 and A0 to D11 for the HT6012/HT6014.

If the trigger signal is not applied, the chip only consumes a standby current which is less than $1\mu A$ (for V_{DD}=5V).

The address pins are usually preset so as to transmit data codes with their own particular security codes by the DIP switches or PCB wiring, while data is selected using push button or electronic switches.

Address/Data Sequence

The following table provides the position of the address/data sequence for various models of the 3¹² series encoders.

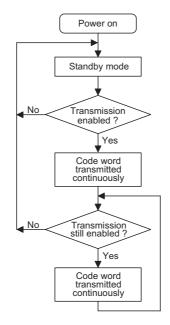
Part No.		Address/Data Bits										
	0	1	2	3	4	5	6	7	8	9	10	11
HT6010	A0	A1	A2	A3	A4	A5	A6	A7	AD8	AD9	AD10	AD11
HT6012	A0	A1	A2	A3	A4	A5	A6	A7	A8	A9	D10	D11
HT6014	A0	A1	A2	A3	A4	A5	A6	A7	D8	D9	D10	D11

Transmission Enable

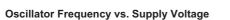
For the $\overline{\text{TE}}$ trigger type of encoders, transmission is enabled by applying a low signal to the $\overline{\text{TE}}$ pin. But for the Data trigger type, it is enabled by applying a low signal to one of the data pins D8~D11.

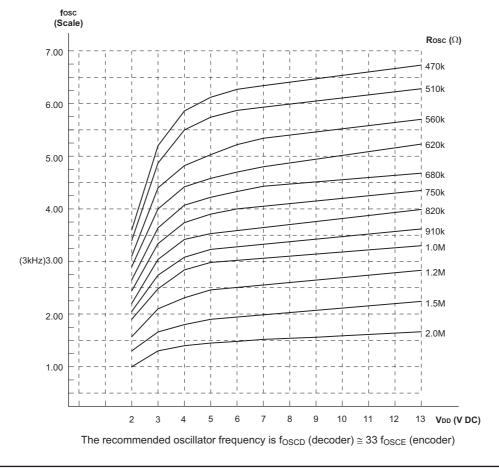


Flowchart



Note: D8~D11 are transmission enable of the HT6012/HT6014. $\overline{\text{TE}}$ is the transmission enable of the HT6010.

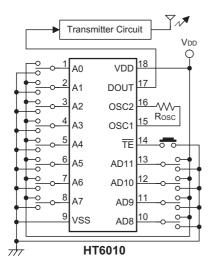


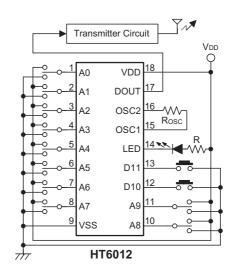


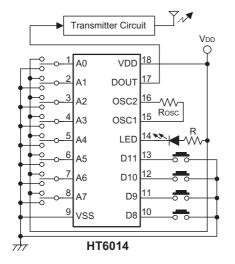
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Application Circuits



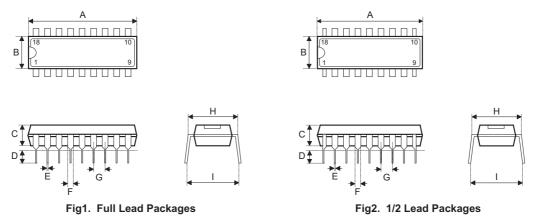






Package Information

18-pin DIP (300mil) Outline Dimensions



• MS-001d (see fig1)

Symbol	Dimensions in mil		
	Min.	Nom.	Max.
A	880	—	920
В	240	_	280
С	115		195
D	115		150
E	14		22
F	45		70
G		100	_
Н	300	_	325
I			430

• MS-001d (see fig2)

Symbol	Dimensions in mil		
	Min.	Nom.	Max.
A	845	—	880
В	240		280
С	115		195
D	115		150
E	14		22
F	45		70
G	_	100	—
Н	300		325
I			430

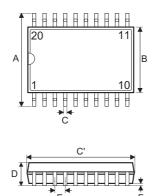


• MO-095a (see fig2)

Symbol	Dimensions in mil		
	Min.	Nom.	Max.
А	845		885
В	275		295
С	120		150
D	110		150
E	14		22
F	45		60
G		100	_
н	300		325
I	_		430



20-pin SOP (300mil) Outline Dimensions





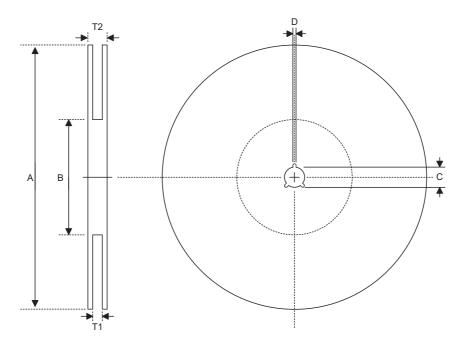
• MS-013

Symbol	Dimensions in mil		
	Min.	Nom.	Max.
А	393	_	419
В	256	_	300
С	12	_	20
C′	496	_	512
D	_	_	104
E	_	50	
F	4	_	12
G	16	_	50
Н	8	_	13
α	0°	_	8 °



Product Tape and Reel Specifications

Reel Dimensions

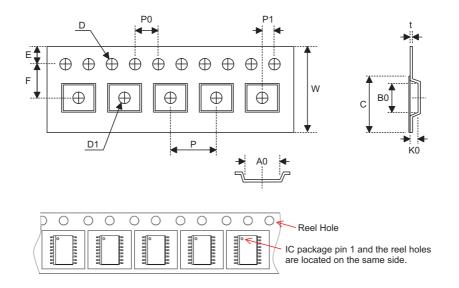


SOP 20W

Symbol	Description	Dimensions in mm
А	Reel Outer Diameter	330.0±1.0
В	Reel Inner Diameter	100.0±1.5
С	Spindle Hole Diameter	13.0 ^{+0.5/-0.2}
D	Key Slit Width	2.0±0.5
T1	Space Between Flange	24.8 ^{+0.3/-0.2}
T2	Reel Thickness	30.2±0.2



Carrier Tape Dimensions



SOP 20W

Symbol	Description	Dimensions in mm
W	Carrier Tape Width	24.0 ^{+0.3/-0.1}
Р	Cavity Pitch	12.0±0.1
E	Perforation Position	1.75±0.10
F	Cavity to Perforation (Width Direction)	11.5±0.1
D	Perforation Diameter	1.5 ^{+0.1/-0.0}
D1	Cavity Hole Diameter	1.50 ^{+0.25/-0.00}
P0	Perforation Pitch	4.0±0.1
P1	Cavity to Perforation (Length Direction)	2.0±0.1
A0	Cavity Length	10.8±0.1
В0	Cavity Width	13.3±0.1
K0	Cavity Depth	3.2±0.1
t	Carrier Tape Thickness	0.30±0.05
С	Cover Tape Width	21.3±0.1



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