



## HT7936A/HT7936B

### White LED Step-up Charge Pump Converter

#### Features

- Input voltage range: 2.8V to  $V_{OUT}$
- Driving current: 110mA (HT7936A), 150mA (HT7936B)
- Fixed output voltage: 5V (HT7936A), 4.5V (HT7936B)
- Oscillator frequency: 1MHz
- Soft start function, current limit, short circuit protection
- Small outline SOT23-6 package

#### Applications

- Cellular phones
- PDAs
- DSCs
- Handheld devices
- White LED display backlighting

#### General Description

The HT7936A and HT7936B are charge-pump type DC-DC step-up converter devices, which can generate regulated fixed output voltages of 5.0V and 4.5V. Their high degree of functional integration requires only the addition of three external capacitors, namely a flying capacitor and an input and output capacitor, to implement a full step-up conversion function.

The devices have the additional benefit of an integrated soft start function to reduce the level of in-rush current

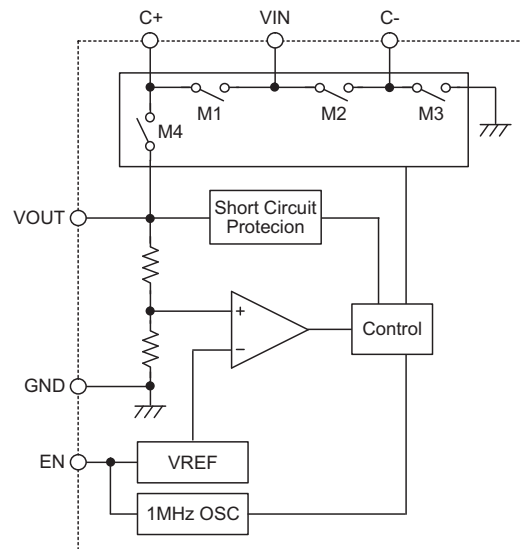
during device power on. The operating voltage range is from 2.8V to the full output voltage value,  $V_{OUT}$ .

To meet the demands of today's power sensitive applications, the devices, have a very low standby current value as well as the extra safety features of current limit and short circuit protection. This range of device features, supplied in 6-pin SOT-23-6 package type, will ensure that these device will find excellent use in a wide range of DC-DC step-up converter applications.

#### Selection Table

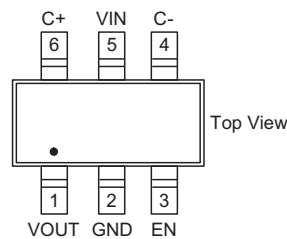
Part No.	Output Voltage	Tolerance	Package
HT7936A	5.0V	±4%	SOT23-6
HT7936B	4.5V	±4%	SOT23-6

**Block Diagram**



Note: During operation the following sequence occurs:  
 1. M1, M3 On / M2, M4 Off  
 2. M2, M4 On / M1, M3 Off

**Pin Assignment**



**Pin Description**

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Pin No.	Pin Name	Description
1	VOUT	Regulated Output Voltage. $V_{OUT}$ should be bypassed with a $1\mu F$ to $10\mu F$ low ESR ceramic capacitor placed as close as possible to the pin for best performance.
2	GND	Ground. This pin is the voltage reference for the regulator output voltage.
3	EN	Device On/Off Control 1: Enable 0: Disable Pin must not be left floating.
4	C-	Capacitor Negative Pin. Flying capacitor negative terminal.
5	VIN	Input Supply Voltage. $V_{IN}$ should be bypassed with a $1\mu F$ to $10\mu F$ low ESR ceramic capacitor.
6	C+	Capacitor Positive Pin. Flying capacitor positive terminal.

**Absolute Maximum Ratings**

Input Supply Voltage.....6V      Other Pins Voltage .....6V  
 Operating Temperature Range .....-40°C to 85°C      Maximum Junction Temperature.....125°C

Note: These are stress ratings only. Stresses exceeding the range specified under "Absolute Maximum Ratings" may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

**Electrical Characteristics**

$V_{IN}=V_{EN}=3.7V$ ,  $C_{IN}=C_{PUMP}=C_{OUT}=1\mu F$ ,  $T_a=25^\circ C$ , unless otherwise specified.

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$V_{IN}$	Input Supply Voltage	HT7936A	2.8	—	5.0	V
		HT7936B	2.8	—	4.5	V
$V_{OUT}$	Output Voltage	HT7936A, $V_{IN}>3.2V$ , $I_O\leq 110mA$	4.80	—	5.20	V
		HT7936B, $V_{IN}>3.2V$ , $I_O\leq 140mA$	4.32	—	4.68	V
$I_{SHDN}$	Shutdown Current	$V_{EN}=0$	—	0.1	1.0	$\mu A$
$I_Q$	Operation Current	—	—	2.0	4.0	mA
$I_O$	Output Current	HT7936A	110	—	—	mA
		HT7936B	150	—	—	mA
$I_{SHORT}$	Short Circuit Current	—	—	60	90	mA
$I_{LIMIT}$	Current Limit	—	—	370	500	mA
$f_{OSC}$	Switching Frequency	—	0.8	1.0	1.3	MHz
$V_{ENH}$	EN Pin Voltage High	$2.8V \leq V_{IN} \leq 5.0V$	1.5	—	—	V
$V_{ENL}$	EN Pin Voltage Low	$2.8V \leq V_{IN} \leq 5.0V$	—	—	0.4	V
$V_{RIPPLE}$	Output Ripple Voltage	$I_O=60mA$ , $C_{OUT}=2.2\mu F$	—	30	—	mV
$\eta$	Efficiency	HT7936A, $V_{IN}=3.0V$ , $I_O=50mA$	—	80	—	%
		HT7936B, $V_{IN}=2.8V$ , $I_O=70mA$	—	80	—	%

Note: Absolute maximum ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but do not guarantee specific performance limits. The guaranteed specifications apply only for the test conditions listed.

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Specifications are production tested at  $T_a=25^\circ C$ . Specifications over  $-40^\circ C$  to  $+85^\circ C$  operating temperature range are assured by design.

## Functional Description

These DC-DC step-up converters can generate fixed level output voltages higher than their input supply voltage.

### Operation

Utilising a charge pump method with three external capacitors to store and transfer energy, the devices can transfer electrical energy from input to output at different voltage levels. Since the capacitors are not able to change their voltage level abruptly, the  $V_{OUT}/V_{IN}$  voltage ratio of  $V_{OUT}$  over  $V_{IN}$  is limited within a fixed range. Capacitive voltage conversion is obtained by the periodic switching of a capacitor. It first charges the capacitor  $C_{PUMP}$  by connecting it across a voltage source after which it is connected to the output. Referring to Figure.1, during the on state of the internal clock, M1 and M3 are closed, M2 and M4 are opened, which charges  $C_{PUMP}$  to a  $V_{IN}$  level. During the off state of the internal clock, M2 and M4 are closed, M1 and M3 are opened, which transfers the energy to the output. The output voltage is therefore  $V_{IN}$  plus  $V_{CPUMP}$ , which is in effect equal to  $2V_{IN}$ .

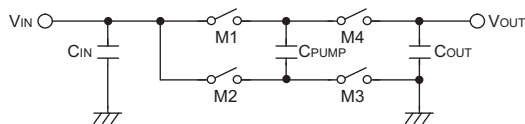


Figure.1

### Shutdown

The devices can be shutdown by setting the EN input pin to a low level. In the shutdown mode, the output is disconnected from the input. The input current will reduce to an extremely low level as most of the internal circuitry is inactive. As the EN pin is a high impedance input, it must not be allowed to float.

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### Short Circuit Protection

The devices contain integrated short circuit protection circuits with current limiting protection. During a short circuit condition, the output current is automatically limited to a typical value of approximately 60mA. If the fault does not clear itself, the protect operation will repeat continuously. This protection feature allows the devices to operate indefinitely under short circuit conditions without damaging the device.

### Capacitor Selection

Careful selection of the three external capacitors  $C_{IN}$ ,  $C_{OUT}$  and  $C_{PUMP}$  is important because they will affect ramp-up time, output ripple and transient performance. Optimum performance will be obtained when low ESR ( $<100m\Omega$ ) ceramic capacitors are used for  $C_{IN}$  and  $C_{OUT}$  and  $C_{PUMP}$ .

In general, a low ESR may be defined as less than  $100m\Omega$ . In all cases, X7R or X5R dielectrics are recommended. For certain applications, low ESR Tantalum capacitors may be substituted, however optimum output ripple performance may not be attainable. Aluminum electrolytic capacitors are not recommended for use with these devices due to their inherent high ESR characteristics.

In general the  $C_{IN}$ ,  $C_{OUT}$  and  $C_{PUMP}$  capacitors may range from  $1\mu F$  to  $10\mu F$  for heavy output load conditions. If the  $C_{PUMP}$  capacitor is increased, then  $C_{OUT}$  should also be increased by the same ratio to minimise output ripple. Lowering the values of  $C_{IN}$ ,  $C_{OUT}$  and  $C_{PUMP}$  may decrease the ramp-up time of  $V_{OUT}$ , but it will inversely increase the output ripple.

### Efficiency

The efficiency of the charge pump regulator varies with the output voltage type, the applied input voltage, the load current and the internal operation mode of the device.

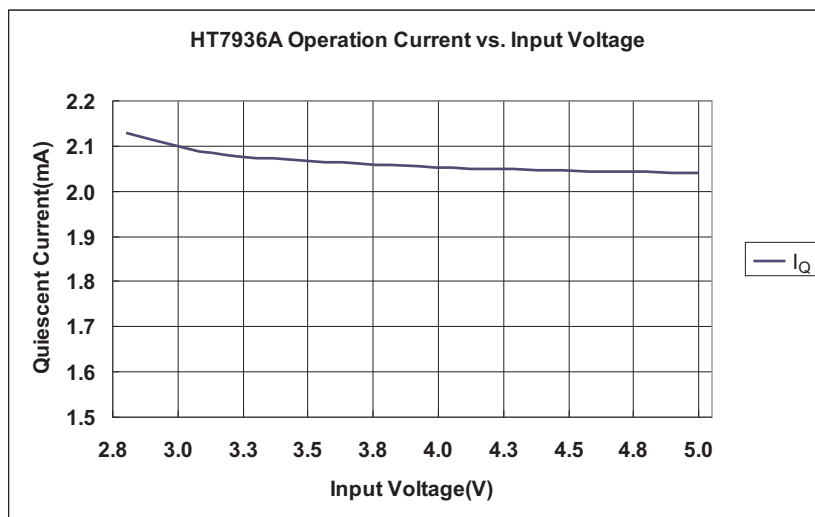
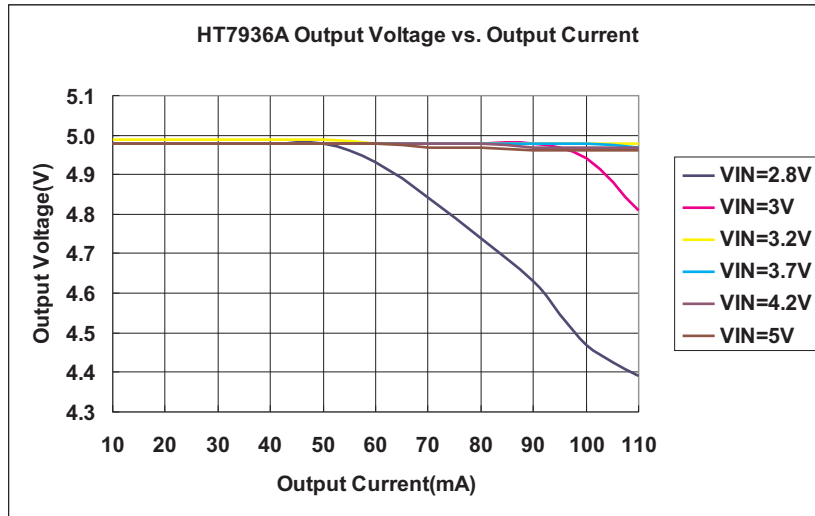
The approximate device efficiency is given by:

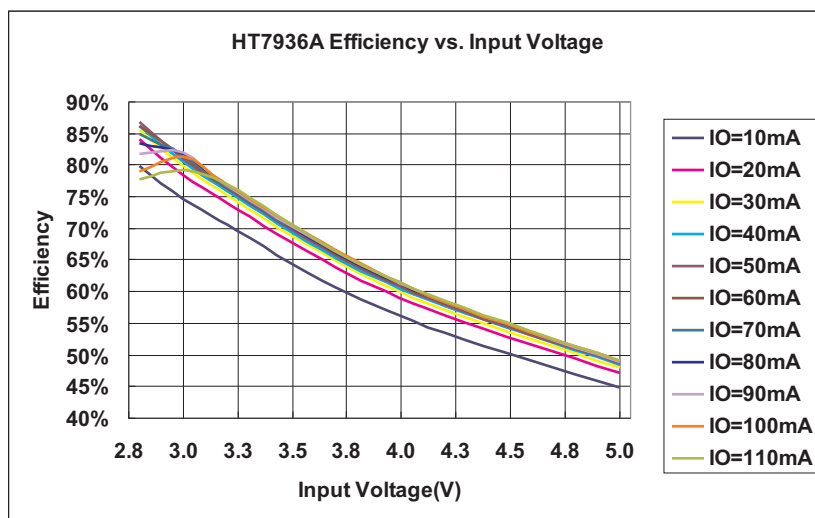
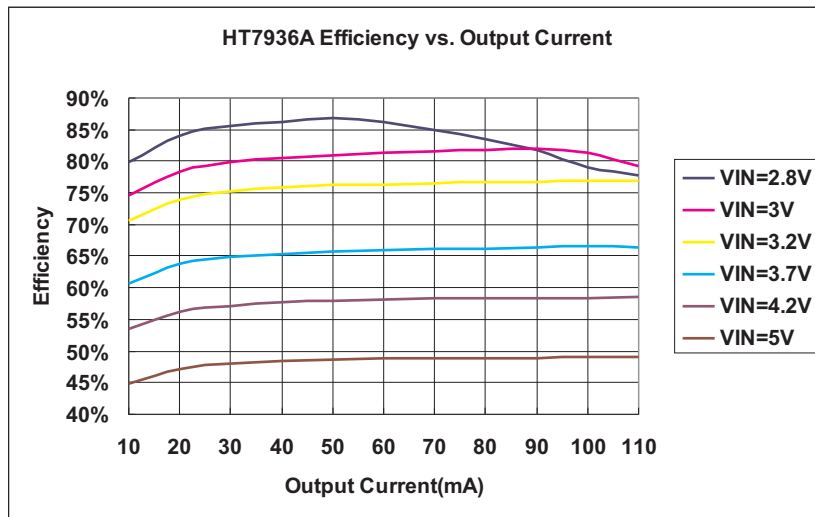
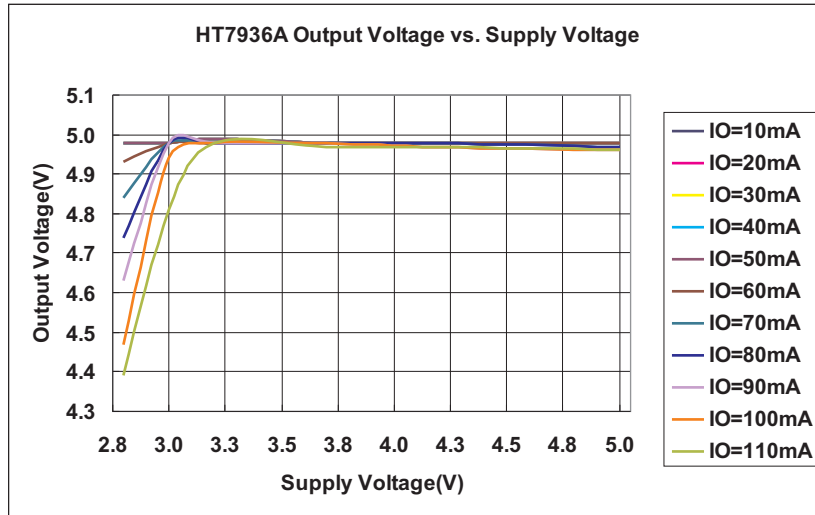
$$\begin{aligned} \eta(\%) &= \frac{P_{OUT}}{P_{IN}} \times 100 \\ &= \frac{V_{OUT} \times I_{OUT}}{V_{IN} \times 2I_{OUT}} \times 100 \\ &= \frac{V_{OUT}}{2V_{IN}} \times 100 \text{ (X2 Charge Pump Operation Mode)} \end{aligned}$$

Due to internal switching losses and IC quiescent current consumption, the actual measured efficiency will decrease.

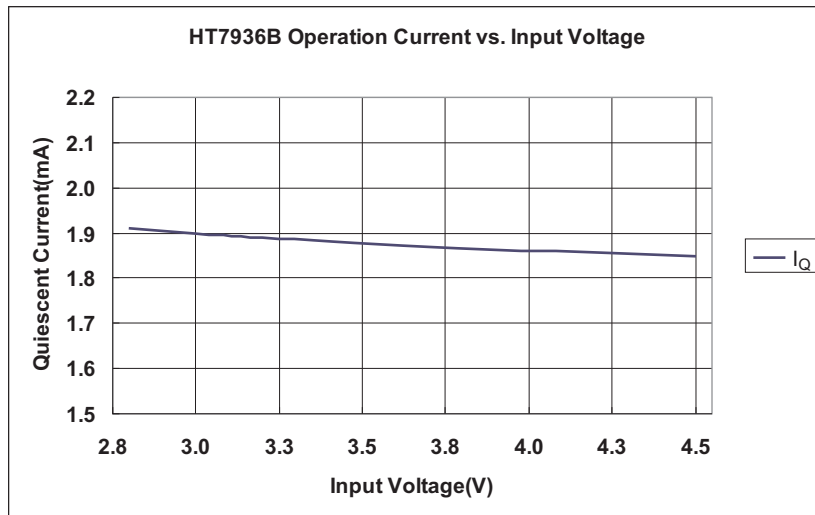
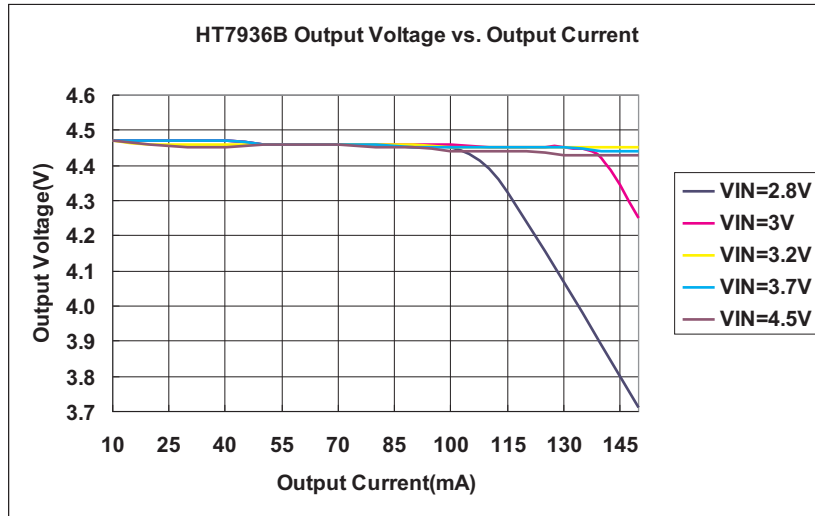
**Typical Operating Characteristics**

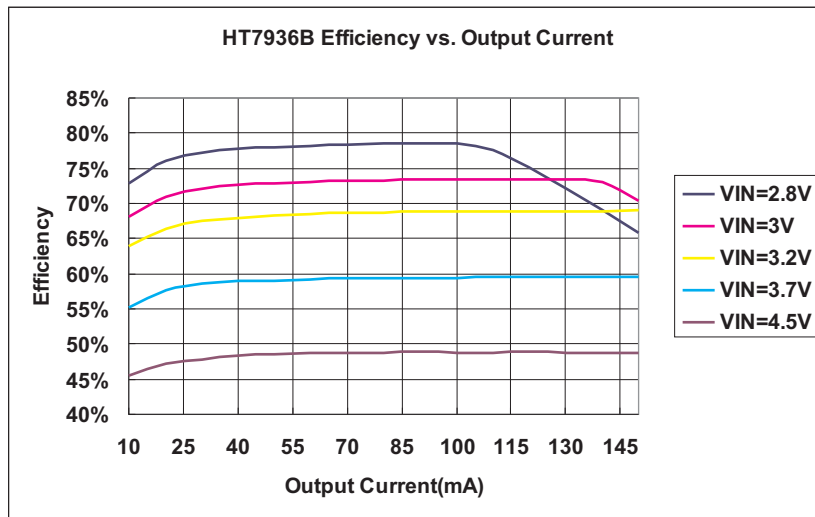
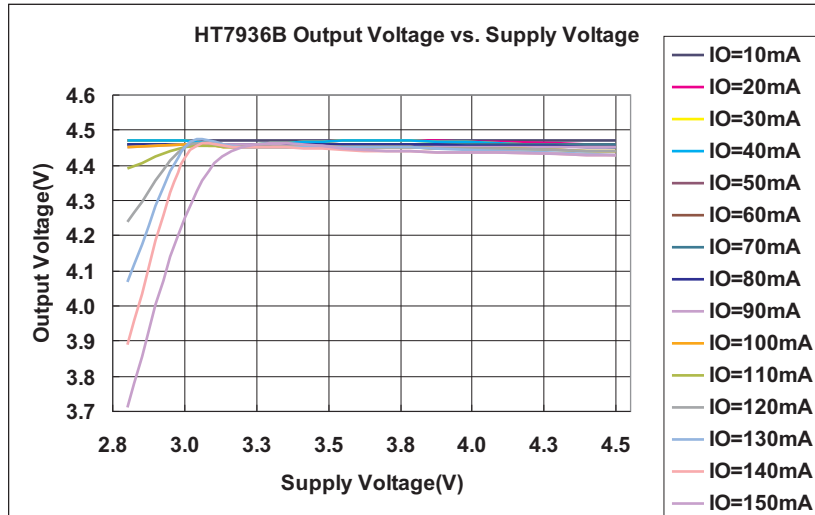
For HT7936A,  $C_{IN}=C_{OUT}=2.2\mu F$ ,  $C_{PUMP}=0.22\mu F$ ,  $T_a=25^\circ C$ , unless otherwise specified



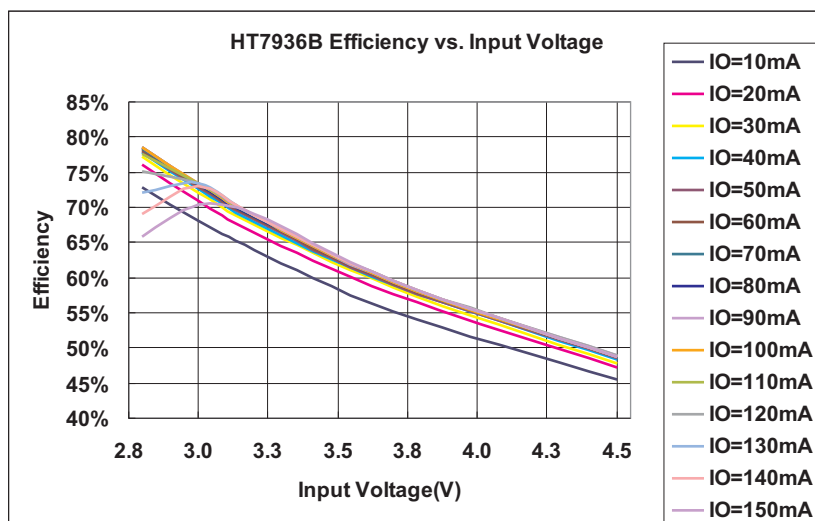


For HT7936B,  $C_{IN}=C_{OUT}=2.2\mu F$ ,  $C_{PUMP}=0.22\mu F$ ,  $T_a=25^\circ C$ , unless otherwise specified



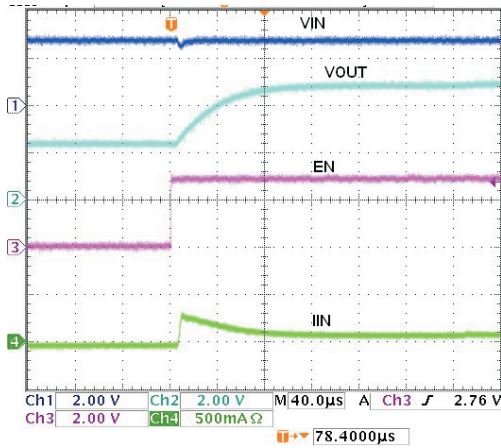


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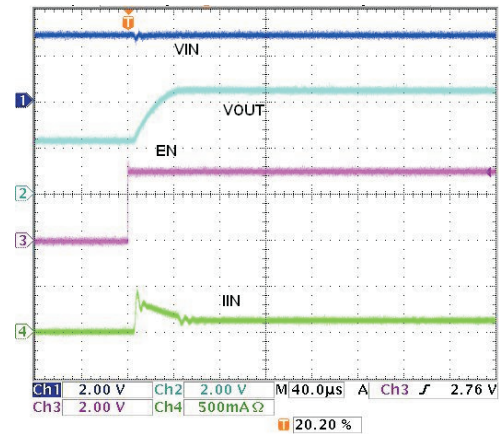




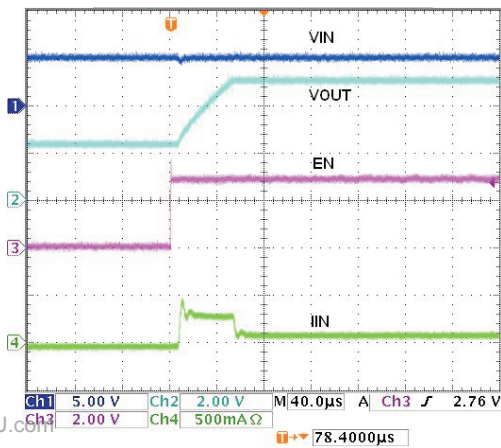
Inrush Current



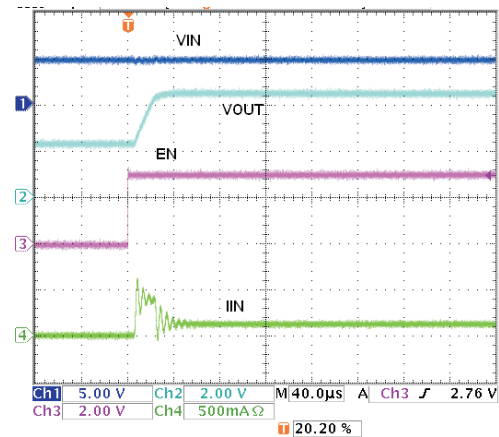
HT7936A  $V_{IN}=2.8V$ ,  $C_{IN}=C_{OUT}=2.2\mu F$ ,  
 $C_{PUMP}=0.22\mu F$



HT7936B  $V_{IN}=2.8V$ ,  $C_{IN}=C_{OUT}=2.2\mu F$ ,  
 $C_{PUMP}=0.22\mu F$



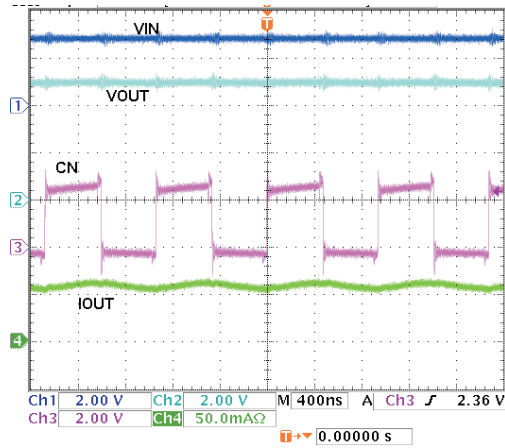
HT7936A  $V_{IN}=5.0V$ ,  $C_{IN}=C_{OUT}=2.2\mu F$ ,  
 $C_{PUMP}=0.22\mu F$



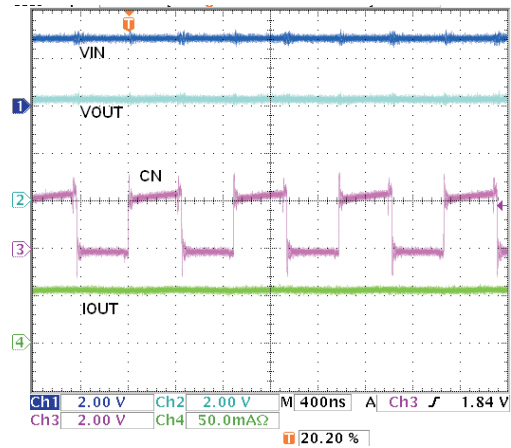
HT7936B  $V_{IN}=4.5V$ ,  $C_{IN}=C_{OUT}=2.2\mu F$ ,  
 $C_{PUMP}=0.22\mu F$

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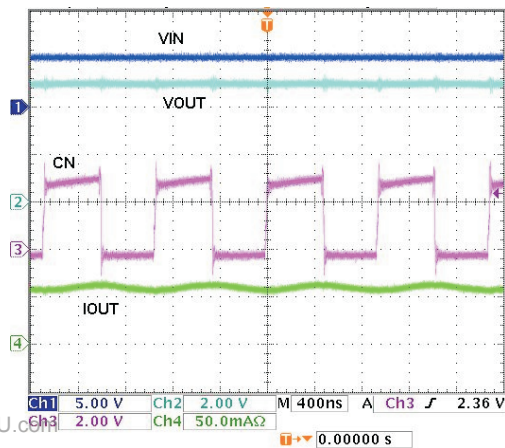
Normal Operation



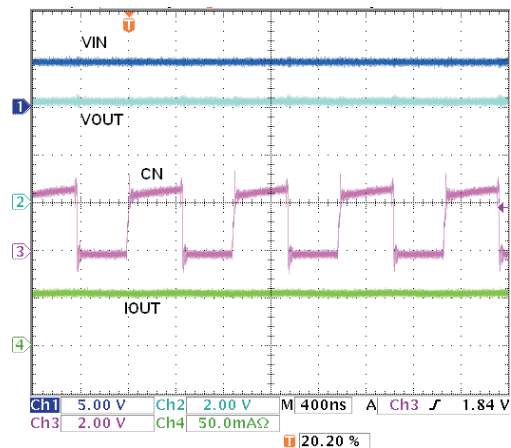
HT7936A  $V_{IN}=2.8V$ ,  $C_{IN}=C_{OUT}=2.2\mu F$ ,  
 $C_{PUMP}=0.22\mu F$



HT7936B  $V_{IN}=2.8V$ ,  $C_{IN}=C_{OUT}=2.2\mu F$ ,  
 $C_{PUMP}=0.22\mu F$



HT7936A  $V_{IN}=5.0V$ ,  $C_{IN}=C_{OUT}=2.2\mu F$ ,  
 $C_{PUMP}=0.22\mu F$

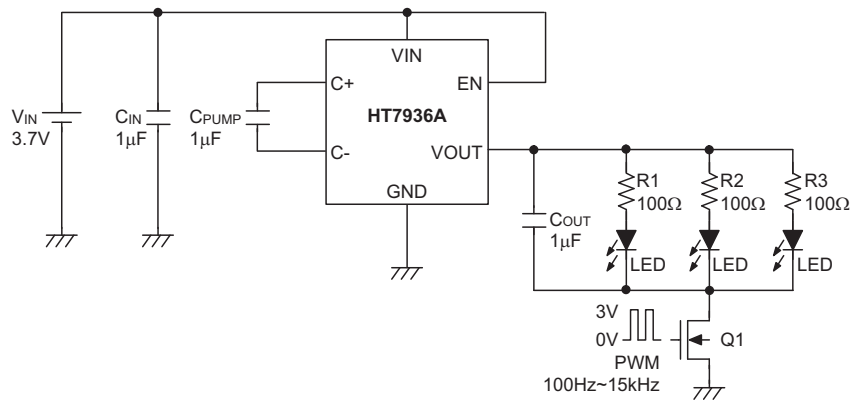
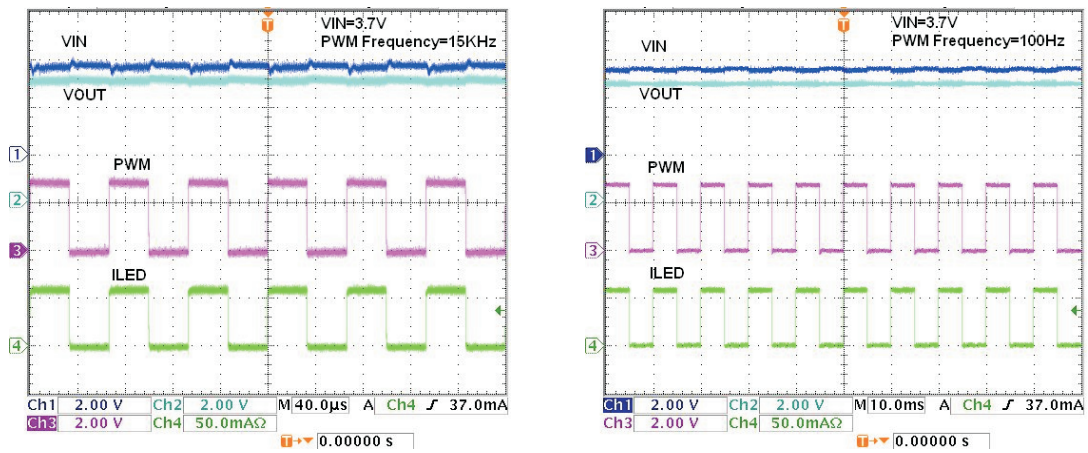


HT7936B  $V_{IN}=4.5V$ ,  $C_{IN}=C_{OUT}=2.2\mu F$ ,  
 $C_{PUMP}=0.22\mu F$

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**Dimming Operation**

HT7936A, Refer to Figure.2



**Figure.2**

HT7936A, Refer to Figure.3

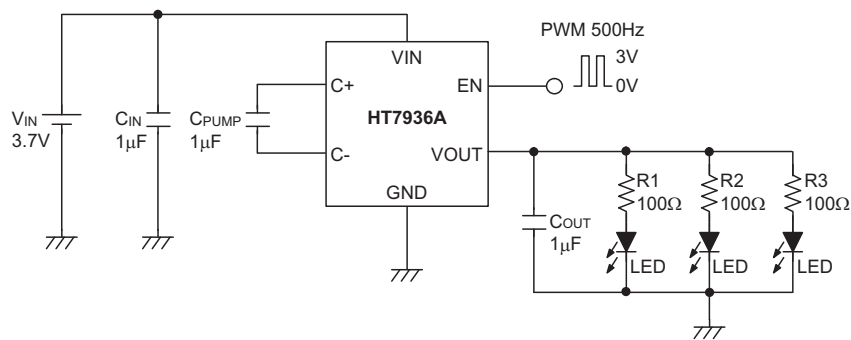
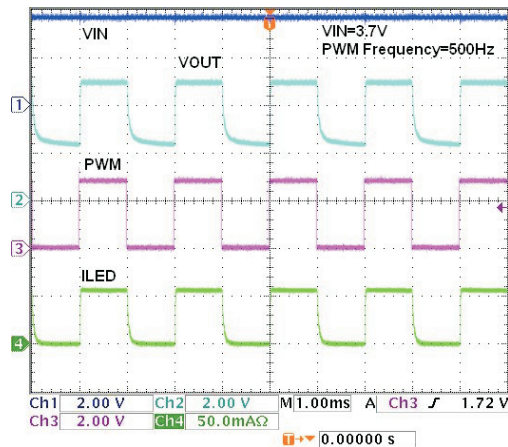
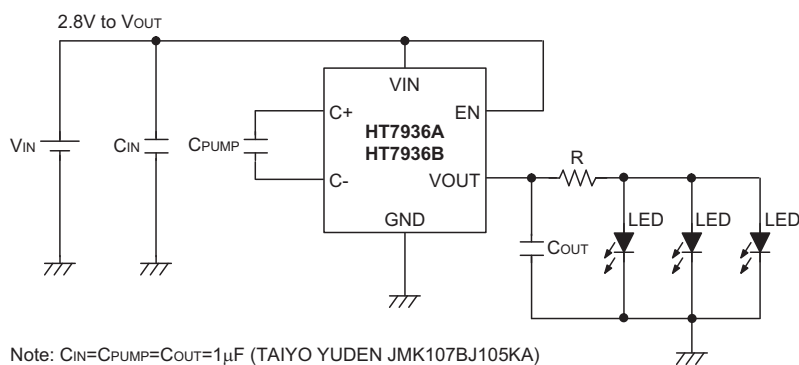


Figure.3

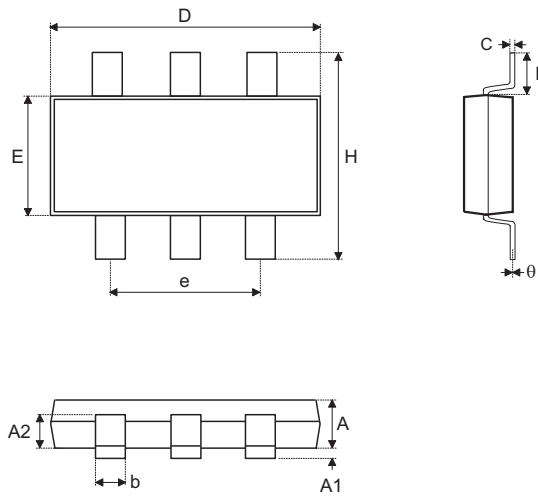
**Application Circuits**

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**Package Information**

**6-pin SOT23-6 Outline Dimensions**

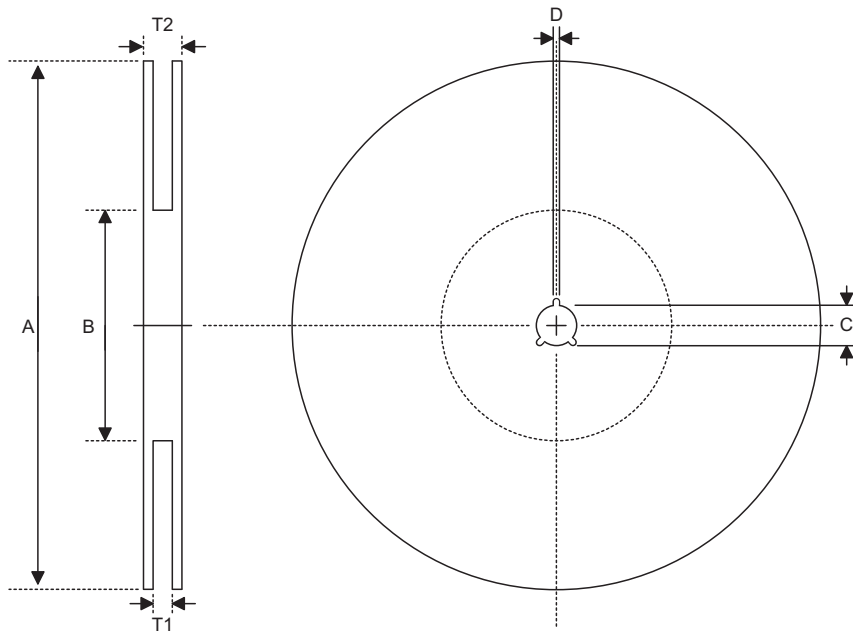


Symbol	Dimensions in mm		
	Min.	Nom.	Max.
A	1.0	—	1.3
A1	—	—	0.1
A2	0.7	—	0.9
b	0.35	—	0.50
C	0.10	—	0.25
D	2.7	—	3.1
E	1.4	—	1.8
e	—	1.9	—
H	2.6	—	3.0
L	0.37	—	—
θ	1°	—	9°

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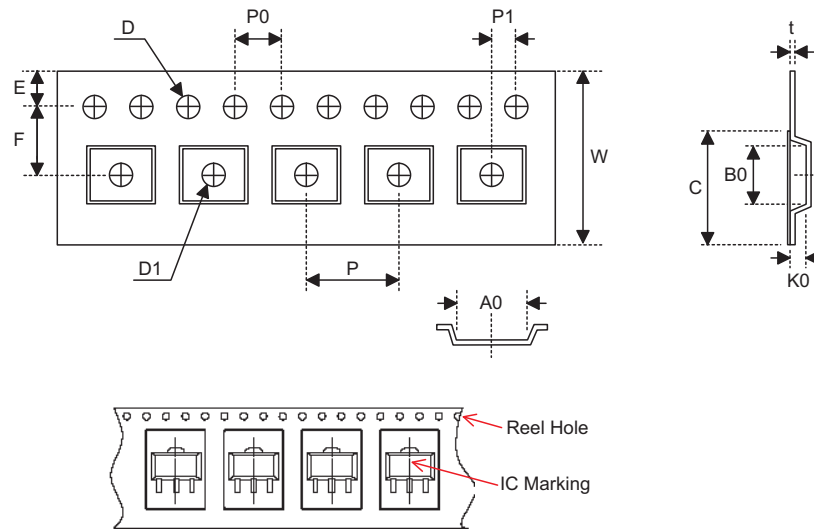
**Product Tape and Reel Specifications**

**Reel Dimensions**



SOT23-6

Symbol	Description	Dimensions in mm
A	Reel Outer Diameter	178.0±1.0
B	Reel Inner Diameter	62.0±1.0
C	Spindle Hole Diameter	13.0±0.2
D	Key Slit Width	2.50±0.25
T1	Space Between Flange	8.4 <sup>+1.5/-0.0</sup>
T2	Reel Thickness	11.4 <sup>+1.5/-0.0</sup>

**Carrier Tape Dimensions**

**SOT23-6**

Symbol	Description	Dimensions in mm
W	Carrier Tape Width	8.0±0.3
P	Cavity Pitch	4.0±0.1
E	Perforation Position	1.75±0.1
F	Cavity to Perforation (Width Direction)	3.50±0.05
D	Perforation Diameter	1.5 <sup>+0.1/-0.0</sup>
D1	Cavity Hole Diameter	1.5 <sup>+0.1/-0.0</sup>
P0	Perforation Pitch	4.0±0.1
P1	Cavity to Perforation (Length Direction)	2.00±0.05
A0	Cavity Length	3.15±0.1
B0	Cavity Width	3.2±0.1
K0	Cavity Depth	1.4±0.1
t	Carrier Tape Thickness	0.20±0.03
C	Cover Tape Width	5.3±0.1

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