

# HTR28 SERIES CONVERTERS – 30WATT

## 1 FEATURES

- 16 to 40 VDC input, typical 28V
- 30W output power
- -55 to +125 operation
- Fully isolated
- 100M minimum (500V DC) isolation
- Inhibit and indefinite short circuit protection
- 30W/in<sup>3</sup> power density
- Equivalent with Interpoint's MTR Series
- Hermetically sealed metal cases



Size: 53.98 × 28.58 × 10.16mm<sup>3</sup>(non-flanged)  
73.91 × 28.58 × 10.16mm<sup>3</sup>(flanged)

Triple Output Models:  
49.53 × 34.29 × 10.29mm<sup>3</sup>(non-flanged)  
69.09 × 34.29 × 10.29mm<sup>3</sup> (flanged)  
Weight: 52 grams (non-flanged)  
60 grams (flanged)

Figure 1 HTR28 series DC/DC converters

Table 1 Product models

MODELS		
SINGLE	DUAL	TRIPLE
HTR28S5(F)	HTR28D5(F)	HTR28T512(F)
HTR28S12(F)	HTR28D12(F)	HTR28T515(F)
HTR28S15(F)	HTR28D15(F)	HTR28T515-A
HTR28S3R3		HTR28T515-B
HTR28S18(F)		

## 2 DESCRIPTION

The HTR28 Series of DC/DC converters offer up to 30 watts of output power with high reliability. The converters are packaged in hermetically sealed metal cases, making them ideal for use in aviation, aerospace and other high reliability applications.

The HTR 28 series of converters use a single ended, pulse width modulated and transformer magnetic feedback topology design. The operating principle is that the sampling signal of output voltage, coupled by the opto-coupler, works together with the sampling signal of input loop current to regulate the pulse width of the controller. The double close loop control create constant voltage output and short circuit protection.

Thick film hybrid techniques provide the HTR 28 Series of converters with reliability levels and optimum miniaturization. The design and manufacturing process of HTR 28 Series of converters are in compliance with General Standards of Hybrid Integrated Circuits and detailed standards of manufacturing. Connected to a HFD-CE03 filter, the HTR 28 Series of converters can achieve better electromagnetic compatibility(EMC) performance.

### 3 ELECTRICAL PERFORMANCE

#### ABSOLUTE MAXIMUM RATINGS

- Input Voltage: 16 to 40 VDC
- Power Dissipation: 30 watts
- Lead Soldering Temperature: 300 (10s)
- Storage Temperature Range: -55 to +125
- Inhibit Voltage: 0.2V max
- External Synchronous Signals:
  - Frequency Range: 450k to 600kHz
  - Duty Ratio: 40% to 60%
  - Level: 0.8 V V 4.5V

#### RECOMMENDED OPERATING CONDITIONS

- Input VDC: 16 to 40 V
- Case Temperature(Tc): -55 to +125

### HTR28S5(F) , HTR28S12(F) , HTR28S15(F)

**Table 2 Electrical Characteristics:** (T<sub>CASE</sub> = -55°C to +125°C, V<sub>IN</sub> = 28V ± 0.5V, Full Load, Unless Otherwise Specified)

Single output models			HTR28S5(F)		HTR28S12(F)		HTR28S15(F)	
Parameter	Conditions		Min	Max	Min	Max	Min	Max
Output Voltage(V)	I <sub>o</sub> =full load	Ambient temperature	4.95	5.05	11.88	12.12	14.85	15.15
		high and low temperature	4.85	5.15	11.64	12.36	14.55	15.45
Output Current(A)	V <sub>in</sub> = 16 TO 40 VDC		—	5	—	2.5	—	2
Output Power(W)	—		—	25	—	30	—	30
Output Ripple Voltage (mV)	BW=10 kHz to 2 MHz I <sub>o</sub> =full load	Ambient temperature	—	50	—	40	—	40
		high and low temperature	—	90	—	90	—	90
Line Regulation (mV)	V <sub>in</sub> = 16 TO 40 VDC, I <sub>o</sub> =full load		—	50	—	50	—	50
Load Regulation (mV)	I <sub>o</sub> =No load to load		—	50	—	50	—	50
Input Ripple Current (mA)	BW=10 kHz to 10 MHz I <sub>o</sub> =full load		—	50	—	50	—	50
Efficiency (%)	I <sub>o</sub> =full load	Ambient temperature	76	—	80	—	80	—
		high and low temperature	73	—	77	—	78	—
Isolation (M )	Input to output or any pin to case (except pin7,8) at 500 VDC, T <sub>A</sub> = 25° C		100	—	100	—	100	—
Inhibit Function	T <sub>A</sub> = 25° C , Inhibit voltage, output disabled		yes		yes		yes	
Protection Function	T <sub>A</sub> = 25° C		yes		yes		yes	
Start-up Overshoot (mV pk)	V <sub>in</sub> =0 to 28V, I <sub>o</sub> =full load		—	50	—	120	—	150
Start-up Delay (ms)	V <sub>in</sub> =0 to 28V, I <sub>o</sub> =full load		—	5	—	5	—	5
Capacitive Load (μF)	T <sub>A</sub> = 25° C, No effect on DC performance		—	300	—	3000	—	3000

**(to be continued) Table 2 Electrical Characteristics**

Switching Frequency(kHz)	I <sub>o</sub> =full load	400	600	400	600	400	600
Step Load Response Transient (mV pK)	50% load -- full load -50% load	-300	300	-400	400	-500	500
Step Load Response Recovery (μs)	50% load -- full load -50% load	—	200	—	200	—	200
Step Line Response Transient (mV pK)	V <sub>in</sub> =16~40V, I <sub>o</sub> =full load V <sub>in</sub> =40~16V, I <sub>o</sub> =full load	-300	300	-500	500	-600	600
Step Line Response Recovery (μs)	V <sub>in</sub> =16~40V, I <sub>o</sub> =full load V <sub>in</sub> =40~16V, I <sub>o</sub> =full load	—	300	—	300	—	300
Load Fault Short Circuit recovery (ms)	short circuit to full load	—	5	—	5	—	5

**Notes to Specifications:**

The step load transition time should be greater than or equal to 10μs.

The step line transition time should be greater than or equal to 10μs.

Recovery time is measured from application of the transient to point at which V<sub>OUT</sub> is within 1% of V<sub>OUT</sub> at final value.

**HTR28S3R3 ,HTR28S18****Table3 Electrical Characteristics: (T<sub>CASE</sub> = -55°C to +125°C, V<sub>IN</sub> = 28V ± 0.5V, Full Loads, Unless Otherwise Specified)**

Single output models		HTR28S3R3		HTR28S18	
Parameter	Conditions	Min	Max	Min	Max
Output Voltage (V)	I <sub>o</sub> =full load Ambient temperature high and low temperature	3.267 3.201	3.333 3.399	17.82 17.46	18.18 18.54
Output Current(A)	V <sub>in</sub> = 16 TO 40 VDC	—	<b>6.06</b>	—	<b>1.67</b>
Output Power(W)	—	—	<b>20</b>	—	<b>30</b>
Output Ripple Voltage (mV)	BW=10kHz to 2 MHz I <sub>o</sub> =full load Ambient temperature high and low temperature	—	40 50	—	40 90
Line Regulation(mV)	V <sub>in</sub> = 16 TO 40 VDC, I <sub>o</sub> =full load	—	10	—	50
Load Regulation(mV)	I <sub>o</sub> =No load to load	—	10	—	50
Input Ripple Current(mA)	BW=10 kHz to 10 MHz, I <sub>o</sub> =full load	—	50	—	50
Efficiency (%)	I <sub>o</sub> =full load Ambient temperature high and low temperature	74 71	—	81 78	—
Isolation (M )	Input to output or any pin to case (except pin7,8) at 500 VDC, T <sub>A</sub> = 25° C	100	—	100	—
Inhibit Function	T <sub>A</sub> = 25° C , Inhibit voltage, output disabled	yes		yes	
Protection Function	T <sub>A</sub> = 25° C	yes		yes	
Start-up Overshoot (mV pK)	V <sub>in</sub> =0 to 28V, I <sub>o</sub> =full load	—	50	—	180
Start-up Delay(ms)	V <sub>in</sub> =0 to 28V, I <sub>o</sub> =full load	—	5	—	5
Capacitive Load( μ F)	T <sub>A</sub> = 25° C, No effect on DC performance	—	300	—	2000
Switching Frequency(kHz)	I <sub>o</sub> =full load	400	600	400	600
Step Load Response Transient (mV pK)	50% load -- full load -50% load	-250	250	-600	600
Step Load Response Recovery (μs)	50% load -- full load -50% load	—	200	—	200
Step Line Response Transient (mV pK)	V <sub>in</sub> =16~40V, I <sub>o</sub> =full load , V <sub>in</sub> =40~16V, I <sub>o</sub> =full load	-300	300	-800	800
Step Line Response Recovery (μs)	V <sub>in</sub> =16~40V, I <sub>o</sub> =full load , V <sub>in</sub> =40~16V, I <sub>o</sub> =full load	—	300	—	300
Load Fault Short Circuit recovery (ms)	short circuit to full load	—	5	—	5

## Notes to Specifications:

The step load transition time should be greater than or equal to 10 $\mu$ s.

The step line transition time should be greater than or equal to 10 $\mu$ s.

Recovery time is measured from application of the transient to point at which  $V_{OUT}$  is within 1% of  $V_{OUT}$  at final value.

## HTR28D5(F), HTR28D12(F), HTR28D15(F)

**Table 4 Electrical Characteristics:** ( $T_{CASE} = -55^{\circ}C$  to  $+125^{\circ}C$ ,  $V_{IN} = 28V \pm 0.5V$ , Full Loads, Unless Otherwise Specified)

Dual output models			HTR28D5(F)		HTR28D12(F)		HTR28D15(F)		
Parameter	Conditions		Min	Max	Min	Max	Min	Max	
Output Voltage (V)	$I_{O1}=I_{O2}=\text{full load}$	Ambient temperature high and low temperature	4.950	5.050	11.88	12.12	14.85	15.15	
			4.850	5.150	11.64	12.96	14.55	15.45	
		Ambient temperature high and low temperature	-5.075	-4.925	-12.18	-11.82	-15.23	-14.77	
			-5.172	-4.825	-12.42	-11.58	-15.53	-14.47	
Output Current(A)	$V_{IN} = 16 \text{ TO } 40 \text{ VDC}$		—	2.5	—	1.25	—	1	
Output Power (W)	—		—	25	—	30	—	30	
Output Ripple Voltage (mV)	BW=10 kHz to 2 MHz $I_{O1}=I_{O2}=\text{full load}$	Ambient temperature high and low temperature	—	40	—	80	—	80	
			—	90	—	120	—	120	
		Ambient temperature high and low temperature	—	40	—	80	—	80	
			—	90	—	120	—	120	
Line Regulation (mV)	$V_{IN} = 16 \text{ TO } 40 \text{ VDC}$ , $I_{O1}=I_{O2}=\text{full load}$		+ $V_{out}$	—	50	—	50	—	50
			- $V_{out}$	—	100	—	150	—	180
Load Regulation (mV)	$I_{O1}=I_{O2}=\text{No load to full load}$		+ $V_{out}$	—	50	—	50	—	50
			- $V_{out}$	—	100	—	150	—	180
Input Ripple current (mA)	BW=10 kHz to 10 MHz $I_{O1}=I_{O2}=\text{full load}$		—	50	—	50	—	50	
Efficiency (%)	$I_{O1}=I_{O2}=\text{full load}$	Ambient temperature high and low temperature	76	—	79	—	80	—	
			73	—	76	—	77	—	
Isolation (M )	Input to output or any pin to case (except case ground pin) at 500 VDC, $T_A = 25^{\circ}C$		100	—	100	—	100	—	
Inhibit Function	$T_A = 25^{\circ}C$ , Inhibit voltage, output disabled		yes		yes		yes		
Protection Function	$T_A = 25^{\circ}C$		yes		yes		yes		
Start-up Overshoot (mV pk)	$V_{in}=0$ to 28V, $I_{O1}=I_{O2}=\text{full load}$		-180	180	-120	120	-150	150	
Start-up Delay (ms)	$V_{in}=0$ to 28V, $I_{O1}=I_{O2}=\text{full load}$		—	5	—	5	—	5	
Capacitive Load ( $\mu F$ )	$T_A = 25^{\circ}C$ , No effect on DC performance		—	500	—	500	—	500	
Switching Frequency (kHz)	$I_O=\text{full load}$		400	600	400	600	400	600	
Step Load Response Transient (mV pK)	50% load to full load or full load to 50% load		-300	300	-300	300	-400	400	

**(to be continued) Table 4 Electrical Characteristics**

Step Load Response Recovery ( $\mu$ s)	50% load to full load or full load to 50% load,	—	200	—	200	—	200
Step Line Response Transient (mV pK)	$V_{in}=16\sim 40V$ , $I_{o1}=I_{o2}=\text{full load}$ $V_{in}=40\sim 16V$ , $I_{o1}=I_{o2}=\text{full load}$	-400	400	-400	400	-500	500
Step Line Response Recovery ( $\mu$ s)	$V_{in}=16\sim 40V$ , $I_{o1}=I_{o2}=\text{full load}$ $V_{in}=40\sim 16V$ , $I_{o1}=I_{o2}=\text{full load}$	—	300	—	300	—	300
Load Fault Short Circuit recovery (ms)	$I_{o1}=I_{o2}$ : short circuit to full load	—	5	—	5	—	5

**Notes to Specifications:**

The step load transition time should be greater than or equal to 10 $\mu$ s.

The step line transition time should be greater than or equal to 10 $\mu$ s.

Recovery time is measured from application of the transient to point at which  $V_{OUT}$  is within 1% of  $V_{OUT}$  at final value.

**HTR28T512(F), HTR28T515(F)****Table 5 Electrical Characteristics:** ( $T_{CASE} = -55^{\circ}C$  to  $+125^{\circ}C$ ,  $V_{IN} = 28V \pm 0.5V$ , Full Loads, Unless Otherwise Specified)

Triple output models			HTR28T512(F)		HTR28T515(F)	
Parameter	Conditions		Min	Max	Min	Max
Output Voltage (V)	$I_{o1}=I_{o2}=I_{o3}=\text{full load}$	Ambient temperature high and low temperature	4.95	5.05	4.95	5.05
		Ambient temperature high and low temperature	4.85	5.15	4.85	5.15
		Ambient temperature high and low temperature	11.82	12.18	14.77	15.23
		Ambient temperature high and low temperature	11.58	12.42	14.47	15.52
Output Current (A)	$V_{IN} = 16 \text{ TO } 40 \text{ VDC}$	+5 $V_{out}$	-12.18	-11.82	-15.23	-14.77
		+ $V_{out}$	-12.42	-11.58	-15.52	-14.47
		- $V_{out}$	0.1	4	0.1	4
Output Power (W)	—		—	30.84	—	30.84
Output Ripple Voltage(mV)	BW=10 kHz to 2 MHz $I_{o1}=I_{o2}=I_{o3}=\text{full load}$	+5 $V_{out}$	—	125	—	125
		+ $V_{out}$	-60	60	-60	60
		- $V_{out}$	-60	60	-60	60
Line Regulation (mV)	$V_{IN} = 16 \text{ TO } 40 \text{ VDC}$ , $I_{o1}=I_{o2}=I_{o3}=\text{full load}$	+5 $V_{out}$	—	20	—	20
		+ $V_{out}$	-75	75	-75	75
		- $V_{out}$	-75	75	-75	75
Load Regulation (mV)	$I_{o1}=I_{o2}=I_{o3}=\text{No load to full load}$	+5 $V_{out}$	—	50	—	50
		+ $V_{out}$	-75	75	-75	75
		- $V_{out}$	-75	75	-75	75
Input Ripple current (mA)	BW=10 kHz to 10 MHz $I_o=\text{full load}$	Ambient temperature	—	45	—	45
		high and low temperature	—	80	—	80
Efficiency (%)	$I_{o1}=I_{o2}=I_{o3}=\text{full load}$	Ambient temperature	72	—	73	—
		high and low temperature	70	—	71	—

**(to be continued)Table 5 Electrical Characteristics**

Isolation (M )	Input to output or any pin to case (except pin 6,7) at 500 VDC, $T_A = 25^\circ\text{C}$	100	—	100	—	
Inhibit Function	$T_A = 25^\circ\text{C}$ , Inhibit voltage, output disabled	yes		yes		
Protection Function	$T_A = 25^\circ\text{C}$	yes		yes		
Start-up Overshoot (mV pk)	$V_{in}=0$ to 28V, $I_o = \text{full load}$	+5 $V_{out}$	—	500	—	500
		+ $V_{out}$	—	1500	—	1500
		- $V_{out}$	—	1500	—	1500
Start-up Delay(ms)	$V_{in}=0$ to 28V, $I_o = \text{full load}$	—	6	—	6	
Capacitive Load( $\mu\text{F}$ )	$T_A = 25^\circ\text{C}$ , No effect on DC performance	—	—	—	—	
Switching Frequency(kHz)	$I_o = \text{full load}$	400	600	400	600	
Step Load Response Transient (mV pK)	50% load to full load or full load to 50% load,	+5 $V_{out}$	-250	250	-250	250
		+ $V_{out}$	-800	800	-800	800
		- $V_{out}$	-800	800	-800	800
Step Load Response Recovery (mV pK)	50% load to full load or full load to 50% load,	+5 $V_{out}$	—	100	—	100
		+ $V_{out}$	—	4000	—	4000
		- $V_{out}$	—	4000	—	4000
Step line Response Transient (mV pK)	$V_{in}=16\sim 40\text{V}$ , $I_o = \text{full load}$ $V_{in}=40\sim 16\text{V}$ , $I_o = \text{full load}$	-800	800	-800	800	
Step Line Response Recovery ( $\mu\text{s}$ )	$V_{in}=16\sim 40\text{V}$ , $I_o = \text{full load}$ $V_{in}=40\sim 16\text{V}$ , $I_o = \text{full load}$	—	5000	—	5000	
Load Fault recovery (ms)	$I_o$ :short circuit to full load	—	6	—	6	

**Notes to Specifications:**

The step load transition time should be greater than or equal to 10 $\mu\text{s}$ .

The step line transition time should be greater than or equal to 10 $\mu\text{s}$ .

Recovery time is measured from application of the transient to point at which  $V_{OUT}$  is within 1% of  $V_{OUT}$  at final value.

## HTR28T515-A , HTR28T515-B

**Table 6 Electrical Characteristics:** ( $T_{CASE} = -55^{\circ}C$  to  $+125^{\circ}C$ ,  $V_{IN} = 28V \pm 0.5V$ , Full Load<sub>s</sub>, Unless Otherwise Specified)

Triple output models		HTR28T515-A		HTR28T515-B		
Parameter	Conditions	Min	Max	Min	Max	
Output Voltage (V)	$Io_1= Io_2= Io_3 =full$ load	Ambient temperature high and low temperature	4.90 4.80	5.10 5.20	4.90 4.80	5.10 5.20
		Ambient temperature high and low temperature	-15.15 -15.40	-14.85 -14.60	-15.15 -15.40	-14.85 -14.60
		Ambient temperature high and low temperature	14.85 14.60	15.15 15.40	14.85 14.60	15.15 15.40
Output Current (A)	$V_{IN} = 16$ TO 40 VDC	+5V <sub>out</sub>	3	—	3	—
		+V <sub>out</sub>	0.5	—	0.5	—
		-V <sub>out</sub>	0.5	—	0.5	—
Output Power(W)	—	—	30.84	—	30.84	
Output Ripple Voltage (mV)	BW=10 kHz to 2 MHz $Io_1= Io_2= Io_3 = full$ load	+5V <sub>out</sub>	—	50	—	80
		+V <sub>out</sub>	—	100	—	100
		-V <sub>out</sub>	—	100	—	100
Line Regulation (mV)	$V_{IN} = 16$ TO 40 VDC, $Io_1= Io_2= Io_3 = full$ load	+5V <sub>out</sub>	—	50	—	50
		+V <sub>out</sub>	—	80	—	80
		-V <sub>out</sub>	—	80	—	80
Load Regulation (mV)	$Io_1= Io_2= Io_3 =No$ load to full load	+5V <sub>out</sub>	—	50	—	50
		+V <sub>out</sub>	—	80	—	100
		-V <sub>out</sub>	—	80	—	100
Input Ripple current (mA)	BW=10 kHz to 10 MHz $Io= full$ load	—	—	—	—	
Efficiency (%)	$Io_1= Io_2= Io_3=full$ load	75	—	75	—	
Isolation (M )	Input to output or any pin to case (except pin 6,7) at 500 VDC, $T_A = 25^{\circ}C$	100	—	100	—	
Inhibit Function	$T_A = 25^{\circ}C$ , Inhibit voltage, output disabled	yes		yes		
Protection Function	$T_A = 25^{\circ}C$	—		—		
Start-up Overshoot (mV pK)	$V_{in}=0$ to 28V, $Io =full$ load	+5V <sub>out</sub>	—	—	—	—
		+V <sub>out</sub>	—	—	—	—
		-V <sub>out</sub>	—	—	—	—
Start-up Delay(ms)	$V_{in}=0$ to 28V, $Io =full$ load	—	—	—	—	
Capacitive Load( $\mu F$ )	$T_A = 25^{\circ}C$ , No effect on DC performance	—	—	—	—	
Switching Frequency(kHz)	$Io=full$ load	—	—	—	—	
Step Load Response Transient (mV pK)	50% load to full load or full load to 50% load,	+5V <sub>out</sub>	—	—	—	—
		+V <sub>out</sub>	—	—	—	—
		-V <sub>out</sub>	—	—	—	—
Step Load Response Recovery (mV pK)	50% load to full load or full load to 50% load,	+5V <sub>out</sub>	—	—	—	—
		+V <sub>out</sub>	—	—	—	—
		-V <sub>out</sub>	—	—	—	—
Step line Response Transient (mV pK)	$V_{in}=16\sim 40V$ , $Io =full$ load	—	—	—	—	
	$V_{in}=40\sim 16V$ , $Io =full$ load	—	—	—	—	
Step Line Response Recovery ( $\mu s$ )	$V_{in}=16\sim 40V$ , $Io =full$ load	—	—	—	—	
	$V_{in}=40\sim 16V$ , $Io =full$ load	—	—	—	—	
Load Fault recovery (ms)	$Io$ :short circuit to full load	—	—	—	—	

### Notes to Specifications:

The step load transition time should be greater than or equal to 10 $\mu s$ .

The step line transition time should be greater than or equal to 10 $\mu s$ .

Recovery time is measured from application of the transient to point at which  $V_{OUT}$  is within 1% of  $V_{OUT}$  at final value.

## 4 TYPICAL PERFORMANCE CURVES

### (1) Single output (Model HTR28S15)

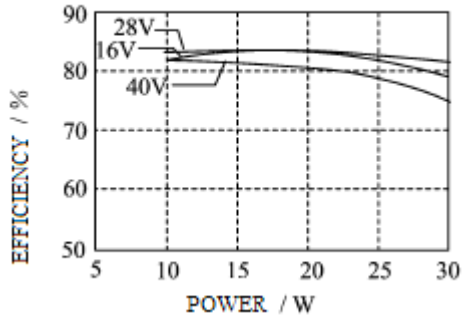


Figure 2 Efficiency (Output Power)

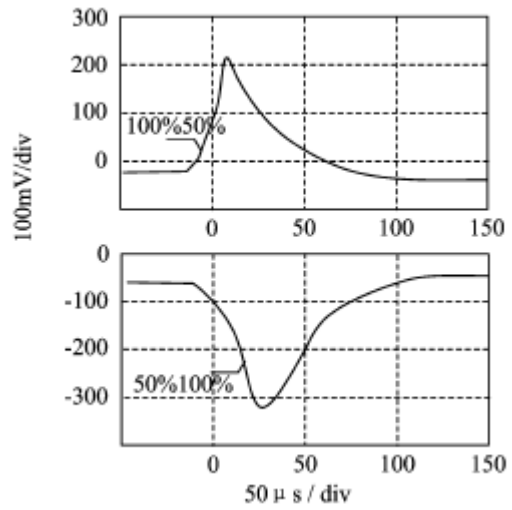


Figure 3 Step Load Response

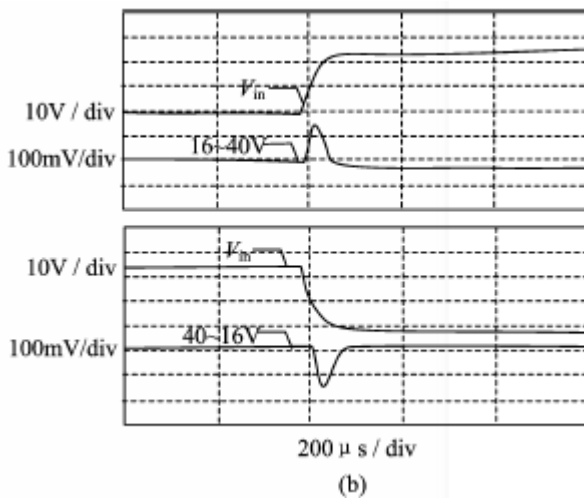


Figure 4 Step Line response

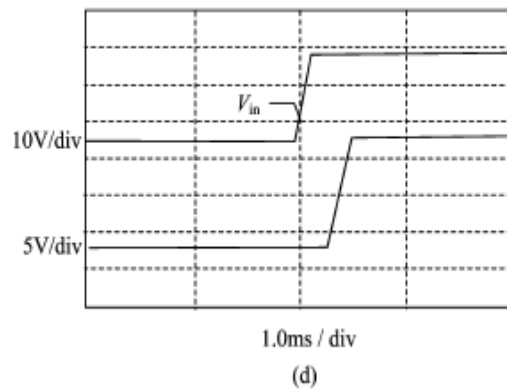


Figure 5 Start-up Overshoot/ Start-up Delay

### (2) Dual Output (Model HTR28D15)

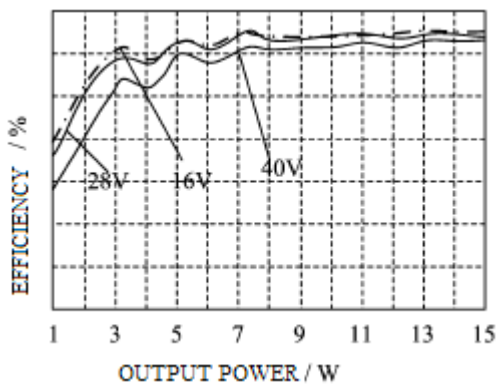


Figure 6 Efficiency (Output Power)

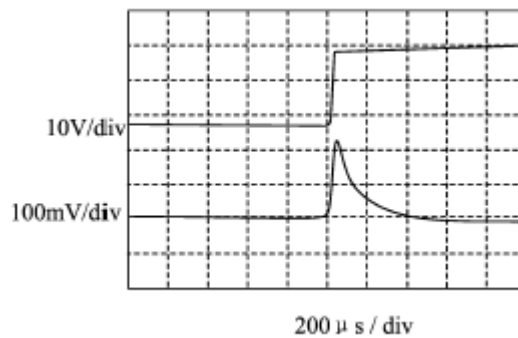


Figure 7 Step Line response



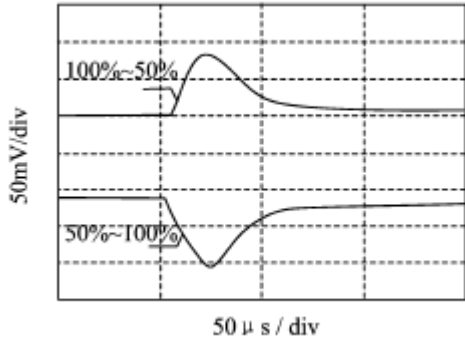


Figure 8 Step Load Response

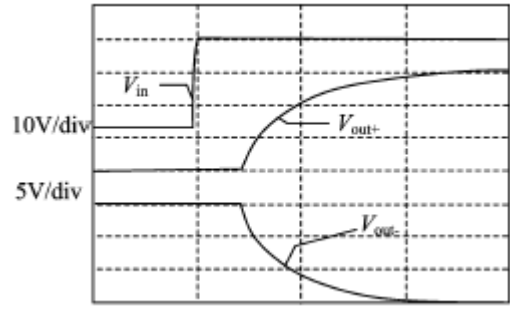


Figure9 Start-up Overshoot/ Start-up Delay

**(3) Triple output (Model HTR28T515)**

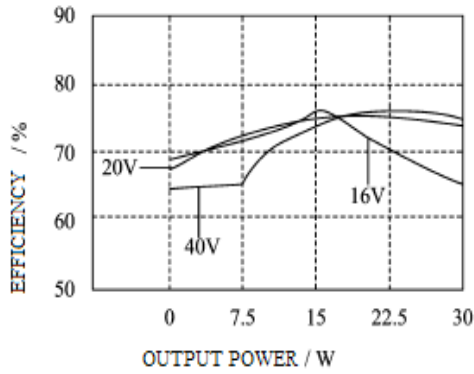


Figure 10 Efficiency (Output Power)

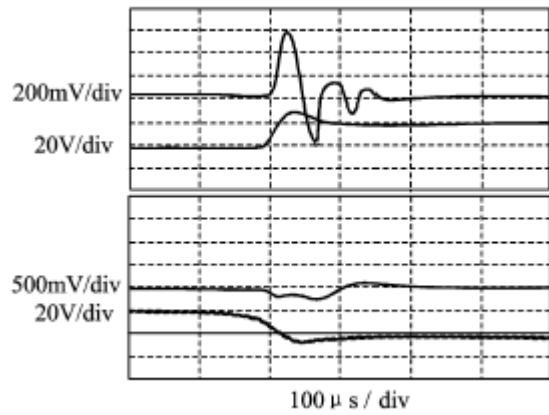


Figure 11 Step Line response

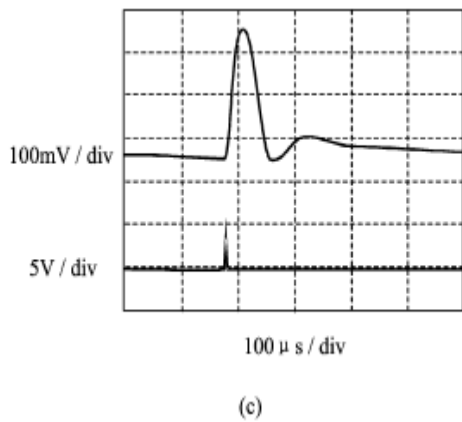


Figure12 Step Load Response

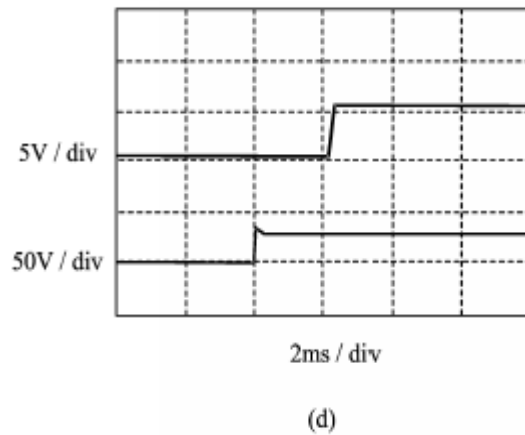


Figure13 Start-up Overshoot/ Start-up Delay

## 5 TYPICAL MTBF CURVES

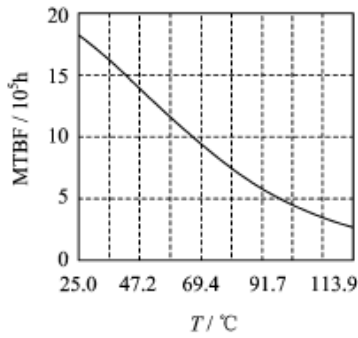


Figure 14 Model HTR28S12

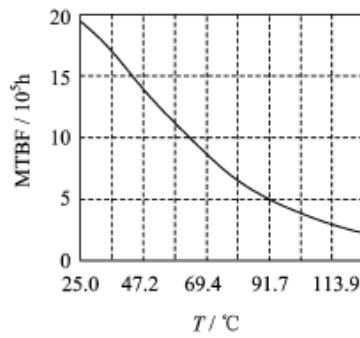


Figure 15 Model HTR28D12

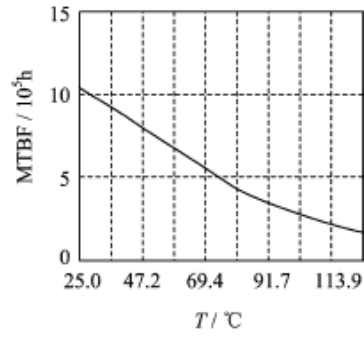


Figure 16 Model HTR28T515-A

## 6 TYPICAL CONNECTION DIAGRAM

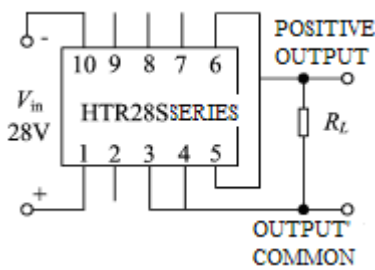


Figure 17 Application Connection Diagram for Single output models

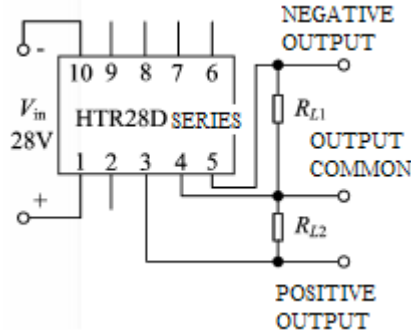


Figure 18 Application Connection Diagram for Dual output models

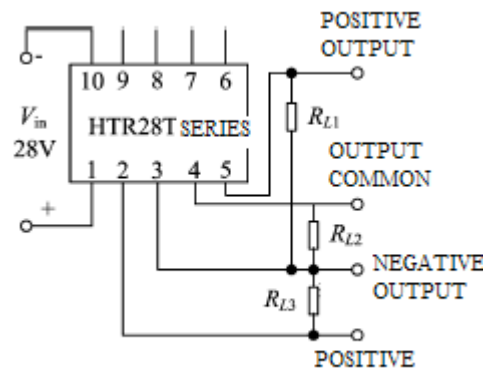


Figure 19 Application Connection Diagram for Triple output models

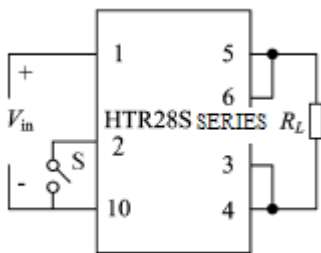


Figure 20 Inhibit Drive connection Diagram for Single Output Models

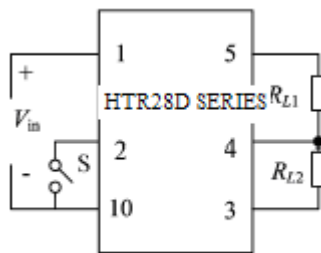


Figure 21 Inhibit Drive connection Diagram for Dual Output Models

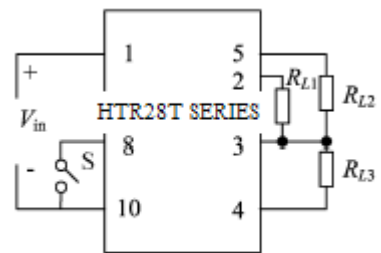


Figure 22 Inhibit Drive connection Diagram for Triple Output Models

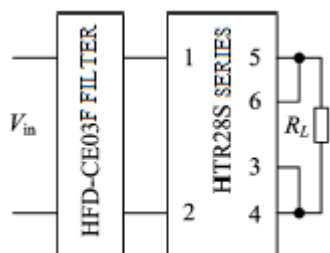


Figure 23 Single Output Converter with EMI Filter connection Diagram

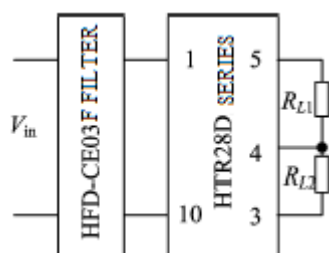


Figure 24 Dual Output Converter with EMI Filter connection Diagram

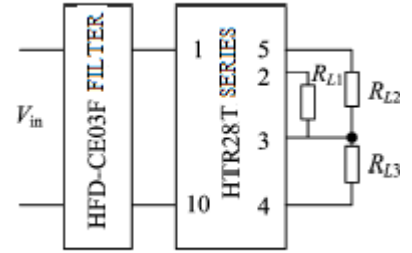


Figure 25 Triple Output Converter with EMI Filter connection Diagram

## 7 PACKAGE SPECIFICATIONS (Units:mm)

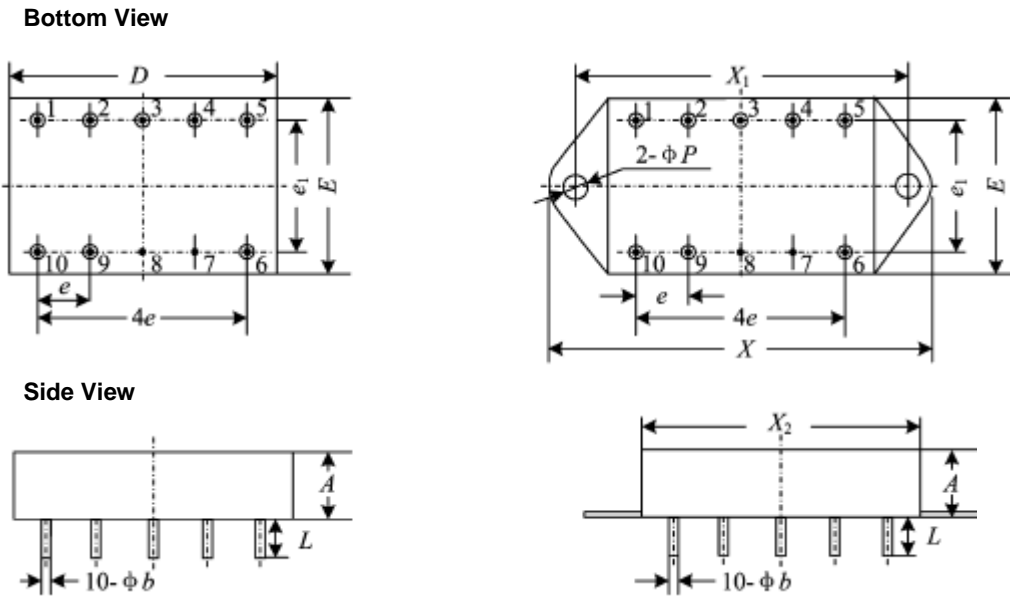


Figure 26 Single Output Model

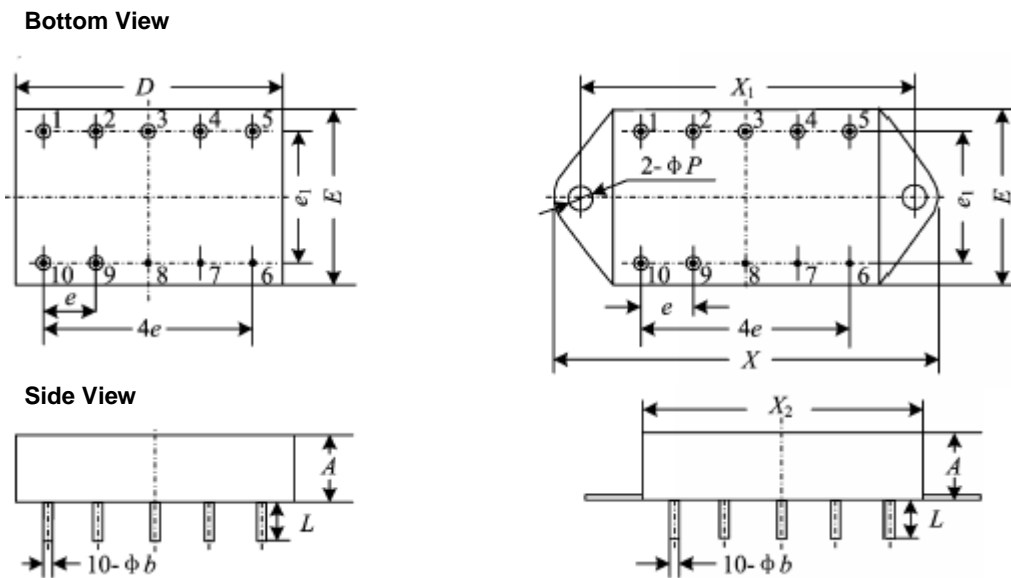
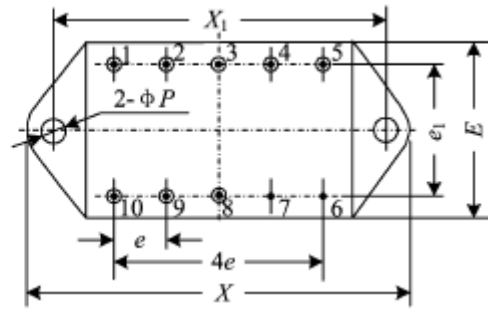
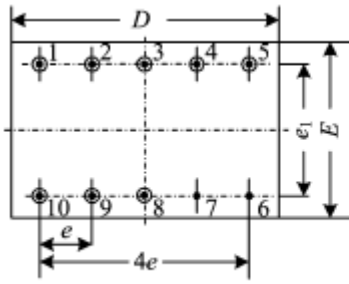


Figure 27 Dual Output Model

Bottom View



Side View

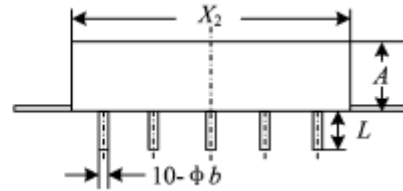
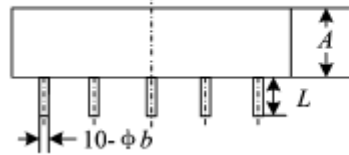


Figure 28 Triple Output Model

Table 7 Package Outline

Units:mm			
Dimension Symbols	Minimum	Nominal	Maximum
A	—	—	10.16
$\phi b$	0.89	—	1.14
D	—	—	53.98
e	—	10.16	—
$e_1$	—	20.32	—
E	—	—	28.58
L	6.09	—	6.60
$\phi P$	3.99	—	4.19
X	—	—	73.91
$X_1$	64.52	—	65.02
$X_2$	—	—	53.98

Table 8 Package Outline OF Model HTR28T512(F), Model HTR28T515(F)

Units:mm			
Dimension Symbols	Minimum	Nominal	Maximum
A	—	—	10.29
$\phi b$	0.89	—	1.14
D	—	—	49.53
e	—	10.16	—
$e_1$	—	25.40	—
E	—	—	34.29
L	6.10	—	6.60
$\phi P$	3.99	—	4.19
X	—	—	69.09
$X_1$	59.70	—	60.20
$X_2$	—	—	49.58

**Table 9 Package Outline OF Model HTR28T515-A, Model HTR28T515-B**

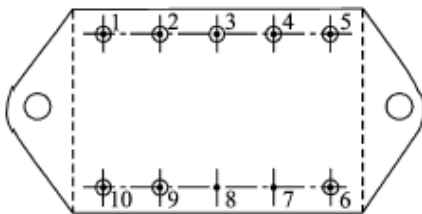
Units:mm			
Dimension Symbols	Minimum	Nominal	Maximum
A	—	—	10.90
$\phi b$	0.80	—	1.20
e	—	10.16	—
$e_1$	—	25.40	—
E	—	—	34.90
L	5.40	—	—
$\phi P$	3.70	—	4.70
X	—	—	69.50
$X_1$	59.64	—	60.24

**Table 10 Case Materials**

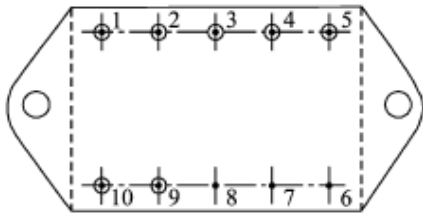
Case Model	Header	Header Plating	Cover	Cover Plating	Pin	Pin Plating	Sealing Style	Notes
UPP5429-10 (Single&Dual)	Cold Rolled Steel(10#)	Nickel	Iron/Nickel Alloy(4J42)	Nickel	Copper Compound	Nickel/Gold	Compression Seal	Nickel plating is for case ground pin.
UPP5034-10c (Triple)	Cold Rolled Steel(10#)	Nickel	Iron/Nickel Alloy(4J42)	Nickel	Iron/Nickel Alloy(4J50)	Nickel/Gold	Compression Seal	Nickel plating is for case ground pin.

Notes:Solder pins individually with heat application not exceeding 300 ° C for 10 seconds per pin.

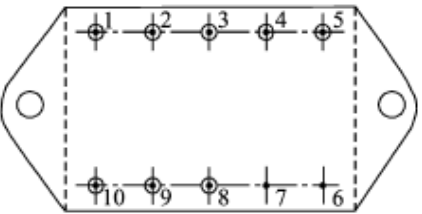
**8 PIN DESIGNATION**



**Figure29 Pin Out Bottom View (Single)**



**Figure29 Pin Out Bottom View (Dual)**



**Figure29 Pin Out Bottom View (Triple)**

**Table 11 Pin designation**

Pin	Single Output	Dual Output	Triple Output
1	Positive Input	Positive Input	Positive Input
2	Inhibit	Inhibit	Positive Output (5V)
3	Negative Inductive	Positive Output	Output Common
4	Output Common	Output Common	Negative Output
5	Positive Output	Negative Output	Positive Output
6	Positive Inductive	Case Ground	Case Ground
7	Case Ground	Case Ground	Case Ground
8	Case Ground	Case Ground	Inhibit
9	Synchronous	Synchronous	Synchronous
10	Input Common	Input Common	Input Common

## 9 ORDERING INFORMATION

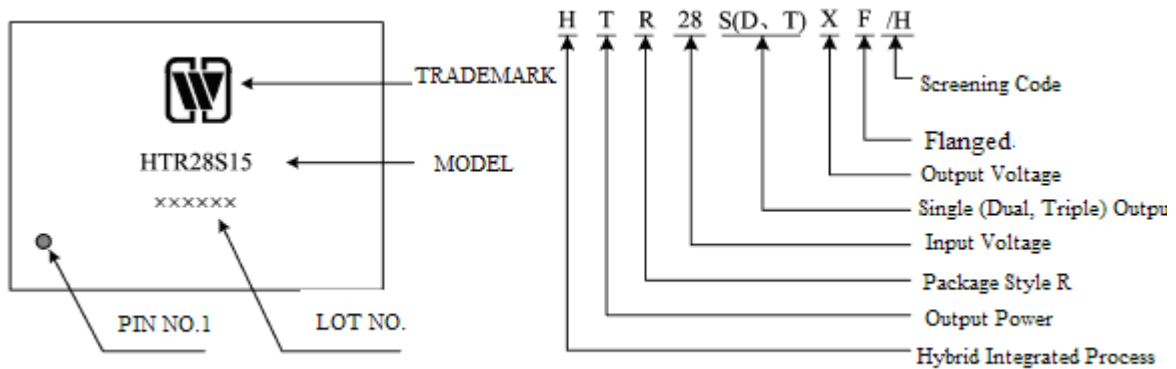


Figure32 Part Numbering Key

### Application Notes:

- The correct power supply is to be ensured that may not cause permanent damage to the device.
- When the electrical performance is tested, the testing position should be pin of the device.
- When the device is mounted, the bottom of the device should be closely attached to the circuit board. So as to avoid the damage of the pins, the shockproof should be increased when it is required
- The pin should not be bending to avoid the glass insulator breaking and case leakage.
- When the case temperature is at 105 , it is suggested that thickness of the thermal sinking plate(copper material) is 3mm, the dimension is greater than 100mm × 100mm.
- When the case temperature is at 125 , it is suggested that thickness of the thermal sinking plate is 3mm, the dimension is greater than 120mm × 80mm.

To request a quotation or place orders ,please contact our sales representative or the ECRIM SalesDepartment at:

**Sales Phone:** (086) 0551-3667943      **Sales Fax:** (086) 0551-3638101

**E-mail:** sales@ecrim.cn