

HTT1213E Silicon NPN Epitaxial Twin Transistor

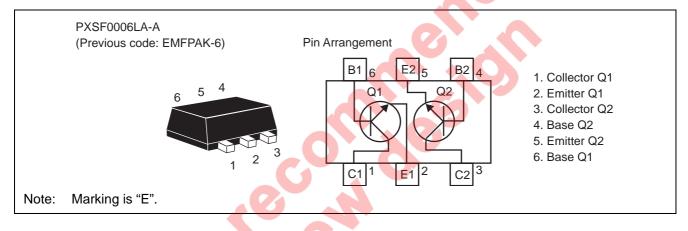
REJ03G0526-0100 (Previous ADE-208-1449(Z)) Rev.1.00 Feb.07.2005

Features

• Include 2 transistors in a small size SMD package: EMFPAK-6 (6 Leads: 1.2 x 0.8 x 0.5 mm)

Q1:	Q2:
Equivalent	Equivalent
Buffer Transistor	OSC Transistor
2SC5700	2SC5700

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

		Ratings		
Item	Symbol	Q1 and Q2	Unit	
Collector to base voltage	V _{CBO}	15	V	
Collector to emitter voltage	V _{CEO}	4	V	
Emitter to base voltage	V _{EBO}	1.5	V	
Collector current	Ι _C	50	mA	
Collector power dissipation	Pc	Total 200*	mW	
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

*Value on PCB. (FR-4(13 x 13 x 0.635 mm))



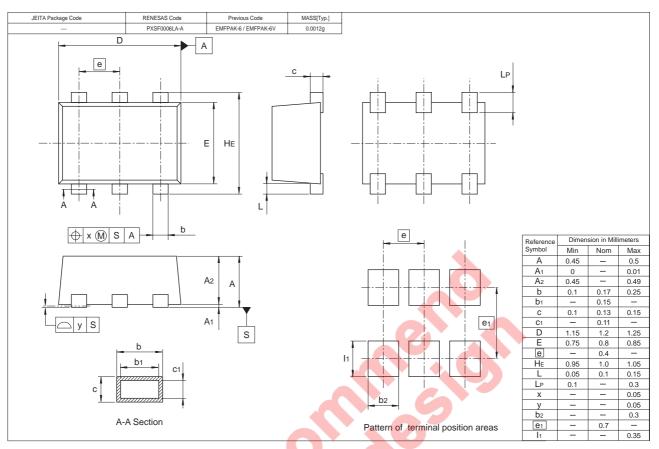
Electrical Characteristics (Q1 and Q2)

 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test conditions
Collector to base breakdown	V _{(BR)CBO}	15	—		V	$I_{\rm C} = 10 \ \mu A, \ I_{\rm E} = 0$
voltage						
Collector cutoff current	I _{CBO}	_	_	0.1	μA	$V_{CB} = 15 \text{ V}, \text{ I}_{E} = 0$
Collector cutoff current	I _{CEO}	_	_	1	μΑ	$V_{CE} = 4 \text{ V}, \text{ R}_{BE} = \text{infinite}$
Emitter cutoff current	I _{EBO}	_	_	0.1	μΑ	$V_{EB} = 0.8 \text{ V}, I_{C} = 0$
DC current transfer ratio	h _{FE}	100	130	150		$V_{CE} = 1 \text{ V}, I_{C} = 5 \text{ mA}$
Reverse transfer capacitance	C _{re}	_	0.30	0.45	pF	V _{CB} = 1 V, f = 1 MHz
						Emitter ground
Gain bandwidth product	f⊤	10	12		GHz	$V_{CE} = 1 \text{ V}, I_{C} = 5 \text{ mA}, f = 1 \text{ GHz}$
Forward transfer coefficient	$ S_{21} ^2$	13	16		dB	$V_{CE} = 1 \text{ V}, I_{C} = 5 \text{ mA},$
Noise figure	NF		1.0	1.7	dB	f = 900 MHz, $\Gamma_{S} = \Gamma_{L} = 50 \ \Omega$



Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
HTT1213EETL	5000	0178Taping

Note: Therefore especially small contact area of terminal, miss contact may occur if inadequate soldering condition is applied.

Contact Renesas sales office for any question regarding recommended soldering condition of Renesas.



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